

User Manual

BL50W – Rugged Box PC for Wireless Applications



Configuration example



BL50W – Rugged Box PC for Wireless Applications

The BL50W is a fanless, maintenance-free box computer that has been designed for independent use or as display computer electronics for embedded wireless applications in transportation, e.g. in trains, commercial vehicles, mobile machines or airplanes.

Four PCI Express® Mini Card slots each with dual SIM make it possible to flexibly implement the whole range of wireless interfaces such as mobile service standards GSM (2G), UMTS (3G), LTE (4G) and derivatives and wireless communication standards WLAN / Wi-Fi IEEE 802.11 and derivatives. A GNSS interface supporting positioning systems GPS and GLONASS complements the possibilities.

The rugged BL50W is powered by a dual-core AMD Embedded G-Series APU (Accelerated Processing Unit), the T48N, running at 1.4 GHz. The G-Series combines low-power CPUs and advanced GPUs, in this case an AMD Radeon™ HD 6310, into a single embedded device. The use of the Embedded G-Series makes for high scalability in CPU (single/dual core) and graphics performance (various Radeon™ GPUs or none at all).

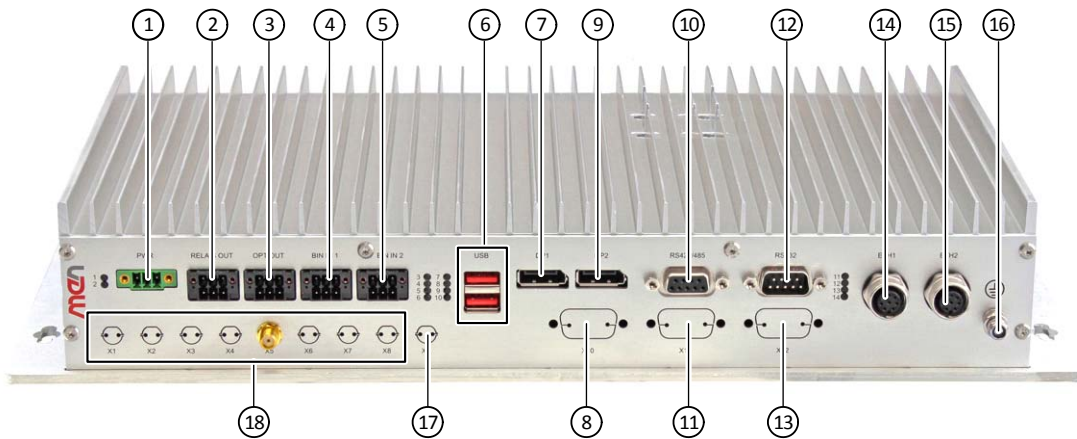
The BL50W is equipped with 2 GB of DDR3 SDRAM and offers SD card and mSATA slots. A SATA hard-disk/solid-state drive can be installed within the housing as an option. The system is designed for fanless operation at temperatures from -40 to +70°C (+85°C for up to 10 minutes), its special aluminum housing with cooling fins serves as a heatsink for the internal electronics and in this way provides conduction cooling.

The BL50W supports up to two DisplayPort® interfaces with full HD resolution. In addition, a multitude of other I/O is available at the front panel, including two Gigabit Ethernet, two USB 2.0, variable slots for legacy serial I/O (e.g. RS232) or CAN bus, general purpose inputs and relay outputs.

The BL50W comes with its own integrated 30W 24 VDC nom. (10 to 50.4 V) class S2 wide-range power supply and is in compliance with EN 50155 and ISO 7637-2 (E-mark for automotive). The power can be switched on and off using an ignition signal on the power connector, and a run-down time after switching off the power can be adjusted by software.

The combination of the various CPU/GPU options with the available selection of external interfaces (realized via separate graphics and I/O interface boards within the system) makes for an extremely flexible system design that can quickly be tailored to a vast number of applications.

Diagram



- | | |
|--|---|
| ① PSU (10V-50.4V) | ⑩ RS422/485 interface |
| ② 2 relay outputs | ⑪ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ③ 2 photocoupler outputs | ⑫ RS232 interface |
| ④ 6 binary inputs | ⑬ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ⑤ 1 odometer input, 1 IBIS slave, 1 binary input | ⑭ Gigabit Ethernet 1 |
| ⑥ 2 USB 2.0 interfaces | ⑮ Gigabit Ethernet 2 |
| ⑦ DisplayPort 1 | ⑯ Earthing stud |
| ⑧ SA-Adapter connector for RS232, RS422/485, IBIS or CAN | ⑰ Antenna connector for GNSS |
| ⑨ DisplayPort 2 | ⑱ Antenna connectors for PCI Express Mini Cards |

Technical Data

CPU

- AMD Embedded G-Series T48N
 - Dual-Core
 - 1.4 GHz processor core frequency
 - Accelerated Processing Unit (APU), also includes GPU (see Graphics)

Controller Hub

- AMD A55E

Memory

- 64 KB L1 and 512 KB L2 cache
- 2 GB DDR3 SDRAM system memory
 - Soldered
 - 1066 MT/s

Mass Storage

- One SD card slot
 - Via USB 2.0
- One mSATA slot
 - SATA Revision 2.x support
 - Transfer rates up to 300 MB/s (3 Gbit/s)
- Serial ATA (SATA)
 - One port for 2.5" hard-disk/solid-state drive mounted within the unit's housing
 - SATA Revision 2.x support
 - Transfer rates up to 300 MB/s (3 Gbit/s)

Graphics

- AMD Radeon™ HD 6310
 - Dual independent display support
 - Dual DisplayPort
 - Maximum resolution: 2560x1600 each port
 - Embedded in T48N APU
- 3D Graphics Acceleration
 - Full DirectX® 11 support, including full speed 32-bit floating point per component operations
 - Shader Model 5
 - OpenCL™ 1.1 support
 - OpenGL® 4.0 support

- Motion Video Acceleration
 - Dedicated hardware (UVD 3) for H.264, VC-1 and MPEG2 decoding
 - HD HQV and SD HQV support: noise removal, detail enhancement, color enhancement, cadence detection, sharpness, and advanced de-interlacing
 - Super up-conversion for SD to HD resolutions

Front I/O

- 2 DisplayPort® 1.1a interfaces
 - AUX channels and hot plug detection
- 2 Gigabit Ethernet
 - Via M12 connectors
 - Electrically isolated
- 2 USB 2.0
 - Via Series A connector
- 7 general purpose inputs
 - Input voltage range from 0 V up to 154 V independent of the power supply input voltage
 - Input signal frequency max. 10 Hz
- 2 relay outputs
 - Max. switching current 0..30 V: 2 A
 - Max. switching current 30..72 V: 0.9 A
 - Max. switching current 72..154 V: 0.3 A
 - Max. switching voltage: 154 V
 - Max. switching frequency: 1 Hz
 - Minimum life time @ 1A, 30V, 20 cpm: 100.000
 - Electrically isolated
- 2 photocouplers (shutters)
 - Max. switching voltage: 154 V
 - Max. current: 120 mA (switching and continuous)
- 1 odometer input
 - For counting odometer pulses of a maximum frequency of 2 kHz
- 1 IBIS slave interface
 - Baud rate up to 19.2 kBaud
 - Electrically isolated
- GNSS interface
 - Frequency band: GPS (L1), Glonass (L1, FDMA), Galileo (E1)
 - Standards: NMEA, RTCM 104
 - 32-channel GNSS architecture
 - Accuracy: 1.5 m
 - A-GPS
 - Time-To-First-Fix – cold start: lower than 35 s
 - Time-To-First-Fix – warm start / aided start: 1s
 - Odometer input for GNSS receiver

- RS232
 - D-Sub connector at front panel
 - Data rates up to 115 200 bit/s
 - 60-byte transmit/receive buffer
 - Handshake lines: RTS, CTS
 - Electrically isolated
- RS422/485
 - D-Sub connector at front panel
 - Full or half duplex
 - Electrically isolated
- 2 SA-Adapter slots for legacy serial I/O
 - For RS232, RS422/485 or IBIS master
- 1 SA-Adapter slot for RS232, RS422/485 or CAN
- 14 status LEDs
 - 4 for Ethernet link and activity status
 - 2 for general board status
 - 8 user LEDs

4 PCI Express® Mini Card slots

- For functions such as
 - Mobile service standards: GSM (2G), UMTS (3G), LTE (4G) and derivatives
 - Wireless communication: WLAN / WiFi IEEE 802.11 and derivatives
 - Real-Time Ethernet functionality via Hilscher PCI Express® Mini cards
- 2 microSIM card slots for each PCI Express® Mini Card
- PCI Express® and USB interface

Real-Time Clock

- Buffered by supercapacitor for 12 h

Electrical Specifications

- Supply voltage:
 - 24V and 36V nominal input voltage according to EN50155
 - 24V nominal input voltage according to ISO 7637-2 (E-mark) requirements
 - 10 to 50.4 V input voltage range
 - EN 50155 power interruption class S2
- Power consumption: 14.4 W with T48N CPU with Windows® 7 operating system and 1 Gb Ethernet connection

Mechanical Specifications

- Dimensions: Height 66 mm x Width 390 mm x Length 215 mm
- Weight: approx. 3 kg

Environmental Specifications

- Temperature range (operation):
 - -40°C to 70°C (screened), with up to 85°C for 10 minutes according to class Tx (EN 50155)
 - Fanless operation
- Temperature range (storage): -40..+85°C
- Relative humidity (operation): max. 95% non-condensing
- Relative humidity (storage): max. 95% non-condensing
- Altitude: -300 m to +3,000 m
- Shock: 50 m/s², 30 ms (EN 61373)
- Vibration (function): 1 m/s², 5 Hz – 150 Hz (EN 61373)
- Vibration (lifetime): 7.9 m/s², 5 Hz – 150 Hz (EN 61373)
- Conformal coating of internal components
- International Protection Rating: IP20

MTBF

- 198 993 h @ 40°C according to IEC/TR 62380 (RDF 2000)

Safety

- Flammability
 - UL 94V-0
- Fire Protection
 - EN 45545-2 (Railway)
 - ECE-R118 (Automotive)
- Electrical Safety
 - EN 50153
 - EN 50155

EMC Conformity (Automotive)

- ECE R10 (E-mark)
- ISO 10605 (ESD)

EMC Conformity (Railway)

- EN 50121-3-2

BIOS

- InsydeH2O™ UEFI Framework

Software Support

- Windows® 7
- Windows® Embedded Standard 7
- Linux



For more information on supported operating system versions and drivers, please see the [online data sheet](#).

Configuration Options

APU

- AMD T48N, 1.4 GHz Dual Core, 18W, AMD Radeon™ HD 6310
- AMD T56N, 1.65 GHz Dual Core, 18W, AMD Radeon™ HD 6320 (on request)
- AMD T56E, 1.65 GHz Dual Core, 18W, AMD Radeon™ HD 6250 (on request)
- AMD T48E, 1.4 GHz Dual Core, 18W, AMD Radeon™ HD 6250 (on request)
- AMD T40N, 1.0 GHz Dual Core, 9W, AMD Radeon™ HD 6290 (on request)
- AMD T40E, 1.0 GHz Dual Core, 6.4W, AMD Radeon™ HD 6250 (on request)
- AMD T52R, 1.5 GHz Single Core, 18W, AMD Radeon™ HD 6310 (on request)
- AMD T44R, 1.2 GHz Single Core, 9W, AMD Radeon™ HD 6250 (on request)
- AMD T40R, 1.0 GHz Single Core, 5.5W, AMD Radeon™ HD 6250 (on request)
- AMD T16R, 615 MHz Single Core, 4.5W, AMD Radeon™ HD 6250 (on request)
- AMD T48L, 1.4 GHz Dual Core, 18W (on request)
- AMD T30L, 1.4 GHz Single Core, 18W (on request)
- AMD T24L, 1000 MHz Single Core, 5W (on request)

Memory

- 2 GB DDR3 SDRAM system memory
- 4 GB DDR3 SDRAM system memory (on request)
- SATA hard-disk/solid state drive (mounted within housing)

Graphics

- Maximum resolution depending on GPU
 - 2560x1600 (all DisplayPort® interfaces) with Radeon™ HD 6310 and 6320
 - 1920x1200 (all DisplayPort® interfaces) with Radeon™ HD 6250 and 6290 (on request)

I/O

- Ethernet
 - Two Fast Ethernet interfaces on two M12 connectors (on request)
- 1 HD audio (on request)
 - HD audio codec
 - Audio stereo in
 - Audio stereo out
 - SPDIF out
 - Available via 9-pin D-Sub connector instead of one SA-Adapter
- Antenna connectors
 - For functions like Wi-Fi, WIMAX, GSM/GPRS, UMTS, LTE in combination with PCI Express® Mini Card(s)
 - Reverse SMA connector
- SA-Adapter
 - Two (when audio is used) or three slots for RS232, RS422/485, IBIS master or CAN bus

Fieldbusses

- Additional Hilscher PCI Express® Mini Cards, which allow further communication possibilities (as listed below), are available with this box PC, after minor modifications. Please contact our sales team for further information:
- PX51, supporting the following communication (determined by firmware):
 - DeviceNet Master
 - DeviceNet Slave

Miscellaneous

- Real-time clock
 - 12 h buffer time
 - 72 h buffer time (on request)

Electrical Specifications

- Input voltages of 48V, 72V/110V (on request)
 - According to EN 50155 class S2

Other Options

- The product concept is very flexible, there are many other configuration possibilities.
- Please contact our sales team if you do not find your required function in the options.
- Some of these options may only be available for large volumes.



For available standard configurations see the [online data sheet](#).

Product Safety

Electrostatic Discharge (ESD)



Computer boards and components contain electrostatic sensitive devices. Electrostatic discharge (ESD) can damage components. To protect the board and other components against damage from static electricity, you should follow some precautions whenever you work on your computer.

- Power down and unplug your computer system when working on the inside.
- Hold components by the edges and try not to touch the IC chips, leads, or circuitry.
- Use a grounded wrist strap before handling computer components.
- Place components on a grounded antistatic pad or on the bag that came with the component whenever the components are separated from the system.
- Only store the board in its original ESD-protected packaging. Retain the original packaging in case you need to return the board to MEN for repair.

About this Document

This user manual is intended only for system developers and integrators, it is not intended for end users.

It describes the hardware functions of the system and connection of peripheral devices. It also provides additional information for special applications and configurations of the system.

The manual does not include detailed information on individual components (data sheets etc.). A list of literature is given in the appendix.

History

Issue	Comments	Date
E1	First issue	2014-01-20
E2	Corrected Technical Data : SATA transfer rate, GPI input signal frequency, conformal coating Corrected Figure 6, Block diagram, on page 23 Corrected tables 22 to 25 in Chapter 3.13 6-Pin Headers on page 61 Added Chapter 6.2 Fuse Protection on page 78 Cosmetics	2014-04-17
E3	Changed IP protection class in Technical Data , added voltage rating in Chapter 6.2 Fuse Protection on page 78 , added note regarding external disconnect device in Chapter 2.5 Electrical Connection on page 38 , extended Chapter 3.1.1 Ignition on page 41 , added Chapter 2.3.1 Installing the BL50W in a 19" Rack on page 36 , cosmetics	2014-12-09
E4	Corrected connector drawing for half and full-duplex interface, clarified IBIS, UART and CAN switching possibilities in MDIS chapter, added note that GND always has to be connected on OPT_OUT connector, clarifications and cosmetics	2015-07-27
E5	Correct IP protection class in Technical Data	2015-09-02

Conventions



Indicates important information or warnings concerning the use of voltages that could lead to a hazardous situation which could result in personal injury, or damage or destruction of the component.



Indicates important information or warnings concerning proper functionality of the product described in this document.



The globe icon indicates a [hyperlink](#) that links directly to the Internet, where the latest updated information is available.
When no globe icon is present, the [hyperlink](#) links to specific elements and information within this document.

<i>italics</i>	Folder, file and function names are printed in <i>italics</i> .
bold	Bold type is used for emphasis.
mono	A monospaced font type is used for hexadecimal numbers, listings, C function descriptions or wherever appropriate. Hexadecimal numbers are preceded by "0x".
<i>comment</i>	Comments embedded into coding examples are shown in <i>green text</i> .
IRQ# /IRQ	Signal names followed by a hashtag "#" or preceded by a forward slash "/" indicate that this signal is either active low or that it becomes active at a falling edge.
in/out	Signal directions in signal mnemonics tables generally refer to the corresponding board or component, "in" meaning "to the board or component", "out" meaning "from the board or component".

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WEEE Application

The WEEE directive does not apply to fixed industrial plants and tools. The compliance is the responsibility of the company which puts the product on the market, as defined in the directive; components and sub-assemblies are not subject to product compliance.

In other words: Since MEN does not deliver ready-made products to end users, the WEEE directive is not applicable for MEN. Users are nevertheless recommended to properly recycle all electronic boards which have passed their life cycle.

Nevertheless, MEN is registered as a manufacturer in Germany. The registration number can be provided on request.

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1 Product Description

1.1 Overview

This chapter gives an overview of the box PC.

Figure 1. The BL50W - overview (revision 01.03.xx and higher)



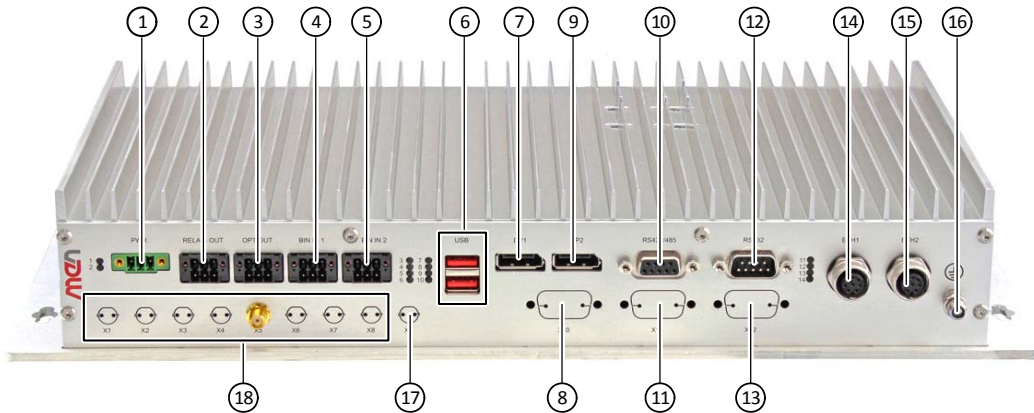
Figure 2. The BL50W - overview (revision 01.02.xx and lower)



1.1.1 External Interfaces

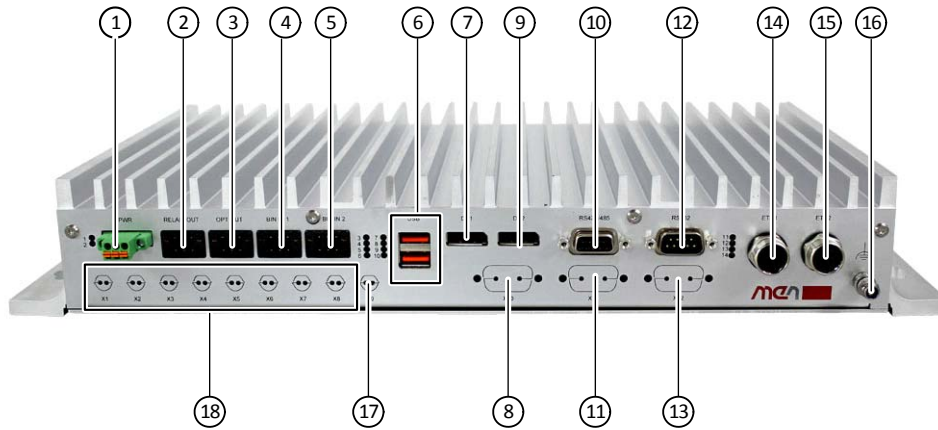
The following picture shows the BL50W front without antennas and flexible SA-Adapters.

Figure 3. The BL50W - front interfaces (revision 01.03.xx and higher)



- | | |
|--|---|
| ① PSU (10V-50.4V) | ⑩ RS422/485 interface |
| ② 2 relay outputs | ⑪ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ③ 2 photocoupler outputs | ⑫ RS232 interface |
| ④ 6 binary inputs | ⑬ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ⑤ 1 odometer input, 1 IBIS slave, 1 binary input | ⑭ Gigabit Ethernet 1 |
| ⑥ 2 USB 2.0 interfaces | ⑮ Gigabit Ethernet 2 |
| ⑦ DisplayPort 1 | ⑯ Earthing stud |
| ⑧ SA-Adapter connector for RS232, RS422/485, IBIS or CAN | ⑰ Antenna connector for GNSS |
| ⑨ DisplayPort 2 | ⑱ Antenna connectors for PCI Express Mini Cards |

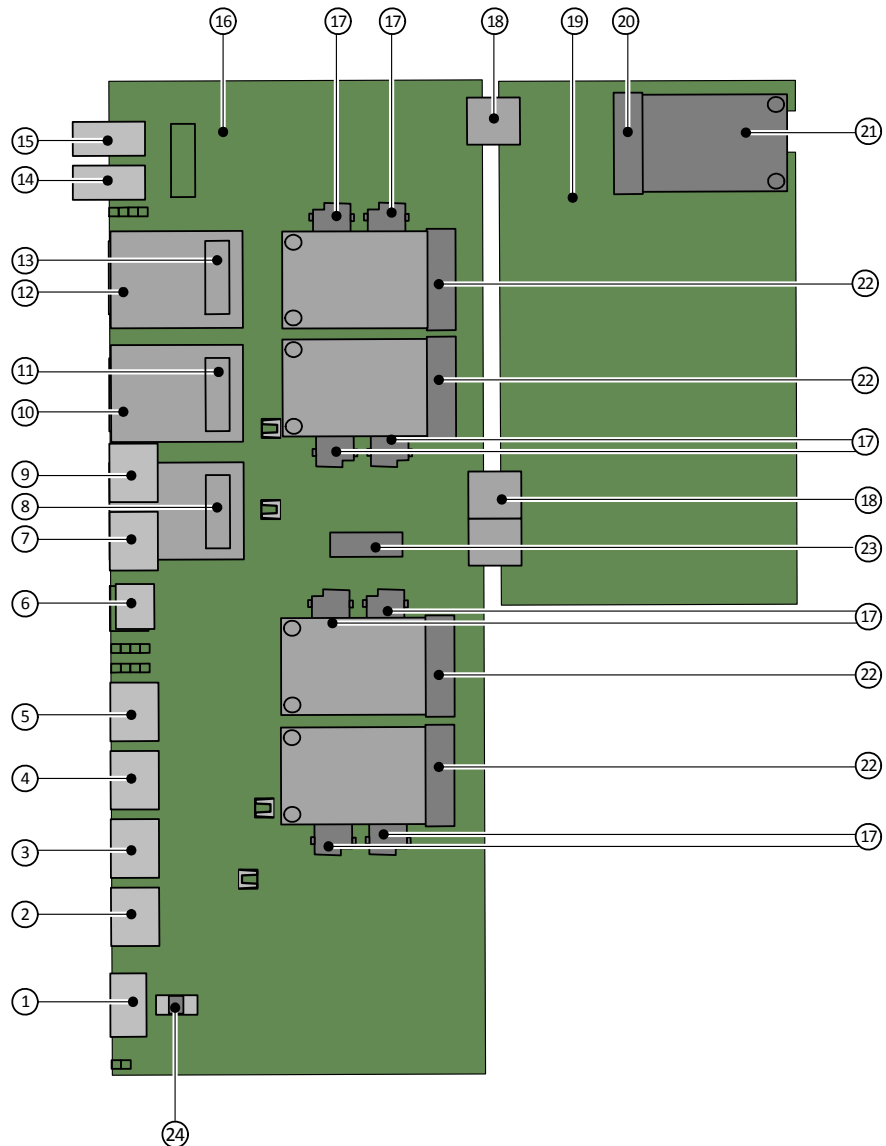
Figure 4. The BL50W - front interfaces (revision 01.02.xx and lower)



- | | |
|--|---|
| ① PSU (10V-50.4V) | ⑩ RS422/485 interface |
| ② 2 relay outputs | ⑪ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ③ 2 photocopler outputs | ⑫ RS232 interface |
| ④ 6 binary inputs | ⑬ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ⑤ 1 odometer input, 1 IBIS slave, 1 binary input | ⑭ Gigabit Ethernet 1 |
| ⑥ 2 USB 2.0 interfaces | ⑮ Gigabit Ethernet 2 |
| ⑦ DisplayPort 1 | ⑯ Earthing stud |
| ⑧ SA-Adapter connector for RS232, RS422/485, IBIS or CAN | ⑰ Antenna connector for GNSS |
| ⑨ DisplayPort 2 | ⑱ Antenna connectors for PCI Express Mini Cards |

1.1.2 Map of the System

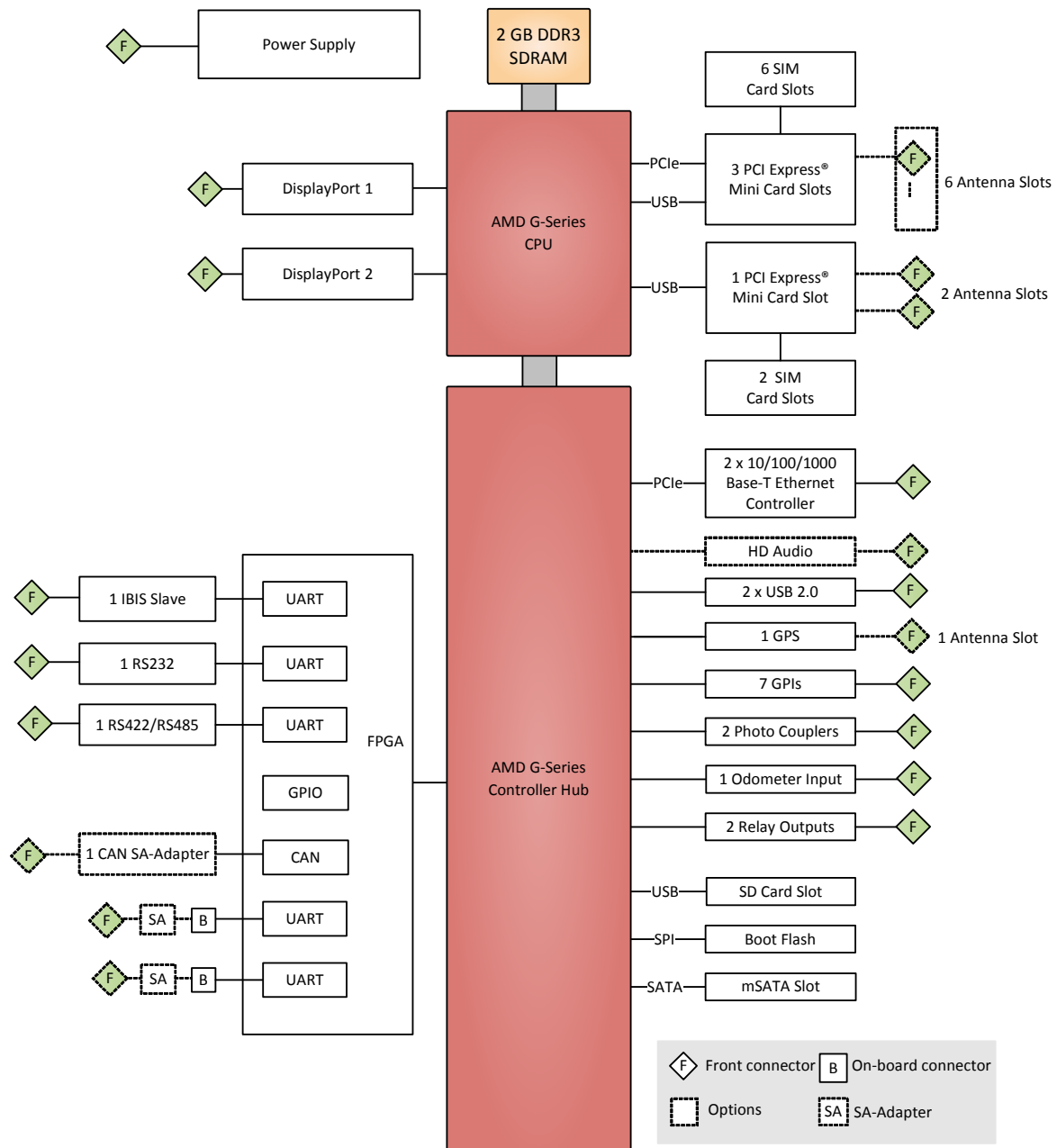
Figure 5. Map of the system – view of the BL50W interior



- | | |
|---|---|
| ① PSU connector | ⑬ SA-Adapter connector for RS232, RS422/485 or IBIS |
| ② 2 relay outputs | ⑭ Gigabit Ethernet 1 on M12 connector |
| ③ 2 optocoupler outputs | ⑮ Gigabit Ethernet 2 on M12 connector |
| ④ 6 binary inputs | ⑯ AE83 I/O Board |
| ⑤ 1 odometer input, 2 IBIS channels, 1 binary input | ⑰ microSIM slot |
| ⑥ 2 USB 2.0 interfaces | ⑱ Board-to-board connector |
| ⑦ DisplayPort1 | ⑲ CPU Board |
| ⑧ SA-Adapter connector for RS232, RS422/485 or CAN | ⑳ mSATA slot |
| ⑨ DisplayPort2 | ㉑ SD card slot (on board bottom) |
| ⑩ RS422/485 interface | ㉒ PCIe Mini Card slot |
| ⑪ SA-Adapter connector for RS232, RS422/485 or IBIS | ㉓ Connector for optional SATA disk |
| ⑫ RS232 interface | ㉔ Fuse for protecting the system |

1.2 Block Diagram

Figure 6. Block diagram



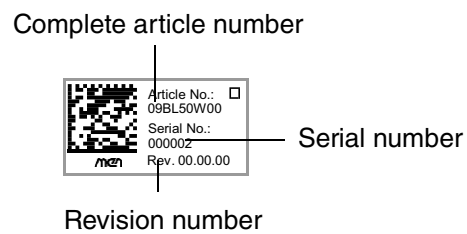
1.3 Product Identification

MEN user documentation may describe several different models and/or design revisions of the BL50W. You can find information on the article number, the design revision and the serial number on a label attached to the chassis.

- **Article number:** Gives the product's family and model. This is also MEN's ordering number. To be complete it must have 9 characters.
- **Revision number:** Gives the design revision of the product.
- **Serial number:** Unique identification assigned during production.

If you need support, you should communicate these numbers to MEN.

Figure 7. Label giving the product's article number, revision and serial number



2 Getting Started

2.1 Unpacking the System

After unpacking, check whether there are any transport or other damages on the system. If one of the following situations arises, get the equipment checked by service personnel:

- The power cable or plug is damaged.
- Liquid has penetrated into the equipment.
- The equipment has been exposed to moisture.
- The equipment does not work well, or you cannot get it to work according to the user manual.
- The equipment has been dropped and damaged.
- The equipment has obvious signs of breakage.



Damaged equipment may be under dangerous voltage and can cause fire. Damaged equipment has unpredictable behavior and characteristics.

Prevent that damaged equipment is installed and put into operation. Mark the damaged equipment and keep it under lock and key. Send the equipment to repair immediately.

When the equipment has been subjected to low temperatures or extreme temperature variations condensation can form on or inside the system. Humidity causes short circuits in electric circuits and damages the system. To avoid damages, do the following:

- Store the equipment in a dry environment
- Ensure that the equipment has the same temperature as the environment before starting it up.
- Do not subject the equipment to the direct radiation of a heating device.
- Wait until the equipment has dried completely or wait 12 hours before switching on the equipment.

2.2 Configuring the Hardware

Check your hardware requirements before mounting the BL50W, since most modifications are difficult or even impossible to do when the box PC is mounted.

The following check list gives an overview on what you might want to configure. For installing the components mentioned below, the BL50W has to be opened.

See [Chapter 2.2.1 Handling Internal Components on page 28](#) and [Chapter 2.2.2 Opening the BL50W on page 28](#).

PCI Express Mini Cards

Four PCI Express Mini Cards for wireless functions can be installed in the BL50W. MEN offers an LTE (4G) and a WLAN card as an accessory.

Refer to [Chapter 2.2.3 Installing PCI Express Mini Cards on page 30](#) and [Chapter 3.12 PCI Express Mini Card Interface on page 59](#) for details on the installation and functionality of the PCI Express Mini Cards.

Antenna connectors

Nine antennas can be installed at the BL50W's front panel (such as SMA, reverse SMA, QMA, FME etc). MEN offers an HF antenna cable with U.FL connector to RP-SMA connector as an accessory.

Refer to [Chapter 2.2.3 Installing PCI Express Mini Cards on page 30](#) and for details on the installation of the antenna connectors.

SD card

The BL50W is equipped with one SD card slot. MEN provides a 4 GB SD card as an accessory (other memory sizes are available on request).

Refer to [Chapter 2.2.5 Installing an SD Card on page 33](#) for information on how to install the SD card.

mSATA disk

The BL50W is equipped with one mSATA disk slot. MEN provides an 8 GB mSATA disk as an accessory (other memory sizes are available on request).

Refer to [Chapter 2.2.6 Installing an mSATA Drive on page 34](#) for information on how to install the mSATA disk.

- ☑ RS232, RS422/485, GPS, IBIS extension through MEN standard SA-Adapters

The BL50W provides three 10-pin I/O connectors for connection of three SA-Adapters for RS232, RS422/485, GPS, IBIS or CAN functionality. MEN provides a range of standard adapters with different functionality.

Refer to [Chapter 2.2.4 Installing SA-Adapters on page 57](#) and [Chapter 3.10 Serial Interfaces on page 55](#) for details on the installation and functionality of the SA-Adapters.



See MEN's [website](#) for information on how to order the accessories.

2.2.1 Handling Internal Components



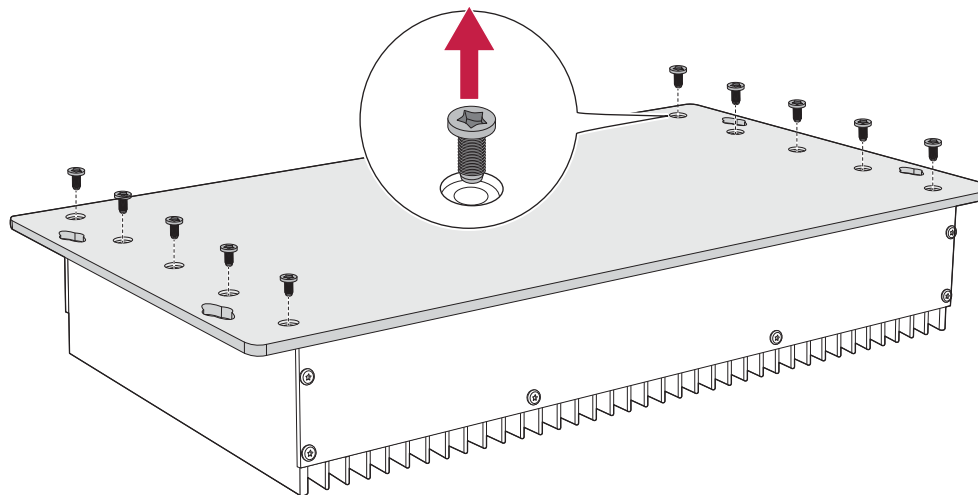
- Activities on the inside of the system have to be carried out by the service person for this area or a suitably instructed user.
- Switch off or unplug the power supply of the box PC before working on internal components.
- Please observe the instructions concerning electrostatic discharge whenever you work on the inside of the computer system. See [Chapter Electrostatic Discharge \(ESD\) on page 10](#).

2.2.2 Opening the BL50W

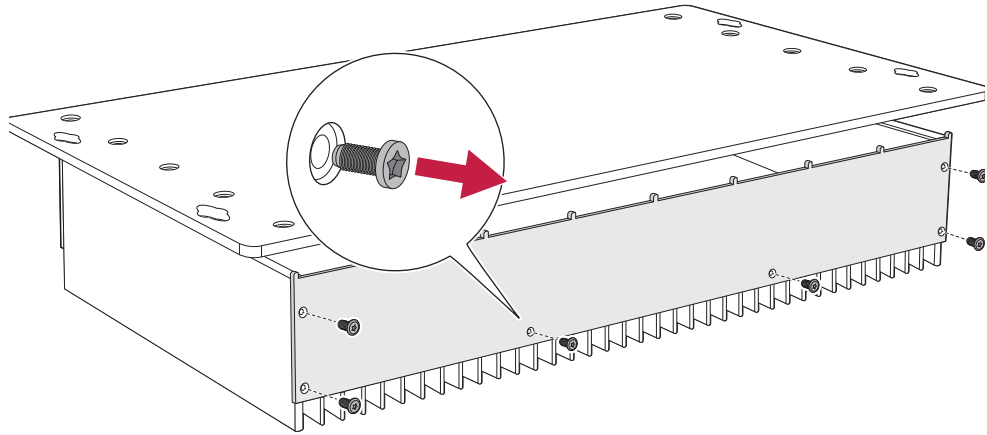
The PCI Express Mini Cards, the SA-Adapters, the microSIM cards, the mSATA slot and SD card slot of the BL50W can be accessed after opening the housing.

- For this purpose, remove the system's bottom panel fixed by ten M3x8 TX8 Torx screws.

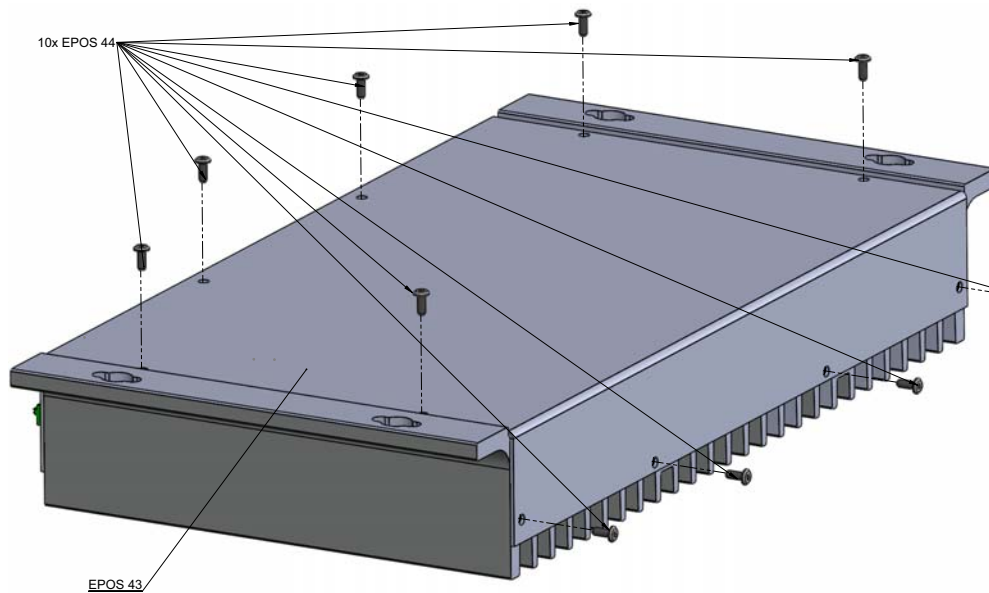
Figure 8. Screw positions on the bottom of the BL50W



- To access the SD card slot, remove the rear panel of the unit fixed by six M3x8 TX8 Torx screws.

Figure 9. Screw positions at the rear of the BL50W**Opening the Housing of BL50W Versions with Revisions R01.02.xx and Lower**

- Remove the system's bottom and rear panel fixed by six M3x8 TX8 Torx screws at the system's bottom and four screws at the system's rear as shown in the following picture.

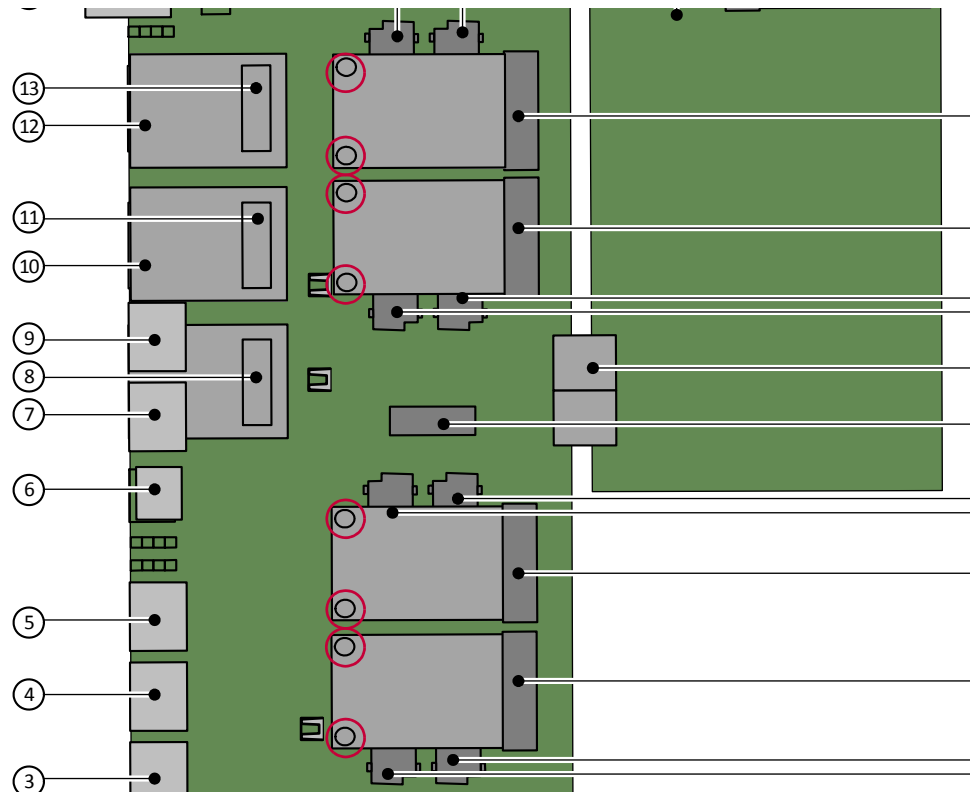
Figure 10. Screw positions on the bottom and rear of the BL50W

2.2.3 Installing PCI Express Mini Cards

Within its housing, the BL50W provides four PCI Express Mini card slots. The M2.5x8 screws required for installation are already screwed onto the board. Carry out the following steps to install a PCI Express Mini card.

- Untighten and remove the screws from the spacers (highlighted in red).

Figure 11. Installing a PCI Express Mini card



- Insert the PCI Express Mini card carefully at a 30° angle.
- Make sure that all the contacts are aligned properly and the card is firmly connected to the connector.
- Fix the card using the two screws removed before.

Antenna Connectors

MEN offers an HF antenna cable with U.FL connector to RP-SMA connector as an accessory.

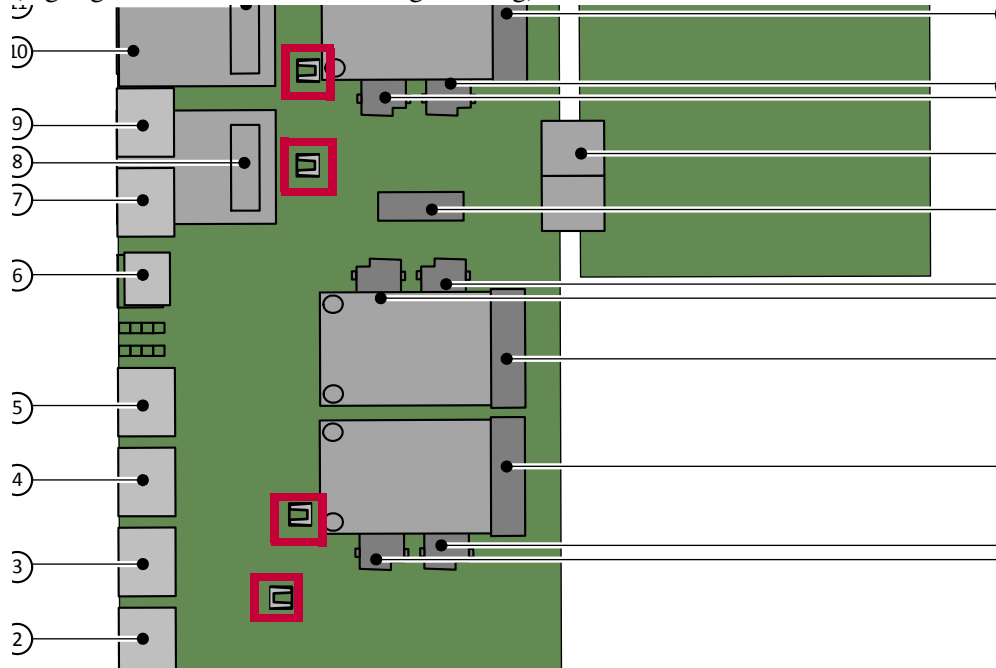


Please see the [MEN website](#) for ordering information.

Before being able to install the antenna on the BL50W's front panel, you have to break out the cover of the antenna slot, using a screw driver or a similar tool.



There are four mounting clips for fixing the antenna cable on the BL50W's PCB (highlighted in red in the following drawing).



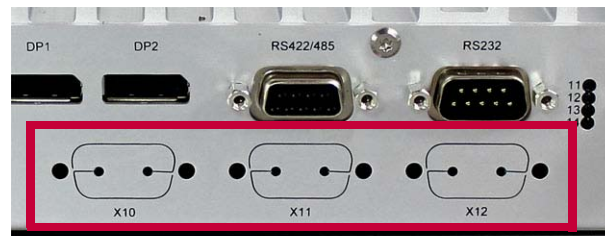
Note: There is only one position in which the antenna can be completely inserted into the front panel slot. When properly inserted the antenna is fixed and cannot be turned anymore.

2.2.4 Installing SA-Adapters

Three SA-Adapters can be mounted in the BL50W on the 10-pin receptacles for slots X10, X11 and X12.

Carry out the following steps to install the SA-Adapters:

- ☑ Remove the front panel as described in [Chapter 2.2.2 Opening the BL50W on page 28](#).
- ☑ Break out the covers of the front panel slots.



- ☑ Make sure that the adapter matches the standard dimensions for SA-Adapters.

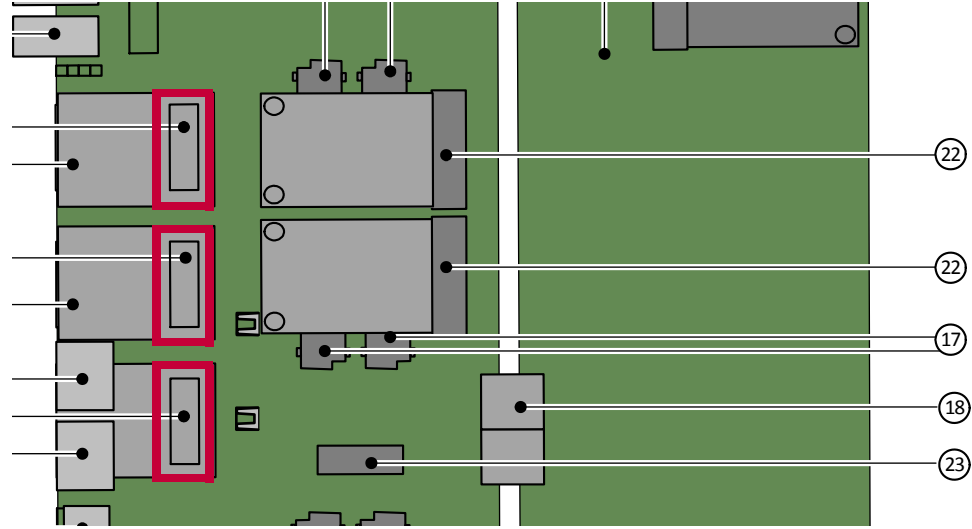


See also installation hints in the adapter's user manual or the list of compatible accessories in the BL70W data sheet on MEN's [website](#).

- ☑ Remove the two front panel screws of the SA-Adapter.



- ☑ Carefully align the SA-Adapter connectors with the 10-pin connectors on the BL50W.

Figure 12. 10-pin SA-Adapter connectors (highlighted in red)

- Press the SA-Adapter firmly onto the BL50W.



Carefully align the front panel with the front connectors and the LEDs. Be careful not to damage the LEDs.

- Screw the front panel back onto the BL50W.
- Screw the SA-Adapter tightly to the BL50W front panel using the two pan-head screws removed before.



2.2.5 Installing an SD Card

Within its housing, the BL50W provides one SD card slot. Carry out the following steps to install an SD card.

- The SD card slot is positioned at the edge of the system, wedged between the PCB and the top of the housing.

- ☑ Insert the SD card into the slot with the contacts facing to the PCB and the cut edge to the right side.

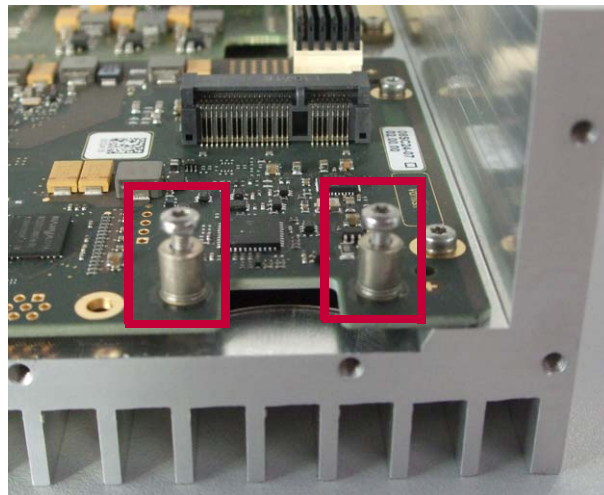


- ☑ Make sure that it clicks into place properly.
- ☑ To eject the SD card, push it until it springs out, then simply pull it out.

2.2.6 Installing an mSATA Drive

Within its housing, the BL50W provides one mSATA slot. Two M2.5x8 screws are provided with the BL50W.

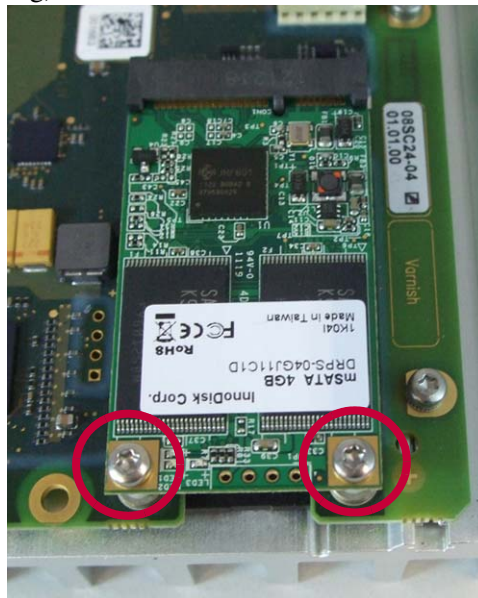
- ☑ Untighten and remove the screws from the spacers (highlighted in red).



- ☑ Insert the mSATA drive carefully at a 30° angle.



- ☑ Make sure that all the contacts are aligned properly and the card is firmly connected to the mSATA connector.
- ☑ Align the spacers and the holes and insert the screws into the spacers from above the mSATA disk.
- ☑ Fix the card by tightening the screws (highlighted in red in the following drawing).



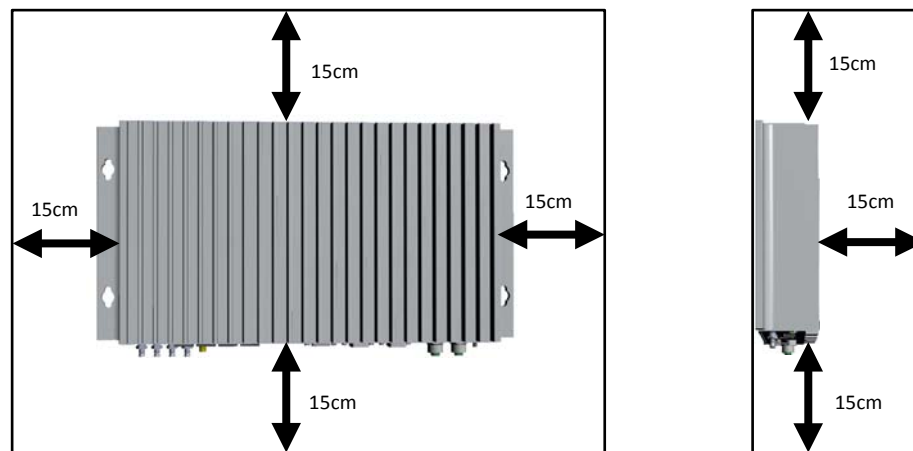
2.3 Mounting the BL50W

Please consider the following instructions when installing the BL50W:



- Do not install the system near any heat sources (e.g. radiators, heat registers).
- Keep the system away from liquids. Avoid exposure to dripping or splashing.
- Keep a free space of 15 cm around the housing to ensure cooling (except on the mounting side).

Figure 13. Mounting distances required for the box PC



- The connector side should face down.
- The BL50W provides four mounting holes for installation.
- Use M5 countersink head screws.

See [Chapter 8.2 Dimensions of the BL50W Box PC on page 82](#) for the exact dimensions of the box PC and the positions of the mounting holes and connectors.

2.3.1 Installing the BL50W in a 19" Rack

MEN offers a 1.5U mounting kit which makes it possible to install the BL50W in a 19" rack (05BL01-00).

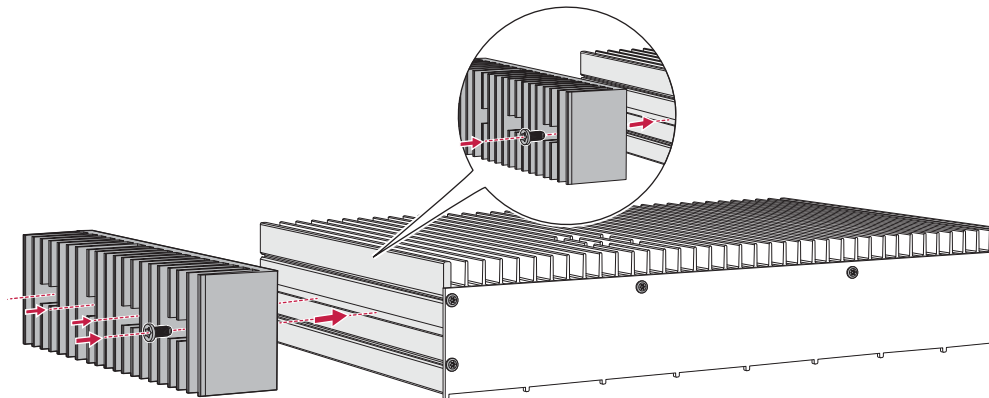


Please see MEN's [website](#) for more information.

- ☑ Remove the BL50W's bottom panel as described in [Chapter 2.2.2 Opening the BL50W on page 28](#).
- ☑ Remove five of the screws fastening the front panel (highlighted in red in the following picture). Do not remove the front panel.

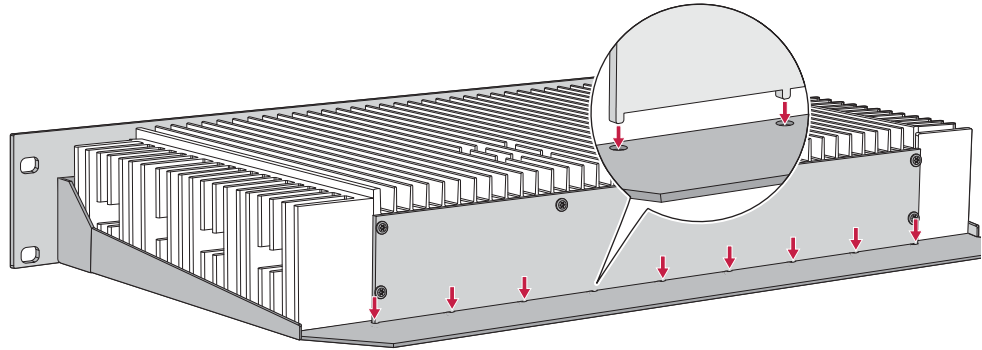


- ☑ Install the two heatsinks supplied with the kit at the sides of the BL50W using four M2.5x10 TX8 Torx screws for each heat sink. The screws are included in the delivery of the 05BL01-00 kit.

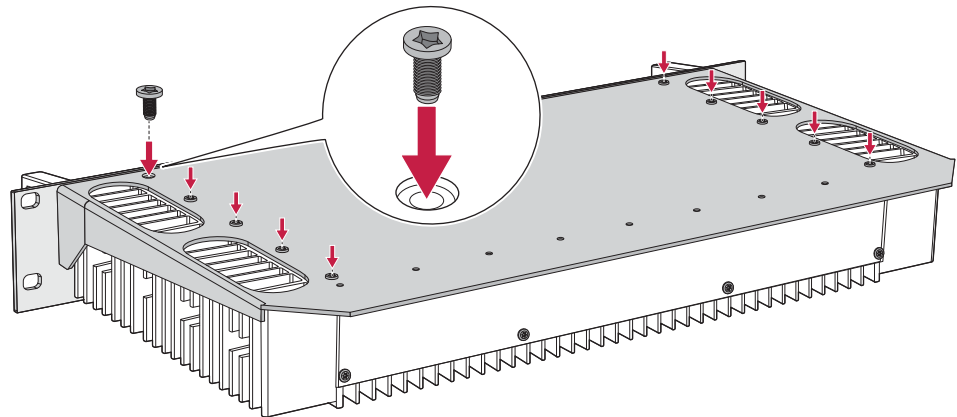


- ☑ Insert the box PC into the opening in the kit's front panel.

- ☑ Take care that the small studs at the bottom of the front and rear panel of the box PC are aligned with the corresponding holes in the bottom plate of the mounting kit.



- ☑ Press the BL50W down onto the bottom plate.
- ☑ Screw the BL50W onto the kit's bottom plate using the ten screws removed before.



- ☑ Fix the BL50W at the kit's front panel using the five M3x8 TX8 Torx screws removed before.



- ☑ Fasten the BL50W in the frame in the 19" switching cabinet using four M6x16 screws.

2.4 Connecting an Earthing Cable

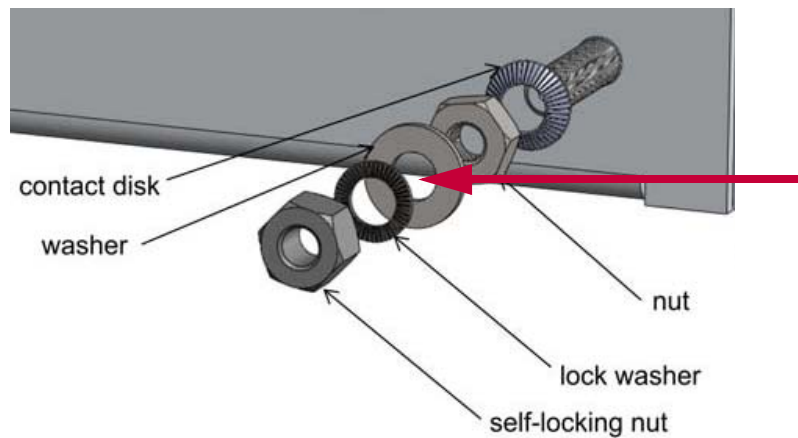
The BL50W features an earthing stud on the right side of the front panel (see [Figure 3, The BL50W - front interfaces \(revision 01.03.xx and higher\)](#) on page 20). A protective earth connection is essential for the system to meet its EMC specifications.



An earthing cable has to be connected to the earthing stud before any other connections! For disassembling the system, the earthing cable has to be detached last.

Carry out the following steps to connect an earthing cable:

- ☑ Take an earthing cable with a cross section of at least 0.75 mm².
- ☑ Slide the cable onto the stud between the washer and the lock washer as indicated in the following picture:



- ☑ Fasten the cable by tightening the nut.

2.5 Electrical Connection



- Ensure that the box PC is completely configured and mounted before connecting and applying power to the system.
- Implement a readily accessible disconnect device external to the box PC for complying with the EN 60950-1 standard.
- Make sure that the voltage of the power supply conforms with the voltage on the type plate.
- Ensure that the power supply (power socket) is grounded correctly and that the power cable is intact and undamaged.
- Do not switch on the system if there are damages on the power cable or plug.
- Use power cables which are approved for the power supply in your country.
- Power supplies have to be grounded.
- Connect the PSU to the power supply via the power cable.

2.6 Starting up the System



Make sure that all peripheral devices are connected to the system before connecting an external power supply and switching on the system.

You can use the following check list when installing the unit for the first time and with minimum configuration.

- Connect a USB keyboard and mouse to the USB connector at the front panel.
- Connect a flat-panel display capable of displaying the resolution of 1024x786 to the DisplayPort connector of the BL50W.
- Power up the system. See [Chapter 3.1 Power Supply on page 41](#).
- You can start up the BIOS setup menu by hitting the <F2> key.
- Now you can make configurations in BIOS.
- Observe the installation instructions for the respective software.

2.7 Installing Operating System Software

The board supports Windows 7 and Linux.



By default, no operating system is installed on the board. Please refer to the respective manufacturer's documentation on how to install operating system software!

2.8 Installing Driver Software

For a detailed description on how to install driver software please refer to the respective documentation of the software package to be installed.



You can find any software available on the BL50W pages on the [MEN website](#).

3 Functional Description

The following describes the individual functions of the system and their configuration. There is no detailed description of the individual controller chips and the CPU. They can be obtained from the data sheets or data books of the semiconductor manufacturer concerned.

3.1 Power Supply

The BL50W is supplied with a nominal input voltage of 24 VDC and 36 VDC (10 to 50.4 V input voltage range) via a 3-pin COMBICON connector. You can find pin 1 at the left (see [Figure 14, PSU connector at BL50W front](#)).

The onboard power supply generates all the necessary internal voltages.

Connector type:

- 3-pin COMBICON receptacle (Phoenix Contact 1843800 MC 1,5/ 3-GF-3,5)

Mating connector:

- 3-pin COMBICON plug, e.g., Phoenix Contact 1863314 MCVR 1,5/ 3-STF-3,5

Figure 14. PSU connector at BL50W front



Table 1. Pin assignment of PSU connector

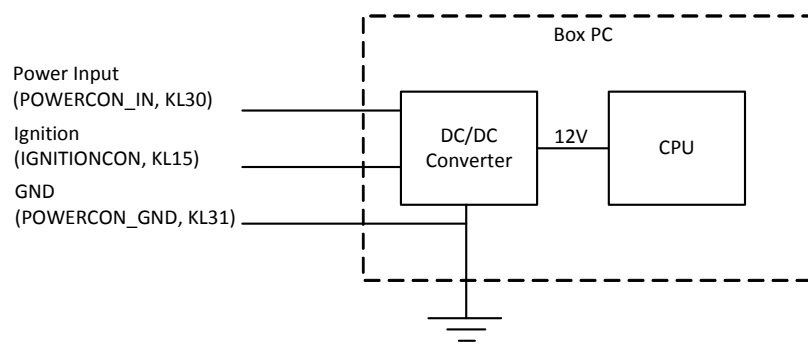
1	POWERCON_IN	Power input
2	POWERCON_GND	Power input ground
3	IGNITIONCON	Ignition

3.1.1 Ignition

Using the ignition pin, the start-up and shut-down of the BL50W can be controlled.

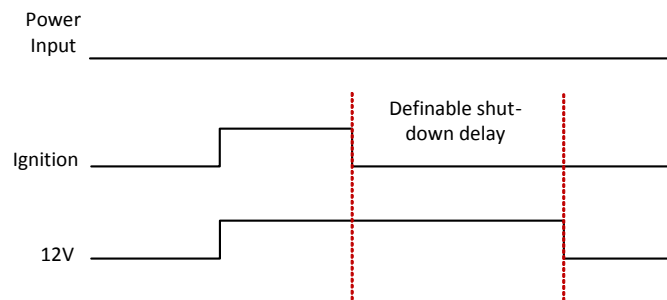
If the ignition pin (IGNITIONCON, KL15) is connected to the power input pin (POWERCON_IN, KL30) via a switch or a controller, switching on and off of the BL50W can be controlled without having to disconnect the power supply.

Figure 15. Connection of power pins from PSU



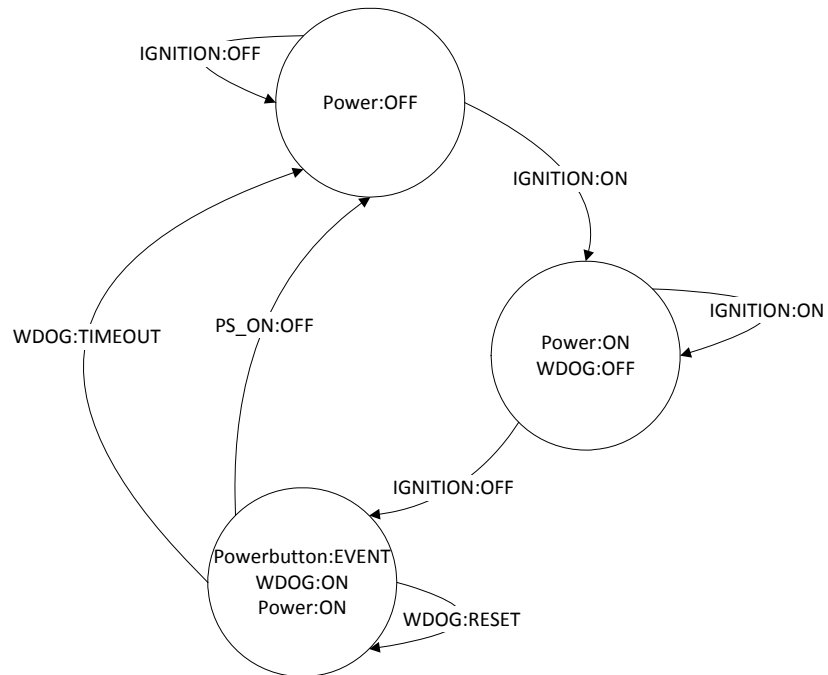
When the ignition is turned on, the 12 V supply voltage is also switched on (see [Figure 16, Ignition behavior](#)). When the ignition is turned off, the input voltage is not switched off at once but can be supplied for a user-defined time (shut-down delay) so that the operating system of the BL50W can shut down in a controlled way.

Figure 16. Ignition behavior



The shut-down delay can be set using a watchdog. If the ignition signal is disabled and the watchdog is running, it is possible to reset the watchdog timer by software using the WDOG_EN signal (see [Figure 32, SMB 0x4E](#)) to avoid a power shutdown. If the watchdog is running and the timer is not restarted, the box computer is forced into power down mode after approximately 5 minutes.

Figure 17. Ignition and watchdog state diagram



The ignition pin (pin 3) can also be permanently connected to the power input pin (pin 1). In this case the BL50W is starting up as soon as the supply voltage is connected and switched off as soon as the supply voltage is disconnected without a delay for shutting down the operating system.

3.2 Real-Time Clock

The board includes a real-time clock connected to the processor as a system RTC. The RTC has an accuracy of approximately 1.7 seconds/day (11 minutes/year) at 25°C.

For data retention during power off the RTC is backed up by a supercapacitor. The supercapacitor gives an autonomy of up to 72 hours when fully loaded.

The real-time clock device is connected to the CPU via SMBus. Due to its reduced current consumption, the life time of the battery or supercapacitor can be increased considerably compared to the RTC integrated in the CPU.



MEN provides a dedicated software driver for the RTC device in order to set date and time as usual in Windows. For a detailed description of the functionality of the driver and for downloading the software please refer to the drivers' documentation on MEN's [website](#).

3.3 Processor Core

The BL50W can be equipped with several dual-core AMD APUs (Accelerated Processing Units). The default APU is the T48N (marked in gray in the following table).

Table 2. Processor core options on BL50W

Model	Clock Speed, No. of Cores	Cache	Max. TDP	DDR3 Speed	Graphics
AMD T56N	1.65 GHz dual core	L1 cache 64 KB, L2 cache 512 kB x2	18 W	DDR3-1333	Radeon HD 6320
AMD T52R	1.5 GHz single core	L1 cache 64 KB, L2 cache 512 kB	18 W	DDR3-1333	Radeon HD 6310
AMD T48N	1.4 GHz dual core	L1 cache 64 KB, L2 cache 512 kB x2	18 W	DDR3-1066	Radeon HD 6310
AMD T40N	1.0 GHz dual core	L1 cache 64 KB, L2 cache 512 kB x2	9 W	LVDDR3-1066	Radeon HD 6310
AMD T44R	1.2 GHz single core	L1 cache 64 KB, L2 cache 512 kB	9 W	LVDDR3-1066	Radeon HD 6250
AMD T40E	1.0 GHz dual core	L1 cache 64 KB, L2 cache 512 kB x2	6.4 W	LVDDR3-1066	Radeon HD 6250
AMD T40R	1.0 GHz single core	L1 cache 64 KB, L2 cache 512 kB	5.5 W	LVDDR3-1066	Radeon HD 6250
AMD T16R	615 MHz single core	L1 cache 64 KB, L2 cache 512 kB	4.5 W	LVDDR3-1066	Radeon HD 6250
AMD T56E	1.65 GHz Dual Core	L1 cache 64KB, L2 cache 512kB x2	18W	DDR3-1333	Radeon HD 6250
AMD T48E	1.4 GHz Dual Core	L1 cache 64KB, L2 cache 512kB x2	18W	DDR3-1066	Radeon HD 6250

Model	Clock Speed, No. of Cores	Cache	Max. TDP	DDR3 Speed	Graphics
AMD T48L	1.4 GHz Dual Core	L1 cache 64KB, L2 cache 512kB x2	18W	DDR3-1066	N/A
AMD T30L	1.4 GHz Single Core	L1 cache 64KB, L2 cache 512kB	18W	DDR3-1066	N/A
AMD T24L	1000 MHz Single Core	L1 cache 64KB, L2 cache 512kB	5W	LVDDR3-1066	N/A

Note: T56N and T40N are models enabled by AMD Turbo Core technology, up to 10% clock speed increase is planned.

3.3.1 Thermal Considerations

The operating temperature range of the BL50W depends on the system configuration (CPU, PCIeMiniCards, Ethernet, USB, ...)

The power dissipation of the system also depends on the environmental conditions. It has a typical power dissipation of 14.4 W in a configuration with a T48N CPU with Windows 7 operating system and 1 Gb Ethernet connection.

The system is designed for a maximum operating temperature of +70°C (+85°C for 10 minutes). The minimum temperature is -40°C for all processors.

As an option, a wider housing with additional cooling fins is available, enabling permanent operation at +85°C.

3.4 Memory and Mass Storage

3.4.1 DRAM System Memory

The standard model of the BL50W is equipped with 2 GB of DDR3 SDRAM. Up to 4 GB are supported. The graphics controller of the BL50W has no own memory and so uses 256 MB of the system memory by default. Other values can be set using a BIOS item in the sub-menu Video Configuration (possible settings 64 MB, 128 MB, 256 MB or 512 MB).

3.4.2 Boot Flash

The BL50W is equipped with a boot Flash containing its BIOS.

3.4.3 SD Card Slot

Within its housing, the BL50W provides one SD card slot. The slot supports the Secure Digital 2.0 specification (SDHC) with a storage capacity of 2 up to 32 GB and a data transfer rate of 25 MB/s.

See [Chapter 2.2.5 Installing an SD Card on page 33](#) for information on how to install the SD card.

3.4.4 mSATA Slot

Within its housing, the BL50W provides one mSATA slot.

See [Chapter 2.2.6 Installing an mSATA Drive on page 34](#) for information on how to install an mSATA disk.

3.4.5 SATA Hard Disk (Optional)

The BL50W offers the possibility to install an additional SATA hard disk in the housing on a special mounting frame.

Please contact the [MEN sales team](#) for further information.

3.5 Graphics

3.5.1 Graphics Processing Unit

The standard model of the BL50W is equipped with an AMD APU (Accelerated Processing Unit) that also includes an AMD Radeon 6310 as a GPU (Graphics Processing Unit).

For other GPU options please refer to [Chapter 3.3 Processor Core on page 44](#).

3.5.2 DisplayPort Interfaces

Two DisplayPort interfaces are available at the front panel. The maximum supported resolution is 2560x1600 at 60 Hz. The interfaces offer AUX channel support and hot plug detection.

Connector type:

- 20-pin DisplayPort receptacle

Mating connector:

- 20-pin DisplayPort plug

Table 3. Pin assignment of 20-pin DisplayPort connector

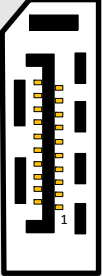
	20	POWER	19	RETURN PWR
	18	DP_HOTPLUG	17	DP_AUX-
	16	GND	15	DP_AUX+
	14	CONFIG2	13	CONFIG1
	12	LANE_3-	11	GND
	10	LANE_3+	9	LANE_2-
	8	GND	7	LANE_2+
	6	LANE_1-	5	GND
	4	LANE_1+	3	LANE_0-
	2	GND	1	LANE_0+

Table 4. Signal mnemonics of 20-pin DisplayPort connector

Signal	Direction	Function
GND	-	Ground
DP_AUX-, DP_AUX+	in/out	Bi-directional half-duplex auxiliary channels for device management and device control
CONFIG1, CONFIG2	-	Connected to Ground
DP_HOTPLUG	in	Hot Plug Detect
LANE_[3..0]+, LANE_[3..0]-	out	Main Link data lanes
POWER	out	Power for connector (3.3 V, 500 mA)
RETURN PWR	-	Return for Power

3.5.3 Other Graphics Interfaces

MEN offers a starter kit including a DisplayPort to DVI adapter.



Please see the [MEN website](#) for ordering information.

In addition, many third-party suppliers offer active adapters from DisplayPort to other graphics interfaces. The maximum resolution depends on the adapter used. Supported interfaces include:

- HDMI
- Single-link DVI
- Dual-link DVI
- VGA

3.6 USB Interfaces

The BL50W provides two USB 2.0 interfaces at the front panel via two automotive USB connectors. The automotive USB connectors are 100% compliant to standard connectors but offer a more robust connection.

Connector types:

- 4-pin USB Series A receptacle according to Universal Serial Bus Specification Revision 1.0
- Mating connector:
4-pin USB Series A plug according to Universal Serial Bus Specification Revision 1.0

Table 5. Pin assignment of USB front-panel connectors

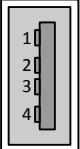
	1	+5V
	2	USB_D-
	3	USB_D+
	4	GND

Table 6. Signal mnemonics of USB front-panel connectors

Signal	Direction	Function
+5V	out	+5 V power supply
GND	-	Digital ground
USB_D+, USB_D-	in/out	USB lines, differential pair

3.7 Ethernet Interfaces

The BL50W provides two Gigabit Ethernet ports at the front.

Connector type:

- 8-pin M12 receptacle, female, A-coded 90° (Phoenix Contact 1436974 SACC-DSIV-FS-8CON-L90 SCO)

Mating connector:

- 8-pin M12 plug, male, A-coded

MEN offers a starter kit including an M12 to RJ45 adapter for making the Ethernet interfaces available on standard Ethernet connectors.



See MEN's [website](#) for information on how to order the accessories.

Table 7. Pin assignment of Ethernet front-panel connectors

		1000Base-T	10/100Base-T
	1	BI_DC-	
	2	BI_DD+	
	3	BI_DD-	
	4	BI_DA-	TX-
	5	BI_DB+	RX+
	6	BI_DA+	TX+
	7	BI_DC+	-
	8	BI_DB-	RX-

Table 8. Signal mnemonics of Ethernet front-panel connectors

Signal	Direction	Function
BI_Dx+/-	in/out	Differential pairs of data lines for 1000Base-T
RX+/-	in	Differential pair of receive data lines for 10/100Base-T
TX+/-	out	Differential pair of transmit data lines for 10/100Base-T

3.7.1 Ethernet Status LEDs

The BL50W provides a total of four Ethernet status LEDs, two for each Ethernet channel. They signal the link and activity status (different LED behavior can be implemented on demand).

Figure 18. Position of Ethernet LEDs at BL50W front (highlighted in red)

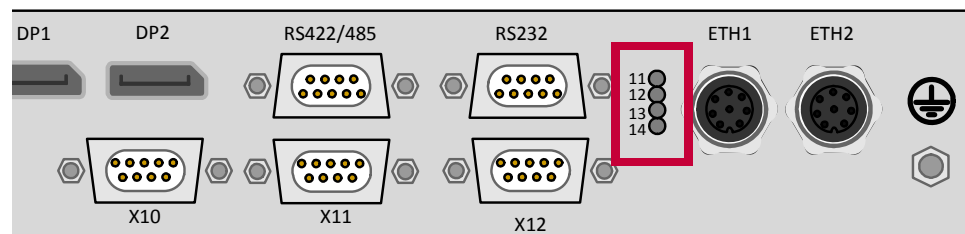






Table 9. Ethernet status LEDs

LED	Description	Color	Function
11 	Port 1 link	green	on, when connection established
12 	Port 1 activity	yellow	on, when Ethernet communication on Rx or Tx
13 	Port 2 link	green	on, when connection established
14 	Port 2 activity	yellow	on, when Ethernet communication on Rx or Tx

3.8 HD Audio (Optional)

As an option, the BL50W can be equipped with a high definition audio interface implemented via an audio codec and available on a standard 9-pin D-Sub connector at the front panel.

Table 10. Pin assignment of the HD audio interface

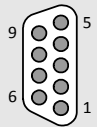
	9	AUDIO_IN_R	5	AUDIO_SPDIF
	8	AUDIO_GND	4	AUDIO_IN_L
	7	AUDIO_OUT_R+	3	AUDIO_OUT_R-
	6	AUDIO_OUT_L-	2	AUDIO_GND
			1	AUDIO_OUT_L+

Table 11. Signal mnemonics of the HD audio interface

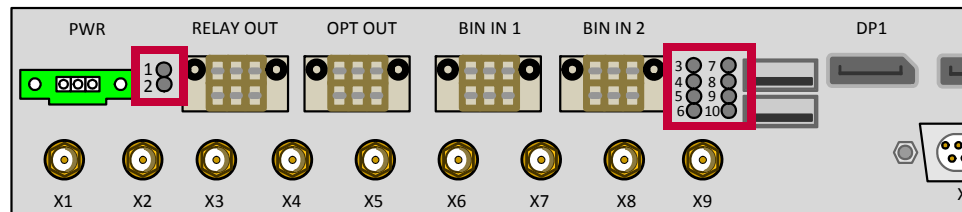
Signal	Direction	Description
AUDIO_EXT_OUT_L±/R±	out	Line out, left and right, differential signal pairs
AUDIO_EXT_IN_L/R	in	Line in, left and right
AUDIO_EXT_GND	-	Analog ground
AUDIO_EXT_SPDIF	out	S/PDIF output

3.9 Status and User LEDs

In addition to the four Ethernet status LEDs, the BL50W provides two status LEDs and eight general status LEDs. One of the status LEDs signals whether the onboard power generated by the BL50W's on-board DC/DC converter is within valid range, the other signals the system status (see [Chapter 3.9.1 Status LED on page 54](#) for a detailed description of the LED's behavior).











The eight user LEDs can be controlled via an I/O expander on the SMBus. The LED can be used freely depending on an application's requirements. See [Chapter 5.1 SMBus Overview](#) for information on how to access the LEDs.

Figure 19. Status and user LEDs on the BL50W's front panel (highlighted in red)



The following table shows a list of all status and user LEDs with their functionality or default state.

Table 12. Status and user LEDs on the BL50W

Number	Description	Color	Function
1 	Power good	green	on, when internal 12V active
2 	Status LED (diagnostic)	yellow	see Chapter 3.9.1 Status LED on page 54
3 	User LED A	yellow	default: off
4 	User LED B	yellow	default: off
5 	User LED C	yellow	default: off
6 	User LED D	yellow	default: off
7 	User LED E	yellow	default: off
8 	User LED F	yellow	default: off
9 	User LED G	yellow	default: off
10 	User LED H	yellow	default: off

3.9.1 Status LED

The status LED (LED2) is connected to the system's board controller.

It has the following behavior:

- off, if system is in S5 state
- blinking at less than 0.5 Hz if system is in S3 state
- on, if system is in S0 state and BIOS has sent live sign after power-up
- it flashes repeatedly n times according to an error code and pauses for one second until the system is restarted or completely powered-off, if system is in error condition and error code is n. See the following table for supported error codes.

Table 13. Error codes signaled by Status LED flashes

Error Code	Description	Solution
1	+V3.3A Voltage Failure	internal failure
2	Input Voltage Failure	internal failure
3	External Power Supply Failure	check power supply voltage range
4	CPU too hot	check thermal constraints
5	BIOS Live Sign Timeout	internal failure
6	System Reset Timeout	internal failure
7	Platform Reset Failure	internal failure
8	Chipset Handshake Failure	internal failure
9	System Power OK Failure	internal failure
255	Invalid PIC state	internal failure

3.10 Serial Interfaces

3.10.1 RS232 Interface

The BL50W provides one isolated RS232 interface with a baud rate of up to 115.2 kbaud. The interface (COM1) supports Rx, Tx, RTS and CTS signals.

Connector types

- 9-pin D-Sub plug according to DIN41652/MIL-C-24308, with thread bolt UNC4-40
- Mating connector:
9-pin D-Sub receptacle according to DIN41652/MIL-C-24308, available for ribbon cable (insulation piercing connection), hand-soldering connection or crimp connection

Table 14. Pin assignment of 9-pin D-Sub connector for RS232

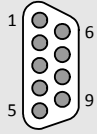
	1	-	6	-
	2	COM0_RXD	7	COM0_RTS#
	3	COM0_TXD	8	COM0_CTS#
	4	-	9	-
	5	GND		

Table 15. Signal mnemonics of RS232 connector

Signal	Direction	Function
GND	-	Ground
+5V	out	+5V supply voltage
CTS#	in	Clear to send
RTS#	out	Request to send
RXD	in	Receive data
TXD	out	Transmit data

3.10.2 RS422/485 Interface

The BL50W provides one isolated RS422/485 full and half duplex interface with a baud rate of up to 1 Mbaud. It is possible to switch between half and full duplex mode via application software. The RS422/RS485 interface is switched between RS422 and RS485 using the RTS signal of the used UART interface (COM2).

Connector types

- 9-pin D-Sub receptacle according to DIN41652/MIL-C-24308, with thread bolt UNC4-40
- Mating connector:
9-pin D-Sub plug according to DIN41652/MIL-C-24308, available for ribbon cable (insulation piercing connection), hand-soldering connection or crimp connection

3.10.2.1 Full-Duplex Interface

Table 16. Pin assignment of the 9-pin D-Sub receptacle – full duplex

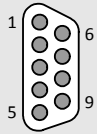
	6	I-VCC	1	-
	7	-	2	-
	8	TX-	3	TX+
	9	RX-	4	RX+
			5	I-GND

Table 17. Signal mnemonics of RS422/485 interface – full duplex

Signal	Direction	Description
I-GND	-	Isolated reference potential
RX+/-	in	Receive data (differential pair)
TX+/-	out	Transmit data (differential pair)
I-VCC	out	Isolated power supply

3.10.2.2 Half-Duplex Interface

Table 18. Pin assignment of the 9-pin D-Sub receptacle – half duplex

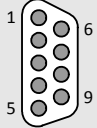
	6	I-VCC	1	-
	7	-	2	-
	8	RX/TX-	3	RX/TX+
	9	-	4	-
			5	I-GND

Table 19. Signal mnemonics of RS422/485 interface – half duplex

Signal	Direction	Description
I-GND	-	Isolated reference potential
RX+/-	in	Receive data
TX+/-	out	Transmit data
I-VCC	out	Isolated power supply

3.11 Serial Interfaces via SA-Adapter

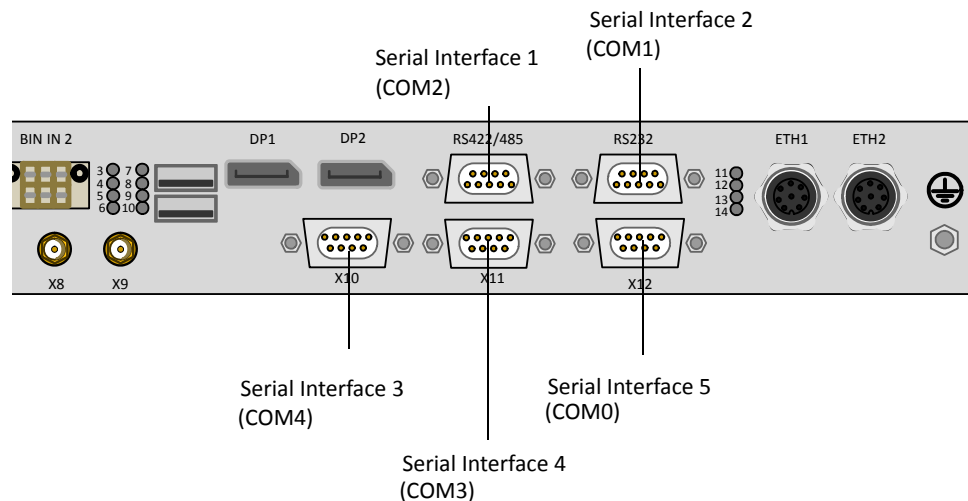
The BL50W offers the possibility to provide three additional serial interfaces at the front of the BL50W using MEN standard SA-Adapters. This way, a serial interface can be used which can be flexibly configured as needed.

Serial interface 3 (X10) can be used for UART or CAN bus functionality (for CAN bus functionality a special product version is required), interface 4 (X11) and 5 (X12) can be used for UART, IBIS or GPS.

If you use the 08SA15-00 SA-Adapter with SGPIO functionality at serial interface 5 (X12) you have to change a setting in the FPGA to switch from UART to SGPIO. See [Chapter 6.1 GPIO Controller Instance 1 on page 78](#).

If you want to use an SA-Adapter at serial interface 3 (X10) you have to activate the UART in the system's FPGA. See [Chapter 6.1 GPIO Controller Instance 1 on page 78](#).

Figure 20. Position of serial interfaces on BL50W front



See [Chapter 2.2.4 Installing SA-Adapters on page 32](#) for installation instructions.



- See MEN's [website](#) for a list of SA-Adapters which can be used on the box PC.
- Please [contact MEN's sales team](#) for information about possible configurations and special board versions.

3.12 PCI Express Mini Card Interface

The BL50W supports the PCI Express Mini Card standard. Its four PCI Express Mini Card slots are located within the housing.

Refer to [Figure 5, Map of the system – view of the BL50W interior on page 22](#) for the exact position of the PCI Express Mini Card slot.

As an option, the BL50W can also be equipped with four PCI Express Mini Card slots compatible with half-size modules.

See [Chapter 2.2.3 Installing PCI Express Mini Cards on page 30](#) for information on how to install the PCI Express Mini Cards in the box PC.

Three of the PCIe MiniCard sockets on the BL50W support both a USB and a PCIe interface. The fourth socket only supports USB.

The power supply of the PCIe MiniCard can be reset via GPIO. This power reset is required when switching from one micro-SIM card to another.

See [Chapter 5.1 SMBus Overview on page 69](#) for more information on how to switch the micro-SIM cards.

Micro-SIM Cards

The BL50W provides eight micro-SIM Card sockets. Two micro-SIM cards are attached to one PCIe MiniCard socket. Either micro-SIM card A or micro-SIM card B can be connected to the PCI Express Mini Card. By default the PCI Express Mini card is connected to micro-SIM card A.

See [Chapter 5.1 SMBus Overview on page 69](#) for more information on how to switch the micro-SIM cards.

3.12.1 Connection of PCI Express Mini Cards

The PCI Express Mini card is connected using a 52-pin standard PCI Express Mini Card connector. The following standard signals are supported (signal directions according to PCI Express Mini Card standard):

Table 20. Pin assignment of 52-pin PCI Express Mini Card connector

Pin	Signal	Pin	Signal
51	Reserved	52	+3.3Vaux
49	Reserved	50	GND
47	Reserved	48	+1.5V
45	Reserved	46	LED_WPAN#
43	GND	44	LED_WLAN#
41	+3.3Vaux	42	LED_WWAN#
39	+3.3Vaux	40	GND
37	GND	38	USB_D+
35	GND	36	USB_D-
33	PETp0	34	GND
31	PETn0	32	SMB_DATA
29	GND	30	SMB_CLK
27	GND	28	+1.5V
25	PERp0	26	GND
23	PERn0	24	+3.3Vaux
21	GND	22	PERST#
19	Reserved	20	W_DISABLE#
17	Reserved	18	GND
Mechanical Key			
15	GND	16	UIM_VPP
13	REFCLK+	14	UIM_RST
11	REFCLK-	12	UIM_CLK
9	GND	10	UIM_DATA
7	CLKREQ#	8	UIM_PWR
5	Reserved	6	1.5V
3	Reserved	4	GND
1	WAKE#	2	+3.3Vaux

Table 21. Signal mnemonics of 52-pin PCI Express Mini Card connector

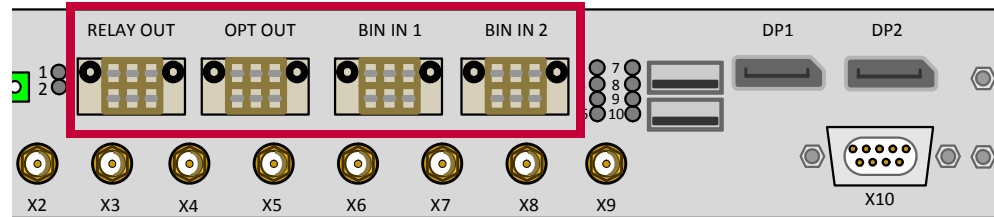
	Signal	Direction	Function
Power	GND	-	Ground
	+3.3Vaux	out	3.3V source
	1.5V	out	1.5V source
SIM card	UIM_PWR	in	microSIM card power
	UIM_DATA	in/out	microSIM card data
	UIM_CLK	in	microSIM card clock
	UIM_RST	in	microSIM card reset
	UIM_VPP	in	not connected
PCI Express	REFCLK-/REFCLK+	out	PCI Express differential reference clock
	PERn0/PERp0	in	PCI Express receive signals
	PETn0/PETp0	out	PCI Express transmit signals
Auxiliary Signals	CLKREQ#	in	Clock request
	PERST#	out	Reset for the Mini Card
	W_DISABLE#	out	Wireless disable
	WAKE#	in	Wake signal
	SMB_CLK	out	System management bus clock
	SMB_DATA	in/out	System management bus data
USB	USB_D-	in/out	USB line
	USB_D+	in/out	USB line
Communications - specific signals	LED_WWAN#	in	not connected
	LED_WLAN#	in	not connected
	LED_WPAN#	in	not connected

Please refer to the PCI Express Mini Card Specification for further details. See [Chapter 9.1 Literature and Web Resources on page 152](#).

3.13 6-Pin Headers

The relay outputs, the binary inputs, the IBIS signals, the odometer input and the photocoupler signals are available on four 6-pin PCB headers at the BL50W front.

Figure 21. Spring cage terminal blocks



Connector type:

- 6-pin header (printed circuit board connector), 3.5 mm, Phoenix Contact DMC 1,5/ 3-G1F-3,5-LR P20THR - 1787027

Mating connector:

- 6-pin plug (printed circuit board connector), e.g. Phoenix Contact DFMC 1,5/ 3-ST-3,5-LR - 1790496

Table 22. Pin assignment of relay out connector

	5	/ REL2_P	3	REL1_N	1	REL1_P
	6	REL2_N	4	REL2_P	2	/ REL1_P

Table 23. Pin assignment of opt out connector

	5	OPT[2]_P	3	OPT[1]_P	1	Ref. Voltage
	6	OPT[2]_N	4	OPT[1]_N	2	Ref. Ground

Note: Pin 2 (Ref. Ground) always has to be activated to ensure that all interfaces on the 6-pin headers work properly.

Table 24. Pin assignment of binary I/O connector 1

	5	BIN5	3	BIN3	1	BIN1
	6	BIN6	4	BIN4	2	BIN2

Table 25. Pin assignment of binary I/O connector 2

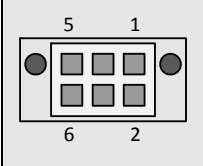
	5	Binary input 7	3	IBIS_RX_P	1	IBIS_TX_P
	6	ODO_IN	4	IBIS_RX_N	2	IBIS_TX_N

Table 26. Signal mnemonics of spring cage terminal block

Signal	Direction	Description
/ REL1/2_P	out	NC (normally closed) contact
REL1/2_P	out	NO (normally open) contact
REL1/2_N	-	Common contact
Reference Ground	-	Reference ground for binary inputs
Reference Voltage	-	Reference voltage for binary inputs
OPT[1/2]_N/P	out	Photocoupler lines
BIN[1..7]	in	Binary input lines 1 to 7
IBIS_TX_N/P	out	Differential pair of IBIS transmit lines
IBIS_RX_N/P	in	Differential pair of IBIS receive lines
ODO_IN	in	Odometer input line

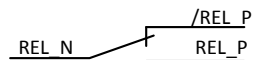
3.13.1 Relay Outputs

The BL50W supports two relay outputs with two-way contacts for switching with the following characteristics:

- Max. switching current 0..30 V: 2 A
- Max. switching current 30..72 V: 0.9 A
- Max. switching current 72..154 V: 0.3 A
- Max. switching voltage: 154 V
- Max. switching frequency: 1 Hz
- Minimum life time @ 1A, 30V, 20 cpm: 100.000

Figure 22. Relay switch - 3 lines (default state)

Relay (switch) – 3 lines



3.13.2 Binary Inputs

The BL50W supports seven isolated digital inputs. These inputs work with an input voltage range from 0 V up to 154 V. This input voltage range is independent of the power supply input voltage. The BL50W works with a maximum input signal frequency of 10 Hz.

The input's switching level is 40% ($\pm 5\%$) of the reference voltage. The required input current for high is $> 5\text{mA}$. The required input current for low is $< 1\text{mA}$.

3.13.3 IBIS

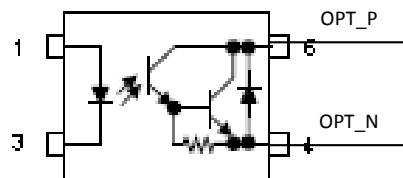
The BL50W provides one isolated IBIS slave interface. The IBIS (Integrated Board Information System) interface is a serial interface which has been established as a standard in the transportation sector for the controlling of display modules. It supports a baud rate of up to 19.2 kbaud.

3.13.4 Photocouplers

The BL50W provides 2 photocouplers used as shutters (see [Figure 23, Photocouplers \(shutters\) - 2 lines](#)). The shutters are open by default.

- Max switching voltage: 154 V
- Max. current: 120 mA (switching and continuous)

Figure 23. Photocouplers (shutters) - 2 lines



3.13.5 Odometer Input

The BL50W provides one isolated odometer input for GNSS. This input counts odometer pulses of a maximum frequency of 2 kHz. The value of the counter is read by an IP core (16Z082_IMPULSE) in the FPGA inside the BL50W.



A driver for the IP core is provided on [MEN's website](#)

The BL50W supports input voltage levels of up to 154 V.

Table 27. Odometer switching voltage levels

PD1 (75k) EN/DIS	PD2 (150k) EN/DIS	High Level	Low Level
DIS	DIS	4.2 V	1.5 V
DIS	EN	8.4 V	3.0 V
EN	DIS	12.6 V	4.5 V
EN	EN	16.8 V	6 V

The odometer default state does not change between power off and power on state. After a power cycle the odometer input enters its default state and will not remember its last value.

A reset does not change the state of the odometer input. During and after a reset the odometer input will hold its state.

Table 28. Odometer default / reset states

	PD1 (75k) EN/DIS	PD2 (150k) EN/DIS	PU EN/DIS	Sink 5mA EN/DIS
During Reset	Last value	Last value	Last value	Last value
After Reset	Last value	Last value	Last value	Last value
During power cycle	DIS	DIS	DIS	EN
After power cycle	DIS	DIS	DIS	EN

3.14 GNSS

The BL50W is equipped with the Telit Jupiter SL869 GNSS receiver module with 32 channels. The module supports GPS, Glonass and Galileo.

The GNSS signals are received via a U.FL antenna connector which has to be connected to an external antenna.

The BL50W itself has no antenna. You need to select and connect an antenna suitable for your application. The accuracy is mainly dependent on antenna quality.

Please note that MEN does not supply antennas with the BL50W, since the choice of a suitable antenna depends on your application. The BL50W supports active and passive antennas. An antenna supply voltage of 5V is provided.

The GNSS receiver converts the data received from the antenna to data packets. The time mark signal is readable by application software. The data transfer rate is configurable.

The GNSS module is accessed via a FTDI2UART chip. Under Linux the device port is probably ttyUSB0. The baudrate is 115200 8N1.

The GNSS's power supply can be reset via GPIO. See [Chapter 5.1 SMBus Overview](#).

As an option, the BL50W's GNSS supports dead reckoning. The box PC is equipped with a 3-axis gyrosensor and provides the possibility to connect an odometer to the GNSS module (see [Chapter 5 Controlling the System via SMBus on page 69](#)).

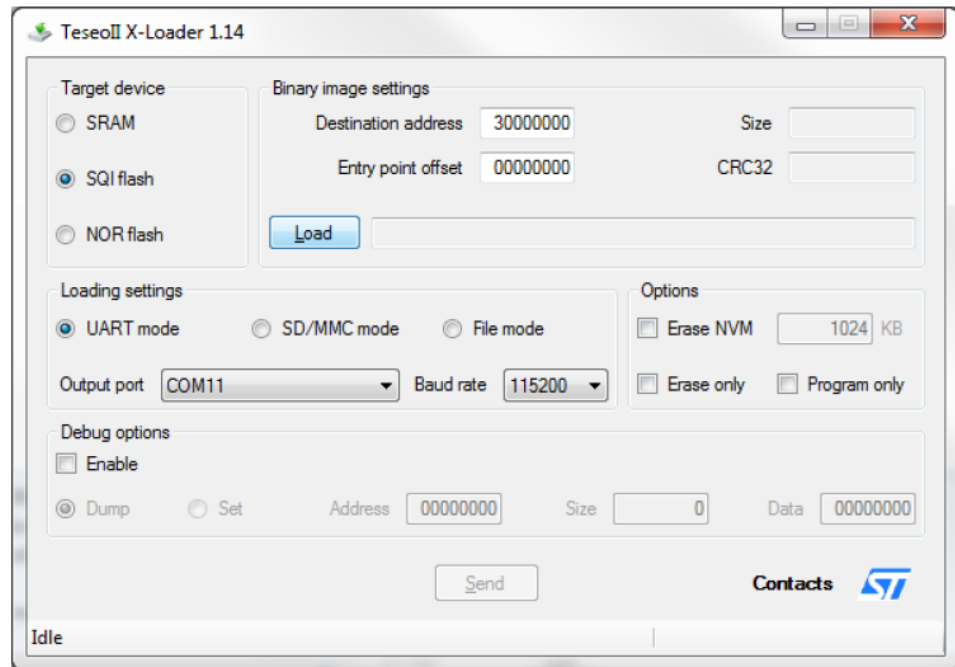
3.14.1 Updating the GNSS Module's Firmware

It is possible to update the GNSS receiver firmware. For this, the GNSS sensor voltage has to be switched off and the programming mode be enabled. This can be done using two control signals which can be accessed via the SMBus and an I/O expander. See [Chapter 5.1 SMBus Overview on page 69](#) for more information.

Please contact the [MEN sales team](#) for further information.

Carry out the following steps to update the GNSS module's firmware;

- Remove all power to the module by setting the GPS_PWR_EN bit to '0'.
- Enter the programming mode by setting #GPS_FLASH to '0'.
- Apply the power by setting the GPS_PWR_EN bit to '1'.
- Launch the (Telit) TESEOII X-Loader and set the selections as shown in the following figure.

Figure 24. TESEOII X-Loader settings

- ☑ Click on the Load button then locate and select the provided software by Telit.
- ☑ After selecting the correct Output port for the connected receiver (Look under Device Manager for possible COM connection), click on Send to program the device.
- ☑ Upon successful completion of re-flashing, remove main power to the module for a minimum of 10 seconds by setting the GPS_PWR_EN bit to '0'.
- ☑ Leave the programming mode by setting the #GPS_FLASH bit to '1'.
- ☑ Apply the power by setting the GPS_PWR_EN bit to '1'.
- ☑ Verify that the SL869 has returned to the normal operating state.

See [Chapter 8.1.1 GNSS on page 81](#) for detailed information on the GNSS module.

4 Organization of the Board

4.1 SMBus Devices

The controller hub of the BL50W has two SMB controllers. The devices responsible for board supervision and hardware control (SPD, BC) are connected to SMB0. The devices related to information and board functions (e.g. EEPROMs, sensors, GPIO controllers) are connected to SMB1.

Table 29. SMBus address of EEPROM

Address	Function
0xAC	EEPROM, includes system information e.g. serial number and the date of production.

Table 30. SMBus devices

SMBus	8-Bit-Address	7-Bit-Address	Function
0	0xA0	0x50	SPD EEPROM
0	0xA2	0x51	SPD EEPROM for stacked memory
0	0x9A	0x4D	Board Controller (PIC)
0	0x64	0x32	External real-time clock ERTC
1	0xAE	0x57	Board information EEPROM with thermal sensor
1	0x3E	0x1F	Thermal sensor of the EEPROM
1	0xE0	0x70	FPGA for SATA SGPIO

5 Controlling the System via SMBus

5.1 SMBus Overview

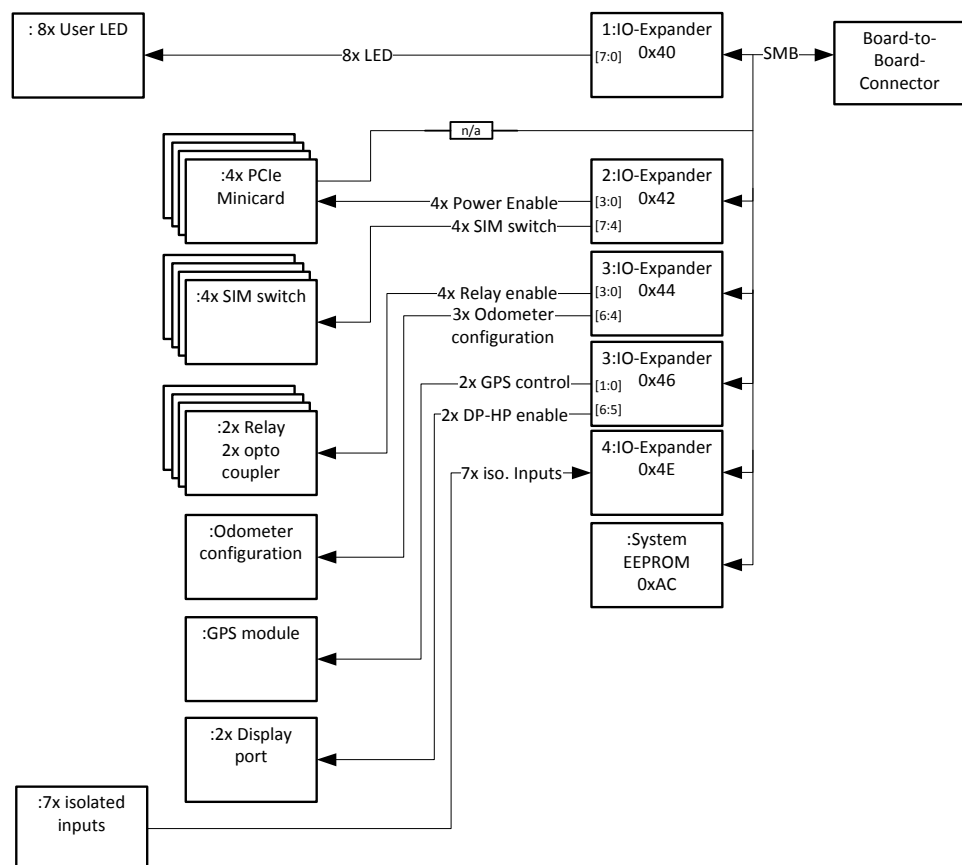
The PCI Express Mini Cards, the microSIM cards, the relays, the photocouplers, the odometer, the GNSS module and the binary inputs can all be controlled using the SMBus and five I/O expanders.



MEN offers a driver for accessing the SMBus. See [MEN's website](#).

Five I/O expanders and a system EEPROM are connected to the SMBus.

Figure 25. SMBus overview



5.2 SMBus Register Description

The SMB address is compliant to the Windows nomenclature. This means that the last bit which is used as the read/write bit is added with the value '0' to the address.

Under Linux a 0 is added at the beginning of the address so that all consecutive address bits are moved one bit to the right.

Example

Binary address of the I/O-Expander with hard coded offset 0b 0100 and chosen address bits:

- 0b 0100 (A2)(A1)(A0)(R/W)

With address bits:

- 0b 0100 010 (R/W)

Windows address:

- (R/W) = 0 -> 0b 0100 0100 -> 0x44

Linux address:

- 0b 0010 0010 -> 0x22

Table 31. SMB address space

Address	Function
0xAC	EEPROM, includes system information e.g. serial number and the date of production.
0x4E	I/O Expander, used to read back the isolated inputs
0x46	I/O Expander, used to control the GNSS module and ignition status.
0x44	I/O Expander, used to control the odometer interface and the relay outputs
0x42	I/O Expander, used to power cycle the PCIe Mini Cards and to switch between the connected SIM cards.
0x40	I/O Expander, used to control the user LEDs at the front panel.

Binary Input Register (0x4E)**Table 32. SMB 0x4E**

Bit	Direction	Signal	Default Value	Description	Group
7	In	BIN_IN[7]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
6	In	BIN_IN[6]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
5	In	BIN_IN[5]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
4	In	BIN_IN[4]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
3	In	BIN_IN[3]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
2	In	BIN_IN[2]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
1	In	BIN_IN[1]	-	Value of the connected isolated input	Isolated input
				0 = Input is active 1 = Input is not active (default value)	
0	In	WDOG_EN	1	Signal to reset the watchdog	System
				0 = Setting this bit to '0' generates a falling edge. The falling edge results in a reset of the watchdog. 1 = For every watchdog reset the bit must be set in sequence '1' -> '0' to generate a falling edge.	

GNSS Module Register (0x46)**Table 33. SMB 0x46**

Bit	Direction	Signal	Default value	Description	Group
7	In	IGNITION_ISO	-	Bit to read back the ignition status.. 0 = Setting this bit to '0' results in the same behavior as deactivating the external ignition signal (power button event, starting the watchdog timer) 1 = If the bit is '1' the ignition signal is active.	System
6	Out	DPB_HP_EN	1	Bit to enable the optional pull up at the Display Port hot plug signal. No changes should be made here.	DisplayPort
5	Out	DP1_HP_EN	1	Bit to enable the optional pull up at the Display Port hot plug signal. No changes should be made here.	DisplayPort
4			1	Not used	
3			1	Not used	
2			1	Not used	
1	Out	#GPS_FLASH	1	Controls the operation mode of the GPS module. 0 = By setting the bit to '0' the GPS module is entering the firmware flash mode. 1 = The default value is '1' and the GPS module is in normal operation mode.	GPS
0	Out	GPS_PWR_EN	1	Signal to control the GPS module power supply. 0 = By setting the bit to '0' the power supply of the GPS module is switched off. 1 = The default value is '1' and the GPS module is powered.	GPS

Odometer, Photocoupler, Relay Signals (0x44)**Table 34. SMB 0x44**

Bit	Direction	Signal	Default Value	Description	Group
7	-	-	1	Not used	-
6	Out	ODO_PU_EN	1	Enable signal for the optional pull up of the odometer interface. 0 = By setting the bit to '0' the corresponding pull up/down resistor is activated at the odometer interface. 1 = The default value is '1' and the pull up/down is not active.	Odometer
5	Out	ODO_PD150K_EN	1	Enable signal for the optional 150k pull down of the odometer interface. 0 = By setting the bit to '0' the corresponding pull up/down resistor is activated at the odometer interface. 1 = The default value is '1' and the pull up/down is not active.	Odometer
4	Out	ODO_PD75K_EN	1	Enable signal for the optional 75k pull down of the odometer interface. 0 = By setting the bit to '0' the corresponding pull up/down resistor is activated at the odometer interface. 1 = The default value is '1' and the pull up/down is not active.	Odometer
3	Out	OPT_EN[2]	1	Enable signal for the second (2) output photocoupler. 0 = By setting the bit to '0' the corresponding coupler is closed. 1 = The default value is '1' and the corresponding coupler is open.	Coupler output
2	Out	OPT_EN[1]	1	Enable signal for the first (1) output photocoupler. 0 = By setting the bit to '0' the corresponding coupler is closed. 1 = The default value is '1' and the corresponding coupler is open.	Coupler output
1	Out	REL_EN[2]	1	Enable signal of the second (2) relay, the relay is used as a switcher.	Relay output

Bit	Direction	Signal	Default Value	Description	Group
				0 = By setting the bit to '0' the corresponding relay is closed or switched (State REL_P). 1 = The default value is '1' and the corresponding relay is open/ not switched (State /REL_P).	
0	Out	REL_EN[1]	1	Enable signal of the first (1) relay, the relay is used as a switcher. 0 = By setting the bit to '0' the corresponding relay is closed or switched (State REL_P). 1 = The default value is '1' and the corresponding relay is open/ not switched (State /REL_P).	Relay output

SIM Card, PCIe Mini Card Power Signals (0x42)

Table 35. SMB 0x42

Bit	Direction	Signal	Default Value	Description	Group
7	Out	SIMA_SW	1	Signal to switch between the primary and secondary SIM card of the PCIe Mini Card A. 0 = The secondary SIM card is connected to the PCIe Mini Card 1 = The primary SIM card is connected to the PCIe Mini Card (default value).	PCIe Mini Card
6	Out	SIMB_SW	1	Signal to switch between the primary and secondary SIM card of the PCIe Mini Card B. 0 = The secondary SIM card is connected to the PCIe Mini Card 1 = The primary SIM card is connected to the PCIe Mini Card (default value).	PCIe Mini Card
5	Out	SIMC_SW	1	Signal to switch between the primary and secondary SIM card of the PCIe Mini Card C. 0 = The secondary SIM card is connected to the PCIe Mini Card 1 = The primary SIM card is connected to the PCIe Mini Card (default value).	PCIe Mini Card
4	Out	SIMD_SW	1	Signal to switch between the primary and secondary SIM card of the PCIe Mini Card D. 0 = The secondary SIM card is connected to the PCIe Mini Card 1 = The primary SIM card is connected to the PCIe Mini Card (default value).	PCIe Mini Card
3	Out	MINI_A_PWR_EN	1	The bit is used to control the PCIe Mini Card A's power. 0 = The power of the PCIe Mini Card is disabled. 1 = The power of the PCIe Mini Card is enabled (default value).	PCIe Mini Card
2	Out	MINI_B_PWR_EN	1	The bit is used to control the PCIe Mini Card B's power. 0 = The power of the PCIe Mini Card is disabled.	PCIe Mini Card

Bit	Direction	Signal	Default Value	Description	Group
				1 =	The power of the PCIe Mini Card is enabled (default value).
1	Out	MINI_C_PWR_EN	1	The bit is used to control the PCIe Mini Card C's power.	PCIe Mini Card
				0 =	The power of the PCIe Mini Card is disabled.
				1 =	The power of the PCIe Mini Card is enabled (default value).
0	Out	MINI_D_PWR_EN	1	The bit is used to control the PCIe Mini Card D's power.	PCIe Mini Card
				0 =	The power of the PCIe Mini Card is disabled.
				1 =	The power of the PCIe Mini Card is enabled (default value).

User LED Signals (0x40)

Table 36. SMB 0x40

Bit	Direction	Signal	Default Value	Description	Group
7	Out	USR_LED[10]	1	Enable bit for the corresponding user LED.	USR_LED
				0 =	The LED is enabled/on.
				1 =	The LED is disabled/off (default value).
6	Out	USR_LED[9]	1	Enable bit for the corresponding user LED.	USR_LED
				0 =	The LED is enabled/on.
				1 =	The LED is disabled/off (default value).
5	Out	USR_LED[8]	1	Enable bit for the corresponding user LED.	USR_LED
				0 =	The LED is enabled/on.
				1 =	The LED is disabled/off (default value).
4	Out	USR_LED[7]	1	Enable bit for the corresponding user LED.	USR_LED
				0 =	The LED is enabled/on.
				1 =	The LED is disabled/off (default value).
3	Out	USR_LED[6]	1	Enable bit for the corresponding user LED.	USR_LED

Bit	Direction	Signal	Default Value	Description	Group
				0 = The LED is enabled/on. 1 = The LED is disabled/off (default value).	
2	Out	USR_LED[5]	1	Enable bit for the corresponding user LED.	USR_LED
				0 = The LED is enabled/on. 1 = The LED is disabled/off (default value).	
1	Out	USR_LED[4]	1	Enable bit for the corresponding user LED.	USR_LED
				0 = The LED is enabled/on. 1 = The LED is disabled/off (default value).	
0	Out	USR_LED[3]	1	Enable bit for the corresponding user LED.	USR_LED
				0 = The LED is enabled/on. 1 = The LED is disabled/off (default value).	

6 Using the BL50W with MDIS5 Software

This chapter gives specific information on how to use the BL50W with the MDIS5 software.



For details about installation and usage of MDIS5 please refer to the respective MDIS5 user manual. See MEN's [website](#) for detailed information and documentation of MDIS5.

The UART, CAN and GPIO interfaces of the Box PC are controlled using IP cores in an FPGA. See the following [Figure 37, Chameleon table](#) for a list of the IP cores.

Table 37. Chameleon table

Name	Device	Variant	Revision	Interrupt	Group	Instance	BAR	Offset	Size
16Z125_UART	125	0	11	6	0	4	0	220	8
16Z125_UART	125	0	11	4	0	0	0	3F8	8
16Z125_UART	125	0	11	3	0	1	0	2F8	8
16Z125_UART	125	0	11	7	0	2	0	3E8	8
16Z125_UART	125	0	11	5	0	3	0	2E8	8
16Z082_IMPULSE	82	0	2	3F	0	0	1	800	20
16Z034_GPIO	34	0	A	B	0	0	1	E200	20
16Z034_GPIO	34	0	A	B	0	1	1	E240	20
16Z037_GPIO	37	1	1	B	0	0	1	E260	20
16Z126_FLASH	126	0	6	3F	0	0	1	E280	20
16Z029_CAN	29	1	12	B	0	0	1	E400	100
16Z076_QSPI	76	0	7	B	0	0	1	E800	800
16Z029_CAN	29	1	12	B	0	1	1	F000	100

6.1 GPIO Controller Instance 1

The GPIO controller instance 1 is used to activate or switch serial interfaces of the box PCs from UART to SGPIO. The functions are switched via multiplexers.

The default device name of the instance 1 of the GPIO controller 16z034- is "gpio_2" for Linux and "z17_2" for Windows.

Table 38. Functions of GPIO controller instance 1

Bit Position	Output-Function (GPO)	Input-Function (PSR)	Default Setting
5	mux_if2	-	GPO(5):drive 0
6	mux_if0	-	GPO(6):drive 0
7	mux_if1	-	GPO(7):drive 0

Interface Multiplexer (mux_if0)

The interface multiplexer 0 is used to change the serial interface 5 (X12) from UART (default setting) to SGPIO (required if you want to use the SA15 SA-Adapter in this slot).

Table 39. Interface Multiplexer 0 truth table:

Interface	Function	mux_if0
SA0	UART0	0
	SGPIO	1

The interface is switched using the following commands:

For Windows:

```
z17_io z17_2 -p=6 -s=1 -h
```

For Linux:

```
z17_io gpio_2 -p=6 -s=1 -h
```

Interface Multiplexer (mux_if1)

In BL50W models where CAN is already activated at serial interface 3 (X10), the interface multiplexer 1 is used to activate the IBIS interface on the BIN IN 2 connector. In BL50W models where UART is used at serial interface 3 (X10) this UART interface has to be activated here. IBIS cannot be used in this case.

Table 40. Interface Multiplexer 1 truth table

Interface	Function	mux_if1
GPIO[7:1]	No functionality	0
	Serial Interface 3 (X10) is active	1

The interface is activated using the following commands:

For Windows:

```
z17_io z17_2 -p=7 -s=1 -h
```

For Linux:

```
z17_io gpio_2 -p=7 -s=1 -h
```

7 Maintenance

7.1 Cleaning the System

The system should be cleaned once a year. Clean the bottom and top plate, remove dirt and dust from air ventilation holes. Light dirt can be removed with a dry cloth. Persistent dirt should only be removed with a mild detergent and a soft cloth.

Take care that no liquid gets inside the system.

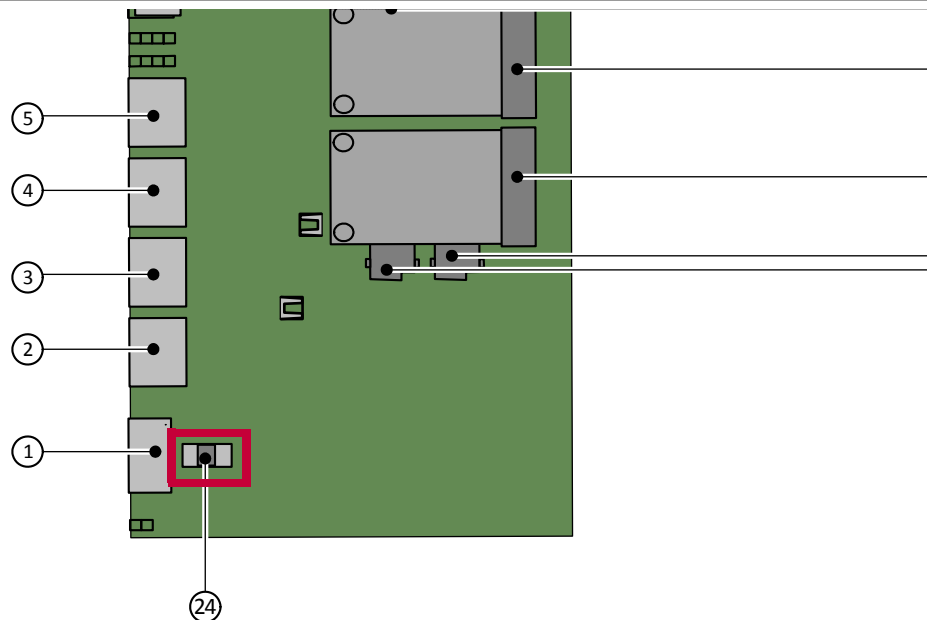
7.2 Fuse Protection



The DC/DC converter inside the Power Supply Unit is protected by a fuse. **This fuse is not intended to be exchanged by the customer. Your warranty for the Power Supply Unit will cease if you exchange the fuse on your own.** Please send your unit to MEN for repair if a fuse blows.

- Current rating: 5 A
- Voltage rating: 350 VDC
- Type: Time-Lag (T)
- MEN part number: 5675-0011

Figure 26. Position of the fuse in the interior of the BL50W (highlighted in red)



8 Appendix

8.1 Literature and Web Resources



BL50W data sheet with up-to-date information and documentation:
www.men.de/products/09BL50W.htm

8.1.1 GNSS



Jupiter SL869 EVK GNSS module
[Jupiter SL869 EVK User Guide](#)

8.1.2 Accelerometer / Magnetometer LSM303DLM



Manufacturer's product page for LSM303DLM
STMicroelectronics
http://www.st.com/web/en/catalog/sense_power/FM89/SC1449

8.1.3 CAN Bus



CAN in Automation e. V.
www.can-cia.de

8.1.4 DVI



Digital Visual Interface Revision 1.0
www.ddwg.org

8.1.5 Ethernet



- ANSI/IEEE 802.3-1996, Information Technology - Telecommunications and Information Exchange between Systems - Local and Metropolitan Area Networks - Specific Requirements - Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) Access Method and Physical Layer Specifications; 1996; IEEE
www.ieee.org
- Charles Spurgeon's Ethernet Web Site
Extensive information about Ethernet (IEEE 802.3) local area network (LAN) technology.
www.ethermanage.com/ethernet/
- InterOperability Laboratory, University of New Hampshire
This page covers general Ethernet technology.
www.iol.unh.edu/services/testing/ethernet/training/

8.1.6 HD Audio



Intel High Definition Audio:
www.intel.com/design/chipsets/hdaudio.htm

8.1.7 PCI Express Mini Card



PCI Express Mini Card Electromechanical Specification
Revision 1.2; October 26, 2007
PCI Special Interest Group
www.pcisig.com

8.1.8 USB



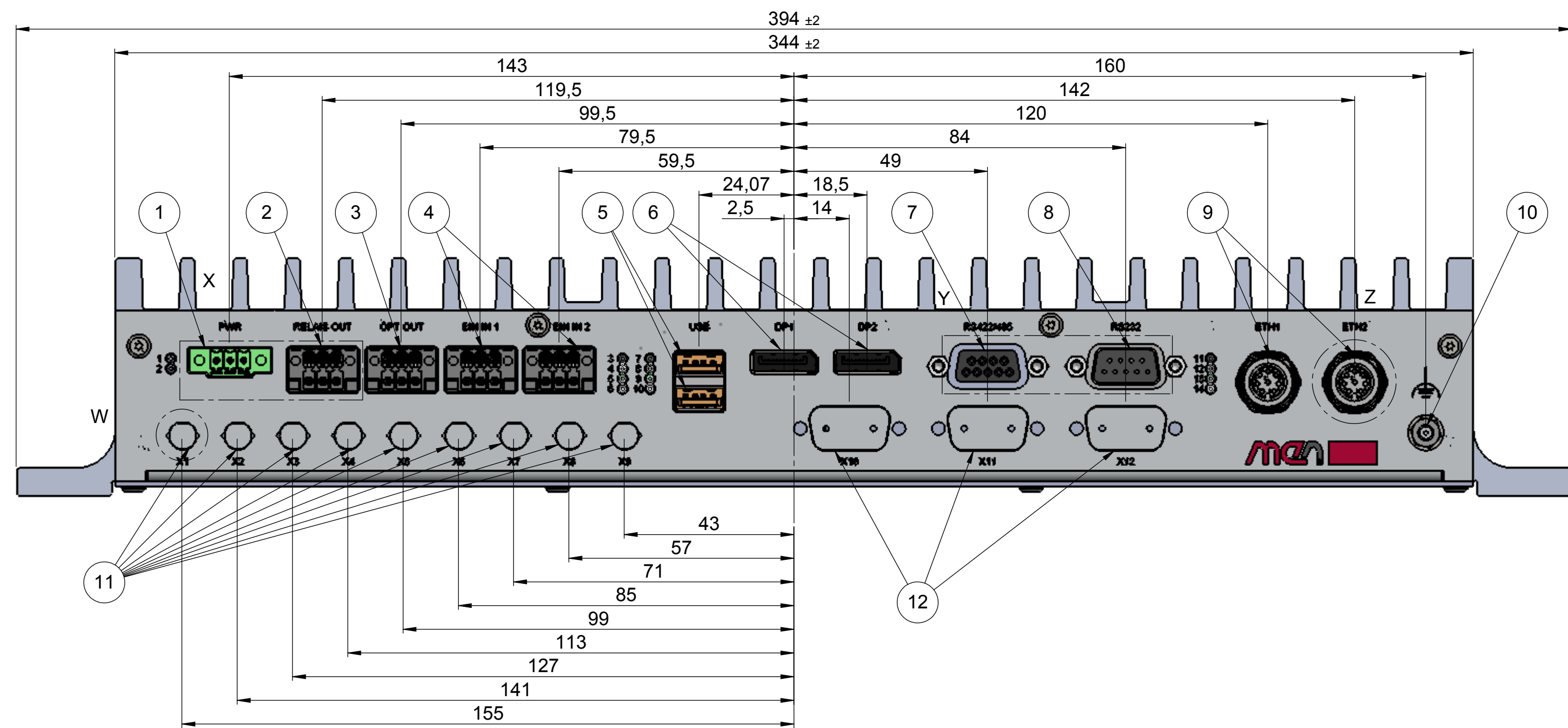
USB Implementers Forum, Inc.
www.usb.org

8.2 Dimensions of the BL50W Box PC

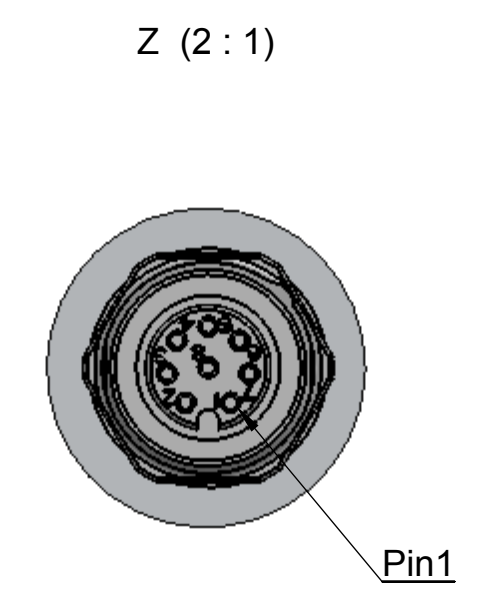
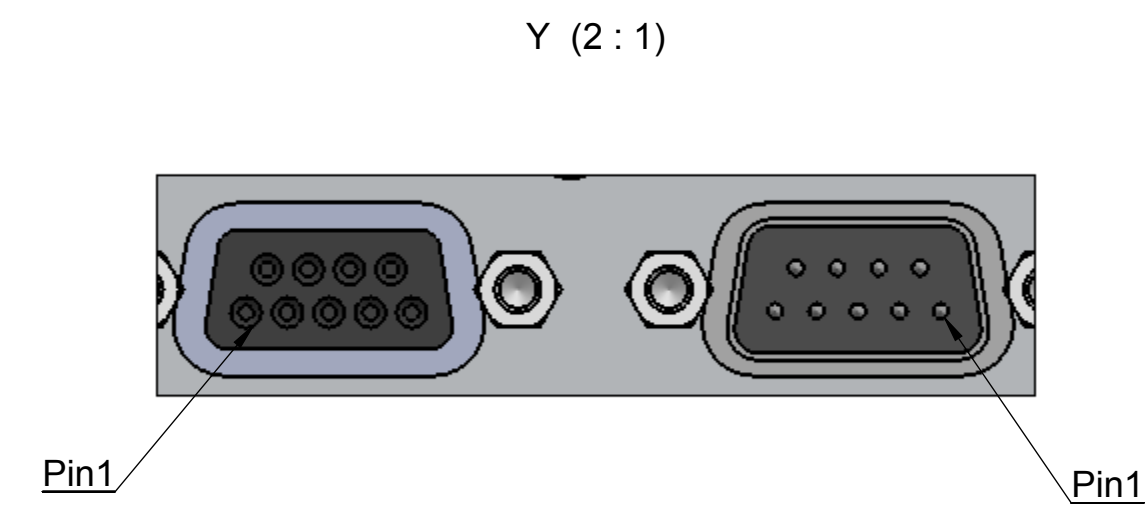
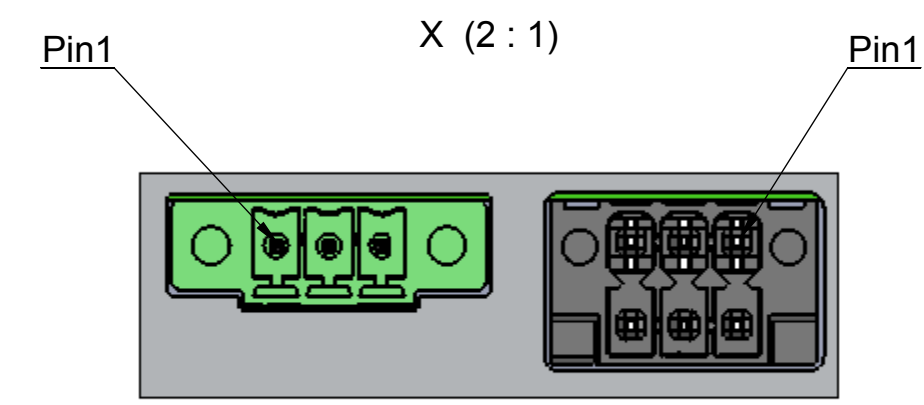
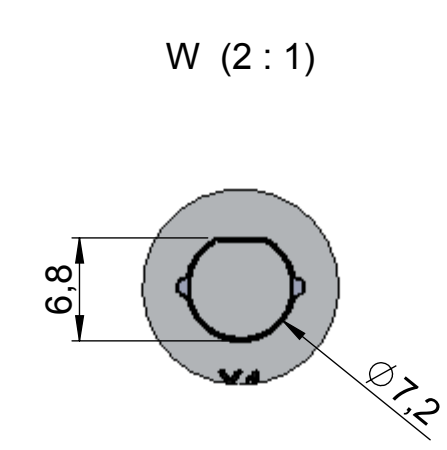
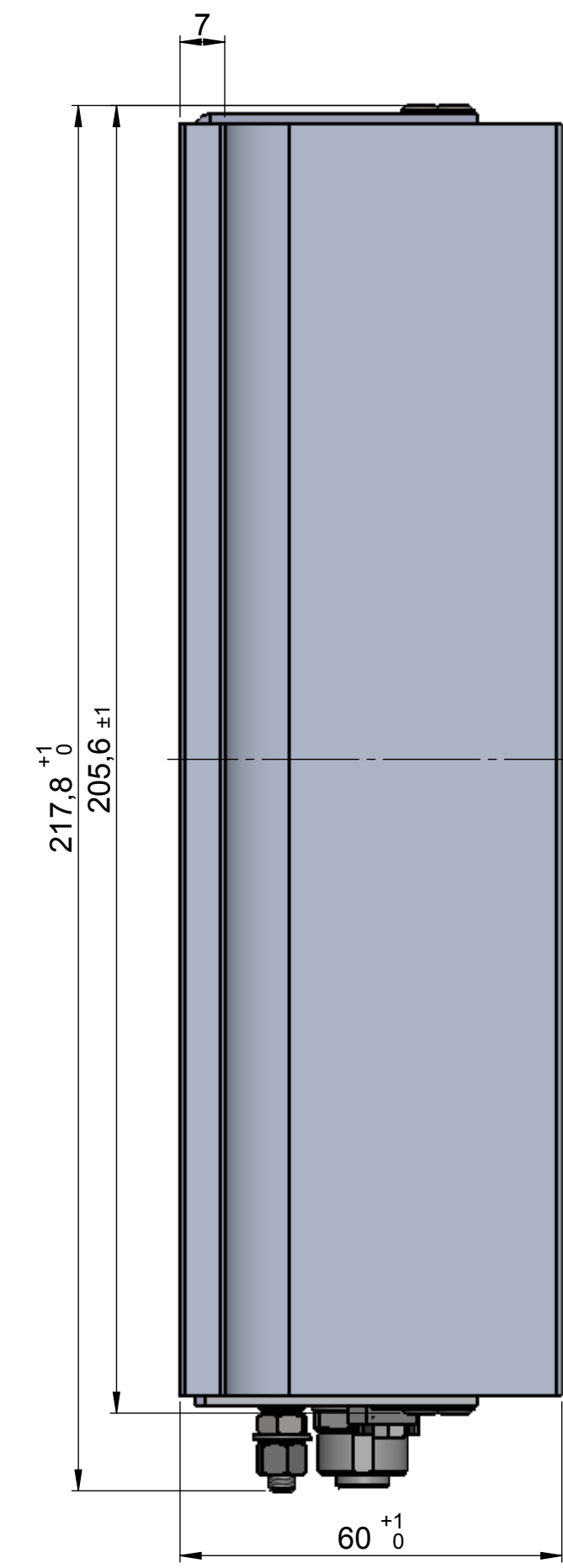
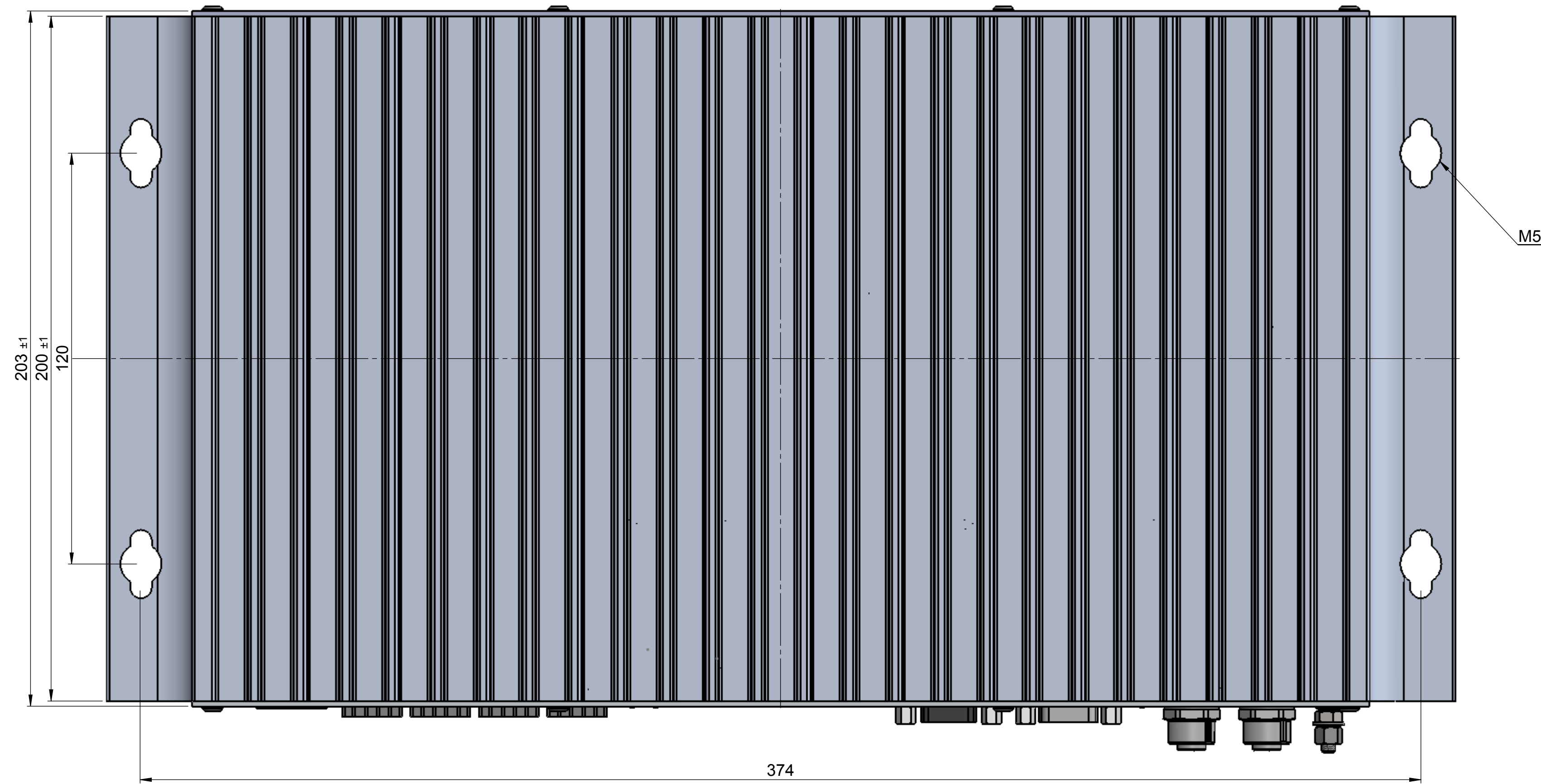
In the following you can find a drawing of the exact dimensions of the box PC.



You can find a detailed 3D drawing (PDF and STEP file) on the [BL50W pages](#) on the [MEN website](#).

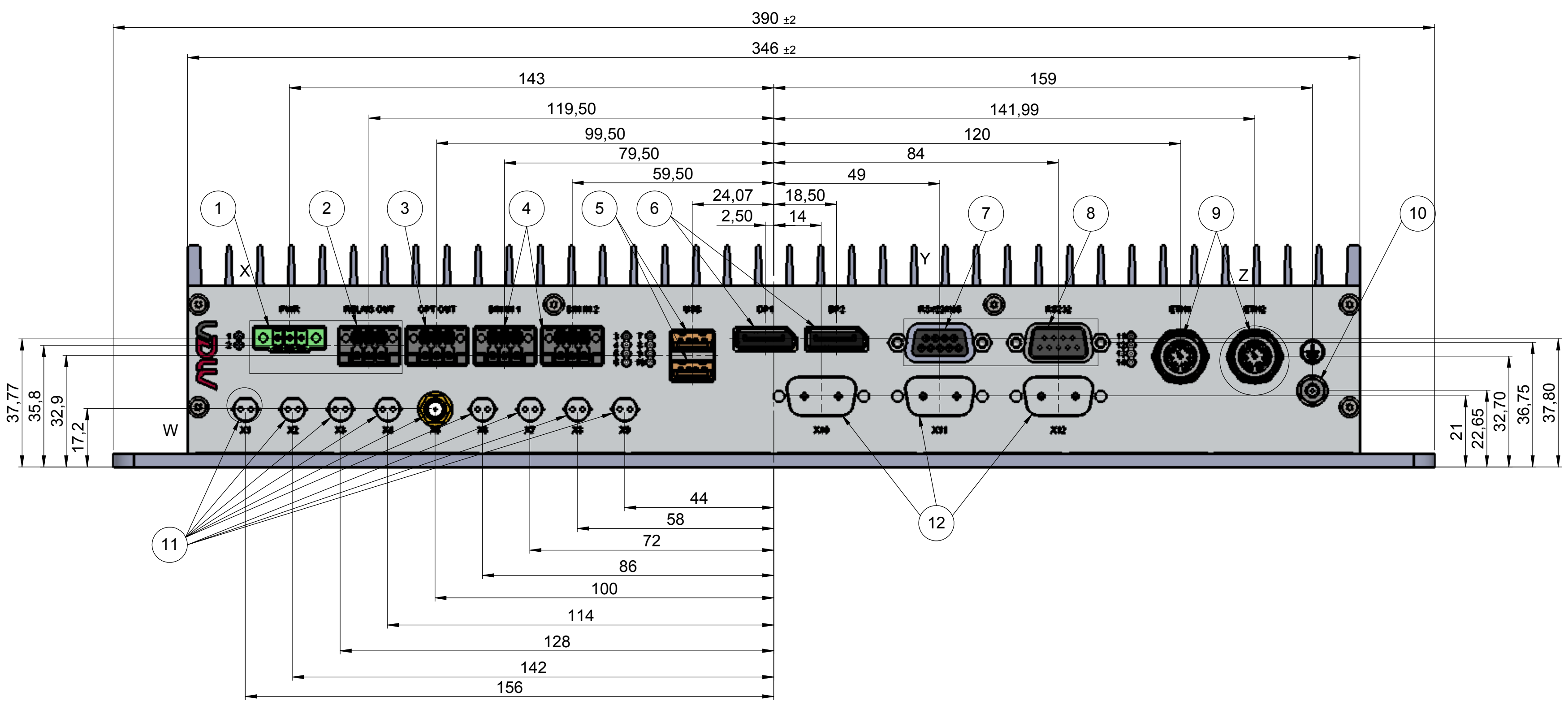


Position	Quantity	Function	Description	Mating connector
1	1	Power	Mini Combicon Plug Grid 3,5mm	Mini Cobicon Jack e.g. Phoenix: 1966101
2	1	Relais output	Dual Mini Combicon Plug	Dual Mini Combicon Jack e.g. Phoenix: 1790496
3	1	Optical output	Dual Mini Combicon Plug	Dual Mini Combicon Jack e.g. Phoenix: 1790496
4	1	Binary input	Dual Mini Combicon Plug	Dual Mini Combicon Jack e.g. Phoenix: 1790496
5	2	USB	USB Type A Jack	USB Type A Plug
6	2	Display	Display Port Jack	Display Port Plug e.g. Molex: 0687830001
7	1	RS422/485	DSub female 9-pin	Dsub male 9-pin
8	1	RS232	DSub male 9-pin	DSub female 9-pin
9	2	Ethernet	M12 Jack 8-pin A-coded	M12 Plug 8-pin e.g. Conec: SAL-12RS8-2/A1
10	1	Ground	M4 Bolt	Cable eye
11	9	Antenna	Pending on function	Pending on function
12	3	SA-Adapter	Pending on function	Pending on function

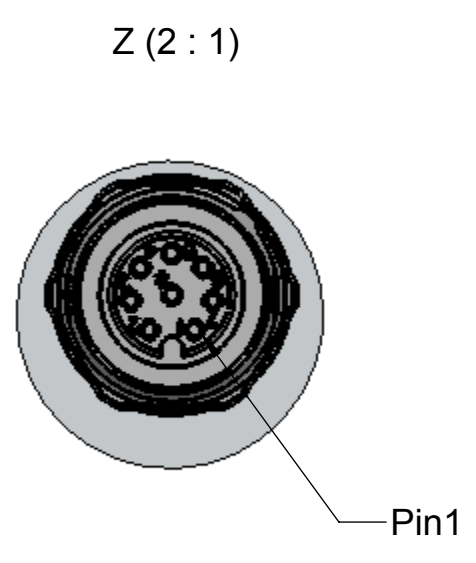
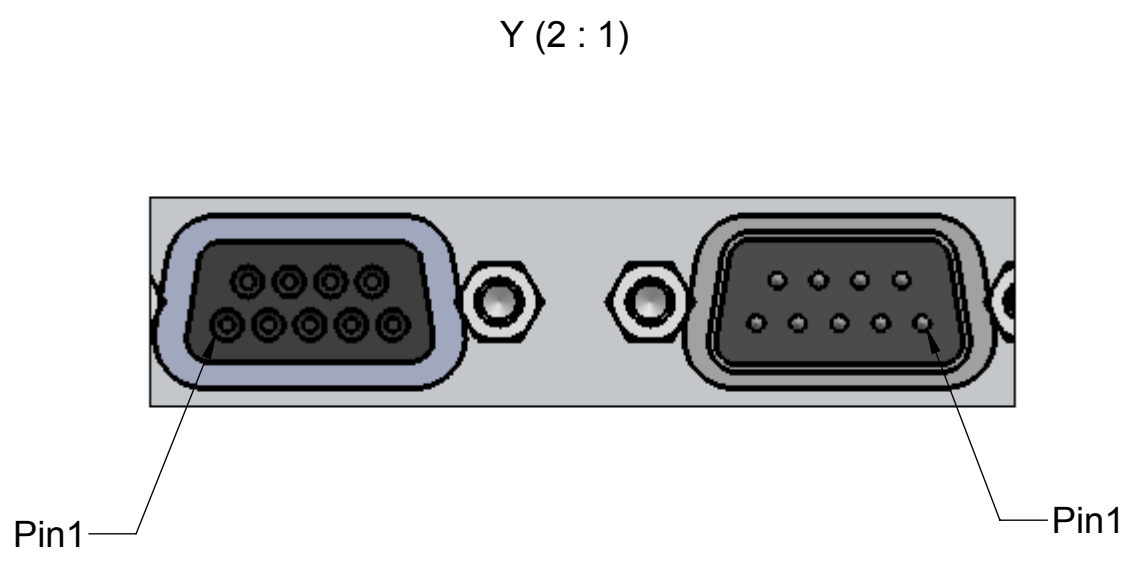
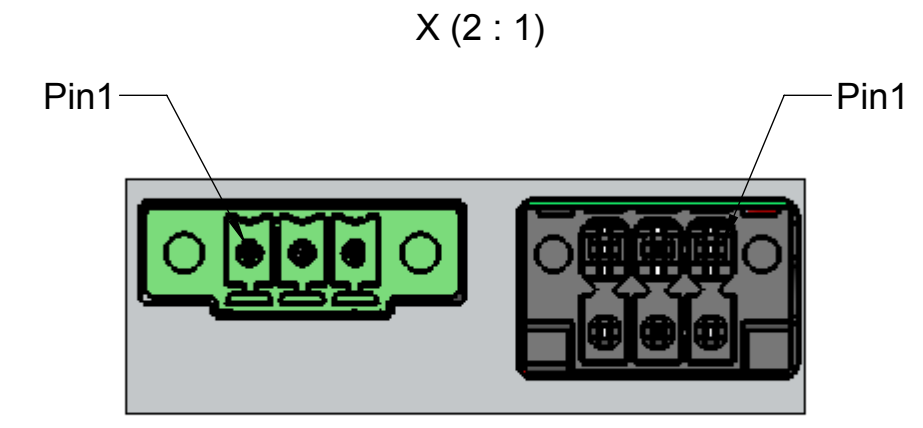
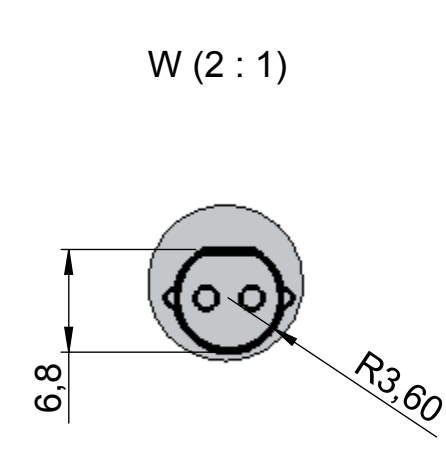
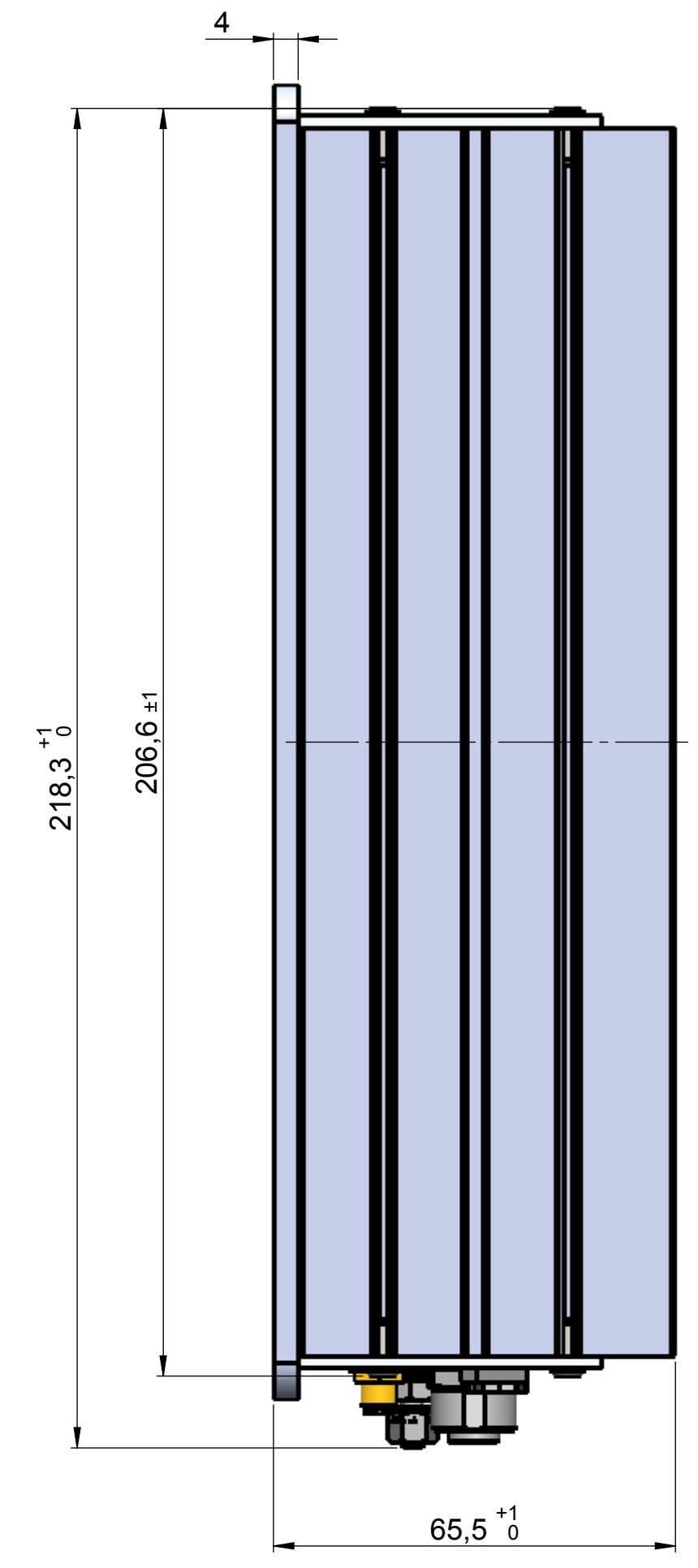
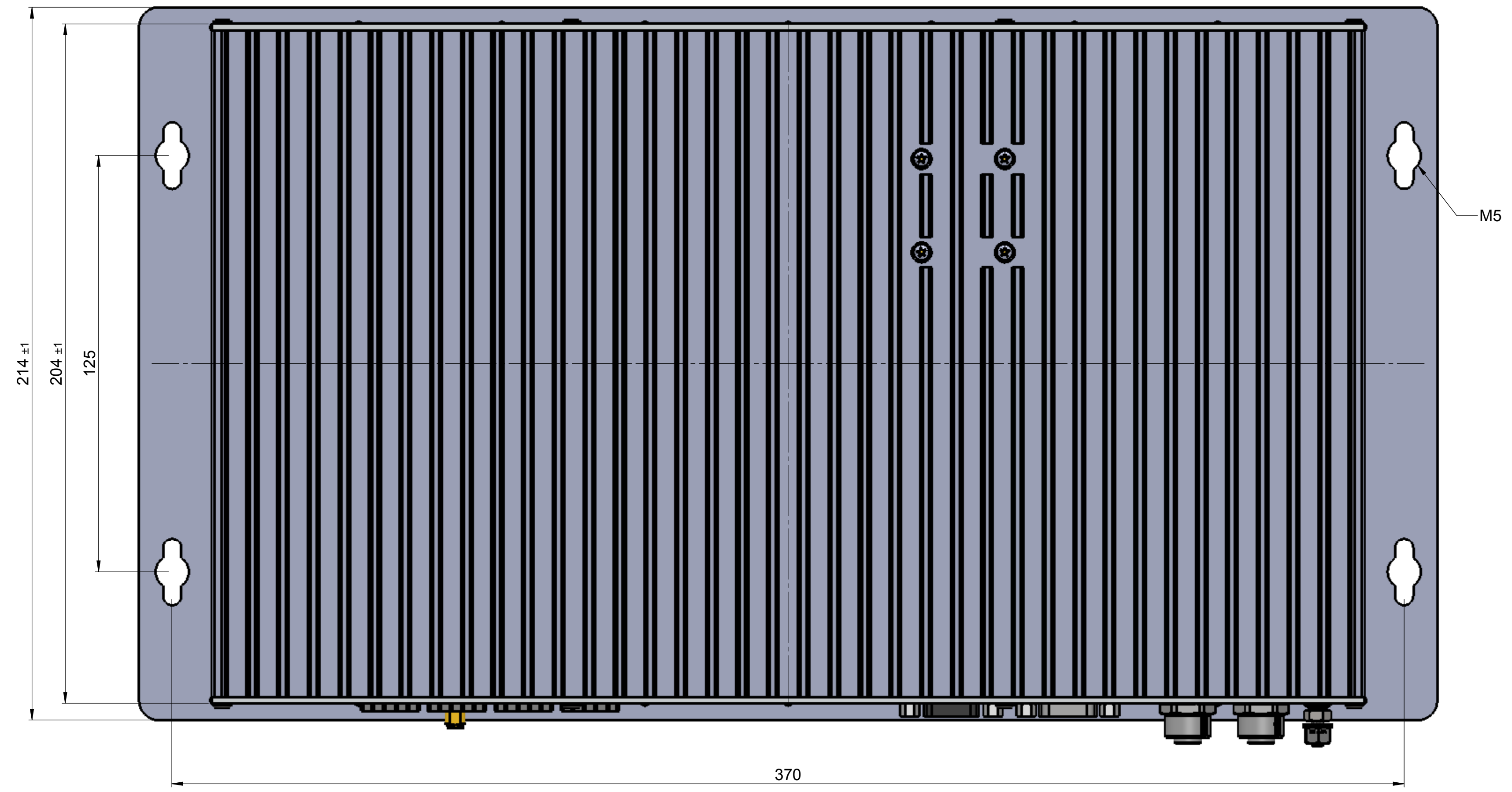


Manufacturer number: 09BL50W00
 Weight: max. 3,2kg
 Input voltage: 24VDC (nom.)
 Operating temperature: -40°C ... +70°C

2.0	J.Ultsch 2013-09-04	Pin 1 marking added
Rev.: Prepared/Date: Modification:		RoHS-compliant (2002/95/EC): yes
Responsible author: J.Ultsch		
Printed version FOR INFORMATION ONLY This is an electronic document. It has been digitally signed. Please see release form.		
Scale: 1:1	Tolerance: ---	DIN ISO 13715
Material: ---	Surface: Transparent passivated (electrically conductive)	DIN A1
Description: Space allocation drawing 09BL50W00_SAD (1.0)		Document No. 09BL50W00_SAD Project: 1302
Sheet: 1/1		Sheet: 1/1



Position	Quantity	Function	Description	Mating connector
1	1	Power	Mini Combicon Plug Grid 3,5mm	Mini Cobicon Jack e.g. Phoenix: 1966101
2	1	Relais output	Dual Mini Combicon Plug	Dual Mini Combicon Jack e.g. Phoenix: 1790496
3	1	Optical output	Dual Mini Combicon Plug	Dual Mini Combicon Jack e.g. Phoenix: 1790496
4	1	Binary input	Dual Mini Combicon Plug	Dual Mini Combicon Jack e.g. Phoenix: 1790496
5	2	USB	USB Type A Jack	USB Type A Plug
6	2	Display	Display Port Jack	Display Port Plug e.g. Molex: 0687830001
7	1	RS422/485	DSub female 9-pin	Dsub male 9-pin
8	1	RS232	DSub male 9-pin	DSub female 9-pin
9	2	Ethernet	M12 Jack 8-pin A-coded	M12 Plug 8-pin e.g. Conec: SAL-12RS8-2/A1
10	1	Ground	M4 Bolt	Cable eye
11	9	Antenna	Pending on function	Pending on function
12	3	SA-Adapter	Pending on function	Pending on function



Manufacturer number: 09BL70W00
 Weight: max. 3,2 kg
 Input voltage: 24VDC (nom.)
 Operating temperature: -40°C ... +70°C

1.0	sdietz	Initial version
Rev.:	2014-11-11	Prepared/Date:
Responsible author:	S.Dietz	Modification:
RoHS-compliant (2011/65/EU): yes		
Scale:	1:2	Tolerance: ---
Material:	---	Surface: ---
Description:		Document No.
Space allocation drawing		09BL70W00_SAD
09BL70W00_SAD (1.0)		DIN A1
		Pj.: 1422
		Sheet: 1 / 1