20F750-00 E1 – 2014-03-14

User Manual

3U CompactPCI® Fieldbus & RT Ethernet Interface Boards F750, F751, F752, F753



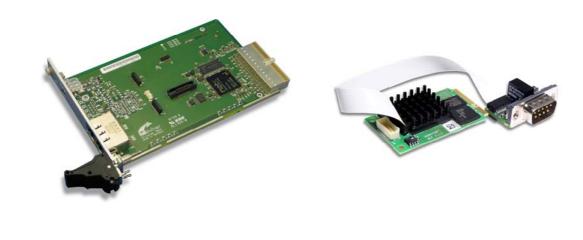


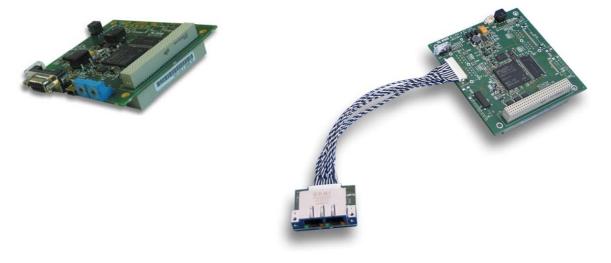


User Manual

PC Cards cifX Compact PCI (CIFX 80) Mini PCI (CIFX 90) Mini PCI Express (CIFX 90E) PCI-104 (CIFX 104C)

Installation, Operation and Hardware Description





Hilscher Gesellschaft für Systemautomation mbH

www.hilscher.com

DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public

Table of Contents

1	INTRODUCTION					
	1.1	About the User Manual				
		1.1.1 List of Revisions	.9			
		1.1.2 Notes on Hardware, Firmware, Software and Driver Versions1	0			
		1.1.3 Conventions in this Manual1	3			
		1.1.4 Used Terminology1	3			
	1.2	Contents of the Product DVD1	4			
		1.2.1 Installation Guide, Documentation Overview1	4			
		1.2.2 What's New1	4			
		1.2.3 Important Changes1	4			
		1.2.4 Device Description Files cifX1	7			
	1.3	Legal Notes1	8			
		1.3.1 Copyright1	8			
		1.3.2 Important Notes1	8			
		1.3.3 Exclusion of Liability1	9			
		1.3.4 Warranty1	9			
		1.3.5 Export Regulations	20			
		1.3.6 Registered Trademarks2	20			
		1.3.7 EtherCAT Disclaimer	21			
		1.3.8 Obligation to read and understand the Manual 2	21			
	1.4	Licenses2	:1			
		1.4.1 License Note about VARAN Client	21			
2	SAFET	ΓΥ2	2			
	2.1	General Note2	2			
	2.2	Intended Use2	22			
	2.3	Personnel Qualification				
	2.4	Safety Instructions to avoid Personal Injury2	23			
		2.4.1 Electrical Shock Hazard2	23			
	2.5	Safety Instructions to avoid Property Damage2	:4			
		2.5.1 Device Destruction by exceeding allowed Supply Voltage2	24			
		2.5.2 Device Destruction by exceeding allowed Signaling Voltage2	25			
		2.5.3 Electrostatically sensitive Devices	25			
	2.6	Labeling of Safety Messages2	:6			
		2.6.1 References Safety	26			
3	DESCF	RIPTIONS AND REQUIREMENTS2	7			
	3.1	Description2	27			
	3.2	PC Cards cifX with integrated Interfaces2				
		3.2.1 PC Cards Compact PCI CIFX 80-XX				
		3.2.2 PC Cards PCI-104: CIFX 104C-XX and CIFX 104C-XX-R				
	3.3	PC Cards cifX with AIFX Assembly Interfaces				
		,				

Tab	le of Co	ontents		3/191
		3.3.1	The Label "\F" in the Device Name	29
		3.3.2	PC Cards Mini PCI CIFX 90-XX\F	29
		3.3.3	PC Cards Mini PCI Express CIFX 90E-XX\F	30
		3.3.4	PC Cards cifX Mini PCI Express CIFX 90E-XX\ET\F	30
		3.3.5	PC Cards Mini PCI Express CIFX 90E-XX\MR\F	31
		3.3.6	PC Cards Mini PCI Express CIFX 90E-XX\MR\ET\F	32
		3.3.7	PC Cards PCI-104: CIFX 104C-XX\F and CIFX 104C-XX-R\F	33
		3.3.8	AIFX Assembly Interfaces	33
	3.4	The Fur	nction "DMA Mode"	34
	3.5	PC Car	ds cifX with additional MRAM	35
	3.6	System	Requirements	35
		3.6.1	Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104	35
		3.6.2	Notes on the Card Height - PC Card cifX Mini PCI and Mini PCIe	36
		3.6.3	Panel Cutout for Installing AIFX	37
		3.6.4	Power Supply and Host Interface	38
	3.7	Require	ements for Operation of the PC Cards cifX	
4	GET	TING STA	RTED	40
	4.1	Installat	tion and Configuration PC Card cifX	40
	4.2	Note on	n Exchange Service (Replacement Case)	43
	4.3	Notes for	or the Configuration of the Master Device	44
	4.4	Device	Names in SYCON.net	45
	4.5	Update	for Firmware, Driver and Software	46
F	הביע			47
5			VINGS	
	5.1		ds cifX Compact PCI	
		5.1.2		
		5.1.3		
		5.1.4	CIFX 80-DN	50
	5.2	PC Car	ds cifX Mini PCI and Mini PCI Express	
		5.2.1	CIFX 90-RE\F, CIFX 90E-RE\F and MR and/or ET Variants	
		5.2.2	CIFX 90-FB\F, CIFX 90E-FB\F and MR and/or ET Variants	52
	5.3	PC Car	ds cifX PCI-104	53
		5.3.1	CIFX 104C-RE, CIFX 104C-RE-R	53
		5.3.2	CIFX 104C-RE\F, CIFX 104C-RE-R\F	54
		5.3.3	CIFX 104C-DP, CIFX 104C-DP-R	55
		5.3.4	CIFX 104C-CO, CIFX 104C-CO-R	56
		5.3.5	CIFX 104C-DN, CIFX 104C-DN-R	57
		5.3.6	CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DN\F	58
		5.3.7	CIFX 104C-DP-R\F, CIFX 104C-CO-R\F, CIFX 104C-DN-R\F	58
	5.4	AIFX As	ssembly Interfaces	59
		5.4.1	Ethernet - AIFX-RE	59
		5.4.2	PROFIBUS - AIFX-DP	60
		5.4.3	CANopen - AIFX-CO	61
		5.4.4	DeviceNet - AIFX-DN	62

Tab	le of Cor	ntents		4/191		
		5.4.5	Diagnostic - AIFX-DIAG	63		
6	HARD	WARE IN	ISTALLATION AND UNINSTALLING	64		
	6.1	Safety M	lessages on Personal Injury	64		
		6.1.1	Electrical Shock Hazard			
	6.2	Property	Damage Messages	65		
		6.2.1	Device Destruction by exceeding allowed Supply Voltage			
		6.2.2 6.2.3	Device Destruction by exceeding allowed Signaling Voltage Electrostatically sensitive Devices			
	6.3		t Plate Sticker at CIFX 80-RE			
	6.4		p PC Cards cifX Compact PCI			
	6.5	-	ling PC Cards cifX Compact PCI			
	6.6		PC Cards cifX Mini PCI and Mini PCI Express			
	6.7		ling PC Cards cifX Mini PCI, Mini PCI Express			
	6.8		PC Cards cifX PCI-104 (PCI-104-Modules)			
	6.9		ling PC Cards cifX PCI-104 (I CI-104-Woudles)			
	0.9	Uninstan				
7	TROL	JBLESHO	OTING	77		
	7.1	Instructio	ons for Problem Solving	77		
8	LED [DESCRIPT	ΓΙΟΝS	78		
	8.1	Overviev	w LEDs Real-Time Ethernet Systems	78		
	8.2	Overview LEDs Fieldbus Systems				
	8.3	System I	79			
	8.4	Power O	79			
	8.5	EtherCA	T Master	80		
	8.6	EtherCA	T Slave	81		
	8.7	EtherNet	EtherNet/IP Scanner (Master)			
	8.8		t/IP Adapter (Slave)			
	8.9		Open Modbus/TCP			
	8.10	POWER	POWERLINK Controlled Node/Slave			
	8.11	PROFIN	PROFINET IO-Controller			
	8.12	PROFIN	ET IO-Device	87		
	8.13	sercos M	Aaster	88		
	8.14	sercos S	sercos Slave			
	8.15		Client (Slave)			
	8.16		US DP Master			
		8.16.1	1 Communication Status LED			
		8.16.2	2 Communication Status LEDs	91		
	8.17		US DP Slave			
		8.17.1	1 Communication Status LED 2 Communication Status LEDs			
		8.17.2				

Tabl	e of Cont	tents		5/191			
	8.18	8 PROFIBUS MPI Device					
		8.18.1	1 Communication Status LED	93			
		8.18.2	2 Communication Status LEDs	93			
	8.19	CANopen	Master	94			
		8.19.1	1 Communication Status LED	94			
		8.19.2	2 Communication Status LEDs	95			
	8.20	CANopen	Slave	96			
		8.20.1	1 Communication Status LED	96			
		8.20.2	2 Communication Status LEDs	97			
	8.21	DeviceNe	t Master	98			
	8.22	DeviceNe	t Slave	99			
9	DEVIC	E CONNE	CTIONS AND SWITCHES				
	9.1	Ethernet I	nterface				
	•••	9.1.1	Ethernet Pin Assignment at the RJ45 Socket				
		9.1.2	Ethernet Connection Data				
		9.1.3	Use of Hubs and Switches	101			
	9.2	PROFIBU	IS Interface	102			
	9.3	CANopen Interface					
	9.4	DeviceNet Interface					
	9.5	Mini-B USB Connector (5 Pin)103					
	9.6	Rotary Switch for PCI-104 Slot Number104					
	9.7	Rotary Sv	vitch Device Address	104			
	9.8	Cable Co	nnector	105			
		9.8.1	Pin Assignment for Cable Connector Ethernet	105			
		9.8.2	Pin Assignment for Cable Connector Fieldbus X3, X304, X4	106			
		9.8.3	Pin Assignment for Cable Connector DIAG	106			
	9.9	SYNC Co	nnector (Pin-Assignment, Hardware/Firmware)	107			
		9.9.1	Pin Assignment SYNC Connector, X51 (CIFX 80 90 104C)	107			
		9.9.2	Items on Hardware	107			
		9.9.3	Items on Firmware	107			
	9.10	Pin Assigi	nment at the PCI Bus	108			
		9.10.1	Overview				
		9.10.2	References PCI Specifications				
		9.10.3	Pin Assignment for Mini PCI Bus, X1				
		9.10.4	Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bo	· · · · ·			
10	TECHN	NICAL DA	ТА				
	10.1						
	10.1	10.1.1	CIFX 80-RE				
		10.1.2	CIFX 80-DP				
		10.1.2	CIFX 80-CO				
		10.1.4	CIFX 80-DN				
		10.1.5	CIFX 90-RE\F	119			

11

	10.1.6	CIFX 90-DP\F	121
	10.1.7	CIFX 90-CO\F	122
	10.1.8	CIFX 90-DN\F	124
	10.1.9	CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E- RE\MR\ET\F	125
	10.1.10	CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E- DP\MR\ET\F	
	10.1.11	CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E- CO\MR\ET\F	
	10.1.12	CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E- DN\MR\ET\F	
	10.1.13	CIFX 104C-RE, CIFX 104C-RE-R	
	10.1.14	CIFX 104C-RE\F, CIFX 104C-RE-R\F	
	10.1.15	CIFX 104C-DP, CIFX 104C-DP-R	
	10.1.16	CIFX 104C-DP\F, CIFX 104C-DP-R\F	
	10.1.17	CIFX 104C-CO, CIFX 104C-CO-R	
	10.1.18	CIFX 104C-CO\F, CIFX 104C-CO-R\F	
	10.1.19	CIFX 104C-DN, CIFX 104C-DN-R	
	10.1.20	CIFX 104C-DN\F, CIFX 104C-DN-R\F	
	10.1.20	AIFX-RE	
	10.1.21	AIFX-DP	
	10.1.22	AIFX-CO	
	10.1.23	All X-CO	
	10.1.24	AIFX-DN AIFX-DIAG	
10.2	PCI IDs F	C Cards cifX on the PCI Bus	154
10.3	Supporte	ed PCI Bus Commands	154
10.4	Technica	I Data of the Communication Protocols	155
	10.4.1	EtherCAT Master	155
	10.4.2	EtherCAT Slave	156
	10.4.3	EtherNet/IP Scanner (Master)	157
	10.4.4	EtherNet/IP Adapter (Slave).	
	10.4.5	Open Modbus/TCP	
	10.4.6	POWERLINK Controlled Node/Slave	
	10.4.7	PROFINET IO-Controller	
	10.4.8	PROFINET IO-Device (V3.4)	
	10.4.9	PROFINET IO-Device (V3.5)	
	10.4.10	sercos Master	
	10.4.11	sercos Slave	
	10.4.12	VARAN Client (Slave)	
	10.4.13	PROFIBUS DP Master	
	10.4.14	PROFIBUS DP Slave	
	10.4.15	PROFIBUS MPI	
	10.4.16	CANopen Master	
	10.4.10	CANopen Master	
	10.4.17	DeviceNet Master	
	10.4.18	DeviceNet Slave	
	10.4.19	Deviceinel Slave	172
ANNE	X		173
11.1	Matrix La	bel	173

Table of Cor	ntents		7/191
11.2	EtherCA	AT Summary over Vendor ID, Conformance test, Membership a	and Network
	Logo		173
	11.2.1	Vendor ID	173
	11.2.2	Conformance	174
	11.2.3	Certified Product vs. Certified Network Interface	174
	11.2.4	Membership and Network Logo	174
11.3	Notes of	n earlier Hardeware Revisions	175
	11.3.1	Failure in 10 MBit/s Half Duplex Mode and Workaround	175
	11.3.2	Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bo	, .
11.4	Disposa	I of Waste Electronic Equipment	178
11.5	Referen	ces	178
11.6	List of F	ïgures	179
11.7	List of T	ables	180
11.8	Glossar	у	183
11.9	Contacts	S	191

1 Introduction

1.1 About the User Manual

This user manual provides descriptions of the **installation**, **operation** and **hardware** of the PC cards cifX *Compact PCI*, *Mini PCI*, *Mini PCI Express* and *PCI-104* under Windows[®] XP, Windows[®] Vista, Windows[®] 7 and Windows[®] 8, as listed subsequently.

PC Cards cifX:

- Compact PCI (CIFX 80),
- Mini PCI (CIFX 90)^A
- Mini PCI Express (CIFX 90E)^A,
- PCI-104 (CIFX 104C)^{A+B}

for CIFX 90, - 90E and - 104C inclusively the AIFX Assembly Interfaces¹:

Ethernet (AIFX-RE)^A

- PROFIBUS (AIFX-DP)^A
- CANopen (AIFX-CO)^A
- DeviceNet (AIFX-DN)^A

for the fieldbus systems:

PROFIBUS DP

PROFIBUS MPI

CANopen

DeviceNet

Diagnose (AIFX-DIAG)^B

for the Real-Time Ethernet systems:

- EtherCAT
- EtherNet/IP
- Open Modbus/TCP
- POWERLINK
- PROFINET IO
- sercos
- VARAN
- i

For information about the **Installation of the Software** refer to the User Manual "Software Installation for PC cards cifX" [DOC120207UMXXEN].

For information about the **Wiring of the Protocol Interface** refer to the "Wiring Instructions" [DOC120208UMXXEN].

The **devices** described in this manual are listed in the sections *PC Cards cifX with integrated Interfaces* (page 28) and *PC Cards cifX with AIFX Assembly* Interfaces (page 29). The devices are described in detail in the chapters *Hardware Installation and Uninstalling* (page 64), *LED Descriptions* (page 78), *Device Connections and Switches* (page 100) and *Technical Data* (page 113).

You can download the latest edition of a manual from the website <u>www.hilscher.com</u> under **Support** > **Downloads** > **Manuals** or under **Products** directly with the information about your product.

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

¹ The AIFX Assembly Interface is also named as "Detached Network Interface".

1.1.1 List of Revisions

Index	Date	Chapter	Revisions
37	13-01-29	All; 1.1.2.2, 8.14, 5.4, 5.4.2, 5.4.3,	Note completed: "Beginning with the EtherCAT Master firmware version 3 RJ45 channel 1 can be reactivated if redundancy is activated." Sections <i>Firmware</i> and <i>sercos Slave</i> (LEDs) updated: Current firmware sercos Slave 3.1.x.x. Sections <i>AIFX Assembly</i> Interfaces
		5.4.4, 5.4.5, 8.16, 8.17, 8.18, 8.19, 8.20, 9.7, 9.10.4, 10.1	 Ethernet - AIFX-RE, PROFIBUS - AIFX-DP, CANopen - AIFX-CO, DeviceNet - AIFX-DN, Diagnostic - AIFX-DIAG updated (information about the LEDs). Descriptions for 2 Communication Status LEDs added: Sections PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device, CANopen Master, CANopen Slave Section Rotary Switch Device Address added; Section Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bootstart), X1/X2 completed: The Pins 36 and 38 are 'not used' (beginning from HW Rev. B). Section Technical Data PC Cards cifX fully revised and completed (details added about galvanic isolation and CE sign).
38	13-11-22	All, 1.1.2, 1.3.2, 3.6.4, 5, 8.5, 8.6, 8.13, 8.14, 8.21, 8.22, 10.1.1, 10.1.5, 10.1.9, 10.1.13, 10.1.21, 10.4.13	In this manual revision the following new PC cards cifX are described: 1.) CIFX 90E-RE\ET\F, CIFX 90E-DP\ET\F, CIFX 90E-CO\ET\F, CIFX 90E- DN\ET\F: "ET" = "Enlarged Temperature", e. g. this cards can be used in an enlarged operating temperature range from -20 °C to +70 °C. 2.) CIFX 90E-IMR, CIFX 90E-RE\MR\F, CIFX 90E-DP\MR\F, CIFX 90E-CO\MR\F, CIFX 90E-DN\MR\F: with the additional memory module MRAM with 128Kbyte (= 64K words) for storing remanent data (via cifX device driver from V1.1.1.0). 3.) CIFX 90E\MR\ET, CIFX 90E-RE\MR\ET\F, CIFX 90E-DP\MR\FT, CIFX 90E- CO\MR\ET\F, CIFX 90E-ND\MR\ET\F: with the additional memory module MRAM with 128Kbyte (= 64K words) for storing remanent data (via cifX device driver from V1.1.1.0) and usable in an enlarged operating temperature range from -20 °C to +70 °C. Further Updates: - cifX Device Driver, Version 1.2.x.x, - Storage temperature range added (for PC cards cifX). - Section Notes on Hardware, Firmware, Software and Driver Versions updated. - Section Notes on Hardware, Firmware, Software and Driver Versions updated. - Section Notes on Hardware, Firmware Versions 2.5 and 4.2" added. Section Notes on Hardware, Firmware Versions 2.5 and 4.2" added. Section Power Supply and Host Interface updated (recommended current added). - Chapter Device Drawings revised and updated. - LED Descriptions: Sections EtherCAT Master, EtherCAT Slave, sercos Master, sercos Slave, DeviceNet Master and DeviceNet Slave revised/updated. - Lower limit of the operating temperature range for all PC cards cifX Real-Time Ethernet updated to 0 ° C: Section CIFX 80-RE: (0 to +70°C). Section CIFX 80-RE: (0 to +70°C). Section CIFX 90-REVF; (0 to +70°C). Section AIFX-RE: (0 to +70°C). Section AIFX-RE: (0 to +70°C). Section AIFX-RE: (0 to +70°C). Note: Due to a plug element from ERNI the lower limit of the operating temperature for
39	13-12-18	10.4.2 10.4.3 10.4.4 10.4.9 10.4.10 10.4.11 10.4.17 11.5	Section Technical Data <i>EtherCAT Slave</i> updated. Section Technical Data <i>EtherNet/IP Scanner (Master)</i> updated. Section Technical Data <i>EtherNet/IP Adapter (Slave)</i> updated. Section Technical Data <i>PROFINET IO-Device (V3.5)</i> added. Section Technical Data <i>sercos Master</i> updated. Section Technical Data <i>sercos Slave</i> updated. Section Technical Data <i>CANopen Slave</i> updated. Section <i>References</i> updated.

Table 1: List of Revisions

1.1.2 Notes on Hardware, Firmware, Software and Driver Versions

Note on Software Update: The hardware revisions and the versions for the firmware, the driver or the configuration software listed in this section functionally belong together. For existing hardware installation the firmware, the driver and the configuration software must be updated according to the details listed in this section.

For the software upgrade system overview refer to section *Update for Firmware, Driver and Software* on page 46.

1.1.2.1 Hardware: PC Cards cifX, AIFX Assembly Interfaces

PC Card cifX , AIFX	Part No.	Hardware Revision	USB from HW Rev.	"DMA Mode" from HW Rev.		
CIFX 80-RE	1280.100	3	1	1		
CIFX 90-RE\F1	1290.100	4	-	1		
CIFX 90E-RE\F1	1291.100	В	-	А		
CIFX 90E-RE\MR\F1	1291.102	В	-	В		
CIFX 90E-RE\ET\F1	1291.104	1	-	1		
CIFX 90E-RE\MR\ET\F1	1291.106	1	-	1		
CIFX 104C-RE	1270.100	5	1	1		
CIFX 104C-RE-R	1271.100	5	1	1	1	
CIFX 104C-RE\F1	1270.101	5	5 ⁵	1		
CIFX 104C-RE-R\F1	1271.101	5	5 ⁵	1		
CIFX 80-DP	1280.410	3	1	1		
CIFX 80-CO	1280.500	3	1	1		
CIFX 80-DN	1280.510	3	1	1		
CIFX 90-DP\F ²	1290.410	4	-	1		
CIFX 90-CO\F ³	1290.500	4	-	1		
CIFX 90-DN\F ⁴	1290.510	4	-	1		
CIFX 90E-DP\F ²	1291.410	В	-	А		
CIFX 90E-DP\MR\F ²	1291.412	В	-	В	-	
CIFX 90E-DP\ET\F ²	1291.414	1	-	1	 ¹ inclusively Ethernet Assembly Interface (AIFX-RE) ² inclusively PROFIBUS Assembly Interface (AIFX-DP) ³ inclusively CANopen Assembly Interface (AIFX-CO) 	
CIFX 90E-DP\MR\ET\F ²	1291.416	1	-	1		
CIFX 90E-CO\F ³	1291.500	В	-	Α		
CIFX 90E-CO\MR\F ³	1291.502	В	-	В		
CIFX 90E-CO\ET\F ³	1291.504	1	-	1		
CIFX 90E-CO\MR\ET\F ³	1291.506	1	-	1		
CIFX 90E-DN\F ⁴	1291.510	В	-	Α		
CIFX 90E-DN\MR\F ⁴	1291.512	В	-	В		
CIFX 90E-DN\ET\F4	1291.514	1	-	1		
CIFX 90E-DN\MR\ET\F ⁴	1291.516	1	-	1		
CIFX 104C-DP	1270.410	2	1	1	⁴ inclusively DeviceNet Assembly Interface	
CIFX 104C-DP-R	1271.410	2	1	1	(AIFX-DN)	
CIFX 104C-DP\F ²	1270.411	2	1 ⁵	1	⁵ only when using the	
CIFX 104C-DP-R\F ²	1271.411	2	1 ⁵	1	Diagnostic Assembly	
CIFX 104C-CO	1270.500	2	1	1	Interface (AIFX-DIAG)	
CIFX 104C-CO-R	1271.500	2	1	1	-	
CIFX 104C-CO\F ³	1270.501	2	1 ⁵	1	-	
CIFX 104C-CO-R\F ³	1271.501	2	1 ⁵	1	-	
CIFX 104C-DN	1270.510	2	1	1	-	
CIFX 104C-DN-R	1271.510	2	1	1	1	
CIFX 104C-DN\F ⁴	1270.511	2	1 ⁵	1	1	
CIFX 104C-DN-R\F ⁴	1271.511	2	1 ⁵	1	-	
AIFX-RE	2800.100	2	-	-		
AIFX-DP	2800.400	2	-	-	-	
		2			-	
AIEX-CO	2800 500	/				
AIFX-CO AIFX-DN	2800.500 2800.510	3	-	-	-	

Table 2: Reference on Hardware PC Cards cifX, AIFX Assembly Interfaces

1.1.2.2 Firmware

Fieldbus System	Firmware File	Firmware Version	Minimum Version of the Firmware for USB Support
CANopen Master	CIFXCOM.NXF	2.11.x.x	from 2.5.2.0
CANopen Slave	CIFXCOS.NXF	3.6.x.x	from 2.4.4.0
DeviceNet Master	CIFXDNM.NXF	2.3.x.x	from 2.2.7.0
DeviceNet Slave	CIFXDNS.NXF	2.3.x.x	from 2.2.7.0
EtherCAT Master	CIFXECM.NXF	3.0.x.x	from 2.4.4.0
EtherCAT Slave	CIFXECS.NXF	2.5. x.x (V2)	from 2.5.13.0
EtherCAT Slave	CIFXECS.NXF	4.2.x.x (V4)	from 2.5.13.0
EtherNet/IP Adapter	CIFXEIS.NXF	2.7. x.x	from 2.3.4.1
EtherNet/IP Scanner	CIFXEIM.NXF	2.6. x.x	from 2.2.4.1
Open Modbus/TCP	CIFXOMB.NXF	2.5. x.x	from 2.3.2.1
POWERLINK Controlled Node	CIFXPLS.NXF	2.1. x.x	from 2.1.22.0
PROFIBUS DP Master	CIFXDPM.NXF	2.6.x.x	from 2.3.22.0
PROFIBUS DP Slave	CIFXDPS.NXF	2.7.x.x	from 2.3.30.0
PROFIBUS MPI Device	CIFXMPI.NXF	2.4.x.x	from 2.4.1.2
PROFINET IO Controller	CIFXPNM.NXF	2.6. x.x	from 2.4.10.0
PROFINET IO Device	CIFXPNS.NXF	3.4.x.x (V3)	from 3.4.9.0
PROFINET IO Device	CIFXPNS.NXF	3.5.x.x (V3)	from 3.4.9.0
sercos Master	CIFXS3M.NXF	2.1.x.x	from 2.0.14.0
sercos Slave	CIFXS3S.NXF	3.1.x.x	from 3.0.13.0
VARAN Client	CIFXVRS.NXF	1.0.x.x	from 1.0.3.0

Table 3: Reference on Firmware

The <u>downloadable cifX firmware</u> runs on PC Cards cifX *Compact PCI, Mini PCI, Mini PCI Express* and *PCI-104*. The firmware automatically detects whether it is running on a PC Cards cifX *Compact PCI, Mini PCI, Mini PCI Express* and *PCI-104*.

1.1.2.3 Driver and Software

Driver and Software		Version
SYCON.net	SYCONnet netX setup.exe	1.360.x.x
netX Configuration Tool-Setup	netXConfigurationUtility_Setup.exe	1.0510.x.x
cifX Device Driver	cifX Device Driver Setup.exe	1.2.x.x
Toolkit		1.1.x.x
cifX TCP/IP Server for SYCON.net	cifX TCP Server.exe	V2.1.0.0
US Driver	USB Driver of Windows [®]	5.1.2600.x

Table 4: Reference on Driver and Software

1.1.3 Conventions in this Manual

Notes, operation instructions and results of operation steps are marked as follows:

Notes

Important: <important note>



Note: <note>



<note, where to find further information>

Operation Instructions

- 1. <instruction>
- 2. <instruction>
- or
- <instruction>

Results

P→ <result>

Safety Instructions

The labeling of safety and warning instructions is explained separately in chapter *Safety*, in section *Labeling of Safety Instructions*.

1.1.4 Used Terminology

PC Card cifX	Communication Interfaces of the cifX family of Hilscher based on the netX technology.
CIFX 80-RE	Example for the product name for a PC card cifX Real-Time Ethernet.
CIFX 90E-XX	Example (,XX' replaces ,RE', ,DP', CO' or ,DN')
CIFX 90E-FB	Example (,FB' replaces ,DP', CO' or ,DN')



For further terminology to the PC cards cifX, its installation, configuration and operation refer to section *Glossary* on page 183.

1.2 Contents of the Product DVD

On the **Communication Solutions DVD** you will find these installation instructions about the software installation and the necessary configuration software, the documentation, the drivers and software for your PC card cifX, and additional auxiliary tools.

1.2.1 Installation Guide, Documentation Overview

The installation guide **Software Installation and Documentation Overview** on the Communication Solutions DVD are in the directory *Documentation\0. Installation and Overview.* The installation guide includes:

- An overview on the **Content of the Communication Solutions DVD** (in the section *What is on the Communication Solutions DVD?*)
- Overviews listing the available **Documentations** for PC cards cifX (in chapter *PC Cards cifX, Software and Documentation*).

1.2.2 What's New



All current version information for hardware and software described in this manual are provided in the folder \Documentation\What's New - Communication Solutions DVD RL XX EN.pdf on the Communication Solutions DVD.

1.2.3 Important Changes

1.2.3.1 DeviceNet Master - SYCON.net and Firmware

The DeviceNet Master firmware from V2.3.11.0 and the DeviceNet Master DTM from V1.360.x.x support the network scan function. If in the device a firmware version V2.3.10.0 or earlier is used then a firmware update to V2.3.11.0 or higher must be done, in order to use the **network scan** function.

1.2.3.2 PROFINET IO-Device Firmware Versions 3.4 and 3.5

The PROFINET IO Device firmware was revised and completed and is available in version 3.5 since the third quarter 2013.

Use the PROFINET IO Device firmware in version 3.5 for a new installation, when you create or develop your application program for the first time.

If you want to change in an existing system from the PROFINET IO Device firmware version 3.4 to the version 3.5, note the following guidelines:

1. Customize your application program according to the Migration Guide **PROFINET IO Device, Migration from V3.4 to V3.5**.



If you want to change to V3.5, please check in the Migration Guide **PROFINET IO Device, Migration from V3.4 to V3.5** which changes are necessary in the application program in order to use version 3.5.

- Adjust the configuration of your PROFINET IO Controller device. Use the new GSDML file in the configuration software of the PROFINET IO Controller for this: GSDML-V2.3-HILSCHER-CIFX RE PNS-20130301.xml.
- 3. Update the PROFINET IO Device firmware in your device to version 3.5.

Note also:

- SYCON.net V1.360.x.x can configure the PROFINET IO Device firmware V3.4 as well as V3.5
- netX Configuration Tool V1.0510.x.x can configure the PROFINET IO Device firmware V3.4 as well as V3.5.
- The development of the PROFINET IO Device firmware V3.4 will not be continued, but this firmware version will be delivered furthermore.

On the Communication Solutions DVD, software and manuals relating to both firmware versions V3.4 and V3.5 are available:

	PROFINET IO-Device V3.4 Directory on the DVD \ File:	PROFINET IO-Device V3.5 Directory on the DVD \ File:
Firmware	Firmware\CIFX\cifxpns.nxf	Firmware\CIFX\PNS V3.5.X\cifxpns.nxf
Header	Examples and API\0. Header\Firmware\PROFINET IO Device V3.4.X	Examples and API\0. Header\Firmware\PROFINET IO Device V3.5.X
GSDML	EDS\PROFINET\V3.4.X\GSDML-V2.3-HILSCHER-CIFX RE PNS-20130225.xml	EDS\PROFINET\V3.5.X\GSDML-V2.3-HILSCHER- CIFX RE PNS-20130301.xml
Protocol API	Documentation\7. Programming Manuals\EN\3. Protocol API\PROFINET IO Device V3.4\PROFINET IO Device Protocol API 13 EN.pdf	Documentation\7. Programming Manuals\EN\3. Protocol API\PROFINET IO Device V3.5\PROFINET IO Device V3.5 Protocol API 06 EN.pdf
	TCP IP - Packet Interface API 13 EN.pdf	PROFINET IO Device - Migration from Version 3.4 to 3.5 MG 03 EN.pdf

Table 5: PROFINET IO-Device Firmware Version 3.4 and 3.5, Header, GSDML and Protocol API Manual

1.2.3.3 EtherCAT Slave Firmware Versions 2.5 and 4.2

The EtherCAT Slave firmware was revised and completed and is available in version 4.2 since the third quarter 2013.

Use the EtherCAT Slave firmware in version 4.2 for a new installation, when you create or develop your application program for the first time.

If you want to change in an existing system from the EtherCAT Slave firmware version 2.5 to the version 4.2, note the following guidelines:

1. Customize your application program according to the Migration Guide EtherCAT Slave, Migration from V2.5 to V4.2.



If you want to change to V4.2, please check in the Migration Guide **EtherCAT Slave, Migration from V2.5 to V4.2** which changes are necessary in the application program in order to use version 4.2.

- 2. Adjust the configuration of your EtherCAT Master device. Use the new XML file in the configuration software of the EtherCAT Master for this: *Hilscher CIFX RE ECS V4.2.X.xml.*
- 3. Update the EtherCAT Slave firmware in your device to version 4.2.

Note also:

- SYCON.net V1.360.x.x can configure the EtherCAT Slave firmware V2.5 as well as V4.2.10.0 and higher.
- netX Configuration Tool V1.0510.x.x can configure the EtherCAT Slave firmware V2.5 as well as V4.2.
- The development of the EtherCAT Slave firmware V2.5 will not be continued, but this firmware version will be delivered furthermore.

On the Communication Solutions DVD, software and manuals relating to both firmware versions V2.5 and V4.2 are available:

	EtherCAT-Slave V2.5 Directory on the DVD \ File:	EtherCAT-Slave V4.2 Directory on the DVD \ File:
Firmware	Firmware\CIFX\cifxecs.nxf	Firmware\CIFX\ECS V4.X\cifxecs.nxf
Header	Examples and API\0. Header\Firmware\EtherCAT Slave V2.5.X	Examples and API\0. Header\Firmware\EtherCAT Slave V4.2.X
XML	EDS\EtherCAT\Slave\V2.X\Hilscher CIFX RE ECS V2.2.X.xml	EDS\EtherCAT\Slave\V4.X\Hilscher CIFX RE ECS V4.2.X.xml
Protocol API	Documentation\7. Programming Manuals\EN\3. Protocol API\EtherCAT Slave V2\EtherCAT Slave Protocol API 21 EN.pdf	Documentation\7. Programming Manuals\EN\3. Protocol API\EtherCAT Slave V4\EtherCAT Slave V4 Protocol API 03 EN.pdf
		EtherCAT Slave - Migration from Version 2.5 to 4.2 MG 02 EN.pdf
		Object Dictionary V3 03 API EN.pdf

Table 6: EtherCAT-Slave Firmware Version 2.5 and 4.2, Header, XML and Protocol API Manual

1.2.4 Device Description Files cifX

The Communication Solutions DVD **EDS** directory includes the device description files for the PC cards cifX. The device description file is required to configure the used Master device. The systems Open Modbus/TCP, PROFIBUS MPI and VARAN do not use device description files.

PC Cards cifX	System		File Name of the Devi	ce Description	n File
CIFX 80-RE CIFX 90-RE\F			For the EtherCAT Slave Firmware V2.5: Hilscher cifX RE ECS V2.2.x.xml (or with extension DDF)		
CIFX 90E-RE\F CIFX 90E-RE\ET\F CIFX 90E-RE\MR\F			For the EtherCAT Slave CIFX RE ECS V4.2.X.x		
CIFX 90E-RE\MR\ET\F CIFX 104C-RE CIFX 104C-RE-R			<i>dilscher cifX RE ECS V2.</i>		l/re-installed, the
CIFX 104C-RE\F CIFX 104C-RE-R\F	EtherNet/IP Adapter (Slave)		HILSCHER CIFX-RE EIS V1.1.EDS		
	EtherNet/IP S	canner (Master)	HILSCHER CIFX-RE E	IM V1.0.eds	
	addi 🔁		files for the EtherNet/IP Master device shall com erNet/IP.		
	POWERLINK- Node/Slave	Controlled-	00000044_CIFX RE PL	.S.xdd	
	PROFINET IO-Device		For the PROFINET IO Device Firmware V3.4: GSDML-V2.3-HILSCHER-CIFX RE PNS-20130225.xml		
			For the PROFINET IO Device Firmware with V3.5.13.1 the GSDML-V2.3-HILSCHER-CIFX RE PNS-20130301.xml is provided.		
	sercos Slave		Hilscher CIFX RE S3S Hilscher CIFX RE S3S		
	one was	of the defaults for changed, then yo	cos Master which is usin vendor code, device ID, u have to export a new u L file into the configuration	input data size	e or output data size L file from SYCON.net
PC Cards cifX	System	File Name	PC Cards cifX	System	File Name
CIFX 80-DP CIFX 90-DP\F CIFX 90E-DP\F CIFX 90E-DP\ET\F CIFX 90E-DP\MR\F CIFX 90E-DP\MR\ET\F CIFX 104C-DP CIFX 104C-DP-R CIFX 104C-DP\F CIFX 104C-DP-R\F	PROFIBUS DP-Slave	HIL_0B69.GSD	CIFX 80-DN CIFX 90-DN\F CIFX 90E-DN\F CIFX 90E-DN\ET\F CIFX 90E-DN\MR\F CIFX 90E-DN\MR\ET\F CIFX 104C-DN CIFX 104C-DN-R CIFX 104C-DN\F CIFX 104C-DN-R\F	DeviceNet Slave	CIFX_DN_DNS.EDS
CIFX 80-CO CIFX 90-CO\F CIFX 90E-CO\F CIFX 90E-CO\ET\F CIFX 90E-CO\MR\F CIFX 90E-CO\MR\ET\F CIFX 104C-CO CIFX 104C-CO-R CIFX 104C-CO\F CIFX 104C-CO-R\F	CANopen Slave	CIFX CO COS.eds			

Table 7: Device Description Files for PC Cards cifX

1.3 Legal Notes

1.3.1 Copyright

© Hilscher, 2008-2013, Hilscher Gesellschaft für Systemautomation mbH All rights reserved.

The images, photographs and texts in the accompanying material (user manual, accompanying texts, documentation, etc.) are protected by German and international copyright law as well as international trade and protection provisions. You are not authorized to duplicate these in whole or in part using technical or mechanical methods (printing, photocopying or other methods), to manipulate or transfer using electronic systems without prior written consent. You are not permitted to make changes to copyright notices, markings, trademarks or ownership declarations. The included diagrams do not take the patent situation into account. The company names and product descriptions included in this document may be trademarks or brands of the respective owners and may be trademarked or patented. Any form of further use requires the explicit consent of the respective rights owner.

1.3.2 Important Notes

The user manual, accompanying texts and the documentation were created for the use of the products by qualified experts, however, errors cannot be ruled out. For this reason, no guarantee can be made and neither juristic responsibility for erroneous information nor any liability can be assumed. Descriptions, accompanying texts and documentation included in the user manual do not present a guarantee nor any information about proper use as stipulated in the contract or a warranted feature. It cannot be ruled out that the user manual, the accompanying texts and the documentation do not correspond exactly to the described features, standards or other data of the delivered product. No warranty or guarantee regarding the correctness or accuracy of the information is assumed.

We reserve the right to change our products and their specification as well as related user manuals, accompanying texts and documentation at all times and without advance notice, without obligation to report the change. Changes will be included in future manuals and do not constitute any obligations. There is no entitlement to revisions of delivered documents. The manual delivered with the product applies.

Hilscher Gesellschaft für Systemautomation mbH is not liable under any circumstances for direct, indirect, incidental or follow-on damage or loss of earnings resulting from the use of the information contained in this publication.

1.3.3 Exclusion of Liability

The software was produced and tested with utmost care by Hilscher Gesellschaft für Systemautomation mbH and is made available as is. No warranty can be assumed for the performance and flawlessness of the software for all usage conditions and cases and for the results produced when utilized by the user. Liability for any damages that may result from the use of the hardware or software or related documents, is limited to cases of intent or grossly negligent violation of significant contractual obligations. Indemnity claims for the violation of significant contractual obligations are limited to damages that are foreseeable and typical for this type of contract.

It is strictly prohibited to use the software in the following areas:

- for military purposes or in weapon systems;
- for the design, construction, maintenance or operation of nuclear facilities;
- in air traffic control systems, air traffic or air traffic communication systems;
- in life support systems;
- in systems in which failures in the software could lead to personal injury or injuries leading to death.

We inform you that the software was not developed for use in dangerous environments requiring fail-proof control mechanisms. Use of the software in such an environment occurs at your own risk. No liability is assumed for damages or losses due to unauthorized use.

1.3.4 Warranty

Although the hardware and software was developed with utmost care and tested intensively, Hilscher Gesellschaft für Systemautomation mbH does not guarantee its suitability for any purpose not confirmed in writing. It cannot be guaranteed that the hardware and software will meet your requirements, that the use of the software operates without interruption and that the software is free of errors. No guarantee is made regarding infringements, violations of patents, rights of ownership or the freedom from interference by third parties. No additional guarantees or assurances are made regarding marketability, freedom of defect of title, integration or usability for certain purposes unless they are required in accordance with the law and cannot be limited. Warranty claims are limited to the right to claim rectification.

1.3.5 Export Regulations

The delivered product (including the technical data) is subject to export or import laws as well as the associated regulations of different counters, in particular those of Germany and the USA. The software may not be exported to countries where this is prohibited by the United States Export Administration Act and its additional provisions. You are obligated to comply with the regulations at your personal responsibility. We wish to inform you that you may require permission from state authorities to export, re-export or import the product.

1.3.6 Registered Trademarks

Windows[®] XP, Windows[®] Vista, Windows[®] 7 and Windows[®] 8 are registered trademarks of Microsoft Corporation.

Linux is a registered trademark of Linus Torvalds.

QNX is a registered trademark of QNX Software Systems, Ltd.

VxWorks is a registered trademark of Wind River Systems, Inc.

IntervalZero RTX [™] is a trademark of IntervalZero.

CANopen[®] is a registered trademark of CAN in AUTOMATION - International Users and Manufacturers Group e.V (CiA), Nürnberg.

DeviceNet[™] and EtherNet/IP[™] are trademarks of ODVA (Open DeviceNet Vendor Association, Inc).

EtherCAT[®] is a registered trademark and a patented technology of Beckhoff Automation GmbH, Verl, Germany, formerly Elektro Beckhoff GmbH.

Modbus is a registered trademark of Schneider Electric.

POWERLINK is a registered trademark of B&R, Bernecker + Rainer Industrie-Elektronik Ges.m.b.H, Eggelsberg, Austria

PROFIBUS[®] and PROFINET[®] are registered trademarks of PROFIBUS International, Karlsruhe.

sercos and sercos interface are registered trademarks of sercos international e. V., Suessen, Germany.

PCI[™], PCI EXPRESS[®] and PCIe[®] or MINI PCI[™] are trademarks or registered trademarks of the Peripheral Component Interconnect Special Interest Group (PCI-SIG).

CompactPCI[™] is a trademark of the PCI Industrial Manufacturers Group (PICMG).

All other mentioned trademarks are property of their respective legal owners.

1.3.7 EtherCAT Disclaimer

EtherCAT[®] is registered trademark and patented technology, licensed by Beckhoff Automation GmbH, Germany.



To get details and restrictions regarding using the EtherCAT technology refer to the following documents:

- "EtherCAT Marking rules"
- "EtherCAT Conformance Test Policy"
- "EtherCAT Vendor ID Policy"

These documents are available at the ETG homepage <u>www.ethercat.org</u> or directly over <u>info@ethercat.org</u>.

A summary over Vendor ID, Conformance test, Membership and Network Logo can be found within the appendix section of this document under section *EtherCAT Summary over Vendor ID, Conformance test, Membership and Network Logo* on page 173.

1.3.8 Obligation to read and understand the Manual



Important!

- To avoid personal injury and to avoid property damage to your system or to your PC card, you must read and understand all instructions in the manual and all accompanying texts to your PC card, before installing and operating your PC card.
- First read the **Safety Instructions** in the safety chapter.
- Obey to all Safety Messages in the manual.
- Keep the product DVD providing the product manuals.

1.4 Licenses

If a PC card cifX is used as a Slave, neither for the firmware nor for the configuration software SYCON.net a license is required.

Licenses will be required if the PC card cifX is used with

a firmware with master functionality*.

* The master license includes the PC card cifX operating as master and the license for the configuration software SYCON.net for the respective cifX.

1.4.1 License Note about VARAN Client

In order to use the PC card cifX with VARAN, you need a license which you can acquire at the VNO (VARAN Bus-Nutzerorganisation, Bürmooser Straße 10, A-5112 Lamprechtshausen, info@varan-bus.net) after getting a member of VON.

The license as well as the Vendor ID and the Device ID can be adjusted with the SYCON.net configuration software or with the netX Configuration Tool.

2 Safety

2.1 General Note

The documentation in the form of a user manual, an operating instruction manual or other manual types, as well as the accompanying texts have been created for the use of the products by educated personnel. When using the products, all Safety Messages, Safety Messages, Property Damage Messages and all valid legal regulations have to be obeyed. Technical knowledge is presumed. The user has to assure that all legal regulations are obeyed.

2.2 Intended Use

The **PC cards cifX** described in this user manual are PC cards for the Real-Time Ethernet or fieldbus communication. Depending from the loaded firmware, the Real-Time Ethernet or fieldbus systems listed in the following table can be realized using the respective PC card cifX.

PC Cards cifX	Real-Time Ethernet System	PC Cards cifX	Fieldbus System
IFX 80-RE CIFX 90-RE\F	EtherCAT Master, EtherCAT Slave	CIFX 80-CO CIFX 90-CO\F	CANopen Master, CANopen Slave DeviceNet Master, DeviceNet Slave
CIFX 90E-RE\F CIFX 90E-RE\ET\F CIFX 90E-RE\MR\F	EtherNet/IP Scanner (Master), EtherNet/IP Scanner (Slave)	CIFX 90E-CO\F CIFX 90E-CO\ET\F CIFX 90E-CO\MR\F CIFX 90E-CO\MR\ET\F CIFX 104C-CO	
CIFX 90E-RE\MR\ET\F	Open Modbus/TCP		
CIFX 104C-RE CIFX 104C-RE-R	POWERLINK Controlled Node/Slave	CIFX 104C-CO-R	
CIFX 104C-RE\F CIFX 104C-RE\F CIFX 104C-RE-R\F	PROFINET IO-Controller (Master), PROFINET IO-Device (Slave)	CIFX 104C-CO\F CIFX 104C-CO-R\F	
	sercos Master, sercos Slave	CIFX 80-DN CIFX 90-DN\F	
	VARAN Client (Slave)	CIFX 90E-DN\F CIFX 90E-DN\ET\F	
PC Cards cifX	Fieldbus System	CIFX 90E-DN\MR\F CIFX 90E-DN\MR\ET\F	
CIFX 80-DP CIFX 90-DP\F CIFX 90E-DP\F CIFX 90E-DP\ET\F CIFX 90E-DP\MR\F CIFX 90E-DP\MR\ET\F CIFX 104C-DP CIFX 104C-DP-R CIFX 104C-DP\F CIFX 104C-DP-R\F	PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device	CIFX 90E-DINWIKIET (F CIFX 104C-DN CIFX 104C-DN-R CIFX 104C-DN\F CIFX 104C-DN-R\F	

Table 8: PC Cards cifX and the Real-Time Ethernet or Fieldbus Systems realizable

The **AIFX Assembly Interfaces** are each attached to the respective basic card for the PC card cifX via a cable connector (label "\F"). This way the PC card cifX is equipped with a Real-Time Ethernet or fieldbus interface and for the PC card cifX PCI-104 in addition with a diagnostic interface.

AIFX	PC Cards cifX with AIFX Assembly Interface
AIFX-RE	CIFX 90-RE\F, CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F, CIFX 104C-RE\F, CIFX 104C-RE-R\F
AIFX-DP	CIFX 90-DP\F, CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F, CIFX 104C-DP\F, CIFX 104C-DP-R\F
AIFX-CO	CIFX 90-CO\F, CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F

AIFX	PC Cards cifX with AIFX Assembly Interface
	CIFX 90-DN\F, CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F
AIFX-DIAG (optional)	CIFX 104C-RE\F, CIFX 104C-RE-R\F CIFX 104C-DP\F, CIFX 104C-DP-R\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F

Table 9: PC Cards cifX with AIFX Assembly Interface

2.3 Personnel Qualification

The PC card cifX must only be installed, configured and removed by qualified personnel. Job-specific technical skills for people professionally working with electricity must be present concerning the following topics:

- Safety and health at work
- Mounting and connecting of electrical equipment
- Measurement and Analysis of electrical functions and systems
- Evaluation of the safety of electrical systems and equipment
- Installing and Configuring IT systems

2.4 Safety Instructions to avoid Personal Injury

To ensure your own personal safety and to avoid personal injury, you necessarily must read, understand and follow the following safety instructions and safety messages in this manual about danger causing personal injury, before you install and operate your PC card cifX.

2.4.1 Electrical Shock Hazard

The danger of a lethal electrical shock caused by parts with more than 50V may occur if you open the PC cabinet to install the PC card cifX.

- HAZARDOUS VOLTAGE is present inside of the PC or of the connecting device, into which the PC card cifX is integrated. Strictly obey to all safety rules provided by the device's manufacturer in the documentation!
- First disconnect the power plug of the PC or of the connecting device, before you open the cabinet.
- Make sure, that the power supply is off at the PC or at the connecting device.
- Open the PC cabinet and install or remove the PC card cifX only after disconnecting power.

An electrical shock is the result of a current flowing through the human body. The resulting effect depends on the intensity and duration of the current and on its path through the body. Currents in the range of approximately ½ mA can cause effects in persons with good health, and indirectly cause injuries resulting from startle responses. Higher currents can cause more direct effects, such as burns, muscle spasms, or ventricular fibrillation.

In dry conditions permanent voltages up to approximately 42.4 V peak or 60 V DC are not considered as dangerous if the contact area is equivalent to the size of a human hand.

Reference Safety [S2]

2.5 Safety Instructions to avoid Property Damage

To avoid property damage respectively device destruction to the PC card cifX and to your system, you necessarily must read, understand and follow the following safety instructions and safety messages in this manual about danger causing property damage, before you install and operate your PC card.

2.5.1 Device Destruction by exceeding allowed Supply Voltage

To avoid device destruction due to high supply voltage to your PC card cifX, you must observe the following instructions. These instructions apply to all PC cards cifX described in this manual.

The PC card cifX may only be operated with the specified supply voltage. Make sure that the limits of the allowed range for the supply voltage are not exceeded. A supply voltage above the upper limit can cause severe damage to the PC card cifX! A supply voltage below the lower limit can cause malfunction in the PC card cifX. The allowed range for the supply voltage is defined by the tolerances specified in this manual.

For the PC cards listed hereafter adhere specifically: The PC card cifX

- CIFX 80-RE, CIFX 80-DP, CIFX 80-CO, CIFX 80-DN,
- CIFX 90-RE\F, CIFX 90-DP\F, CIFX 90-CO\F, CIFX 90-DN\F,
- CIFX 90E-RE\F, CIFX 90E-RE\ET\F,
- CIFX 90E-DP\F, CIFX 90E-DP\ET\F,
- CIFX 90E-CO\F, CIFX 90E-CO\ET\F,
- CIFX 90E-DN\F, CIFX 90E-DN\ET\F,
- CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F,
- CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F,
- CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F,
- CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F

may not be powered by a 5V supply voltage! The PC card cifX may only be powered by a 3.3 V dc \pm 5 % supply voltage.



The data on the mandatory supply voltage for the PC cards cifX described in this manual you find in section *Power Supply and Host Interface* on page 38. There the required and permitted supply voltage is provided by device type inclusively the permitted tolerance range.

2.5.2 Device Destruction by exceeding allowed Signaling Voltage

To avoid device destruction due to high signal voltage to your PC card cifX, you must observe the following instructions. These instructions apply to all PC cards cifX described in this manual.

- All I/O signal pins at the PC card cifX tolerate only a specified signaling voltage!
- Operation with a signaling voltage other than the specified signaling voltage may lead to severe damage to the PC card cifX!



The data on the mandatory signaling voltage for the PC cards cifX described in this manual you find in the section *Power Supply and Host* Interface on page 38. There the required and permitted signaling voltage is provided by device type.

2.5.3 Electrostatically sensitive Devices

This equipment is sensitive to electrostatic discharge, which cause internal damage and affect normal operation. Therefore adhere to the necessary safety precautions for components that are vulnerable with electrostatic discharge if you install or replace your device. Follow the guidelines listed hereafter when you handle this equipment:

- Touch a grounded object to discharge potential static.
- Wear an approved grounding wriststrap.
- Do not touch connectors or pins on the PC card cifX.
- Do not touch circuit components inside the equipment.
- If available, use a static-safe workstation.
- When not in use, store the equipment in appropriate static-safe packaging.

Reference Safety [S3]

2.6 Labeling of Safety Messages

- The **Section Safety Messages** at the beginning of a chapter are pinpointed particularly. They are highlighted with a specific safety symbol and a signal word according to the degree of endangerment. Inside the safety message the danger is exactly named.
- The **Integrated Safety Messages** within an instruction description are highlighted with a signal word according to the degree of endangerment and possibly by a principle symbol. Inside the safety message the danger is exactly named.

Safety Symbol	USA	Sort of Warning or Principle
		Warning of Personal Injury and Property Damage Message
		USA: Warning of Personal Injury
		As in the scope of the ANSI Z535 Standard (for USA) instructions to a property damage message may not contain a warning triangle, this property damage messages are listed separately for the USA.
	Ň	Warning of Lethal Electrical Shock
		Warning of Damages by Electrostatic Discharge
		Principle: Disconnect the Power Plug

Signal Word	Meaning	Meaning (USA)
A DANGER	Indicates a direct hazard with high risk, which will have as consequence death or grievous bodily harm if it isn't avoided.	Indicates a Hazardous Situation Which if not Avoided, will Result in Death or Serious Injury.
A WARNING	Indicates a possible hazard with medium risk, which will have as consequence death or (grievous) bodily harm if it isn't avoided.	Indicates a Hazardous Situation Which if not Avoided, could Result in Death or Serious Injury.
A CAUTION	Indicates a minor hazard with medium risk, which could have as consequence simple battery if it isn't avoided.	Indicates a Hazardous Situation Which if not Avoided, may Result in Minor or Moderate Injury.
NOTICE	Indicates a Property Damage Message.	Indicates a Property Damage Message.
Note	Indicates an important note in the manual.	Indicates an Important Note in the Manual.

Table 11: Signal Words

In this document all Safety Instructions and Safety Messages are designed according both to the international used safety conventions as well as to the ANSI Z535 standard, refer to reference safety [S1].

2.6.1 References Safety

- [S1] ANSI Z535.6-2006 American National Standard for Product Safety Information in Product Manuals, Instructions, and Other Collateral Materials
- [S2] IEC 60950-1, Information technology equipment Safety Part 1: General requirements, (IEC 60950-1:2005, modified); German Edition EN 60950-1:2006
- [S3] EN 61340-5-1 and EN 61340-5-2 as well as IEC 61340-5-1 and IEC 61340-5-2

3 Descriptions and Requirements

3.1 Description

The PC cards cifX are communication interfaces of the cifX product family of Hilscher on the basis of the communication controller netX 100 for the Real-Time Ethernet or fieldbus communication. Depending of the loaded firmware, the protocol specific PC card cifX proceeds the communication of the corresponding Real-Time Ethernet or fieldbus system.

The used Real-Time Ethernet systems are: The used fieldbus systems

- EtherCAT Master
- EtherCAT Slave
- EtherNet/IP Scanner (Master)
- EtherNet/IP Adapter (Slave)
- Open Modbus/TCP
- POWERLINK-Controlled-Node/Slave
- PROFINET IO-Controller (Master)
- PROFINET IO-Device (Slave)
- sercos Master
- sercos Slave
- VARAN Client (Slave)

- are:
- PROFIBUS DP Master
- PROFIBUS DP Slave
- PROFIBUS MPI Device
- CANopen Master
- CANopen Slave
- DeviceNet Master
- DeviceNet Slave

The PC card cifX handles the complete data exchange between the connected Ethernet or fieldbus devices and the PC. The data exchange is proceeded via dual-port memory.

3.2 PC Cards cifX with integrated Interfaces

The PC cards Compact PCI CIFX 80-XX and the PC cards PCI-104 CIFX 104C-XX and CIFX 104C-XX-R provide integrated Ethernet, fieldbus or diagnostic interfaces.

3.2.1 PC Cards Compact PCI CIFX 80-XX

PC Card cifX	Description	
PC Cards Compact PCI with integrated Ethernet, fieldbus or diagnostic interface		
Real-Time Ethernet		
CIFX 80-RE	PC Card cifX Compact PCI for Real-Time Ethernet Master or Slave	
PROFIBUS		
CIFX 80-DP	PC Card cifX Compact PCI PROFIBUS DP Master or Slave and PROFIBUS MPI Device	
CANopen		
CIFX 80-CO	PC Card cifX Compact PCI CANopen Master or Slave	
DeviceNet		
CIFX 80-DN	PC Card cifX Compact PCI DeviceNet Master or Slave	

Table 12: PC Cards Compact PCI CIFX 80-XX

3.2.2 PC Cards PCI-104: CIFX 104C-XX and CIFX 104C-XX-R

PC Card cifX	Description	
PC Cards PCI-104 with integrated Ethernet, fieldbus or diagnostic interface		
Real-Time Ethernet		
CIFX 104C-RE	PC Card cifX PCI-104 for Real-Time Ethernet Master or Slave	
CIFX 104C-RE-R	PC Card cifX PCI-104 for Real-Time Ethernet Master or Slave (connectors at the left side)	
PROFIBUS		
CIFX 104C-DP	PC Card cifX PCI-104 PROFIBUS DP Master or Slave and PROFIBUS MPI Device	
CIFX 104C-DP-R	PC Card cifX PCI-104 PROFIBUS DP Master or Slave and PROFIBUS MPI Device (connectors at the left side)	
CANopen		
CIFX 104C-CO	PC Card cifX PCI-104 CANopen Master or Slave	
CIFX 104C-CO-R	PC Card cifX PCI-104 CANopen Master or Slave (connectors at the left side)	
DeviceNet		
CIFX 104C-DN	PC Card cifX PCI-104 DeviceNet Master or Slave	
CIFX 104C-DN-R	PC Card cifX PCI-104 DeviceNet Master or Slave (connectors at the left side)	

Table 13: PC Cards PCI-104: CIFX 104C-XX and CIFX 104C-XX-R

3.3 PC Cards cifX with AIFX Assembly Interfaces

3.3.1 The Label "\F" in the Device Name

The PC cards cifX including the label "**\F**" in its device name are composed of a basic card and an assembly interface AIFX.

- The basic cards CIFX 90, CIFX 90E, CIFX 90E\ET, CIFX 90E\MR and, CIFX 90E\MR\ET are equipped with a Cable Connector Ethernet, to connect the Ethernet assembly interface (AIFX-RE), they are also equipped with a Cable Connector Fieldbus, to connect the PROFIBUS assembly interface (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN).
- The basic cards CIFX 104C-RE\F and CIFX 104C-RE-R\F are equipped with a Cable Connector Ethernet, to connect the Ethernet assembly interface (AIFX-RE).
- The basic cards CIFX 104C-FB\F and CIFX 104C-FB-R\F are equipped with a **Cable Connector Fieldbus**, to connect the PROFIBUS assembly interface (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN).
- The basic cards CIFX 104C-RE\F, CIFX 104C-RE-R\F, CIFX 104C-FB\F and CIFX 104C-FB-R\F are additionally equipped with a Cable Connector DIAG, to optionally connect the diagnostic assembly interface (AIFX-DIAG).



Important! Operating the PC cards cifX Mini PCI, Mini PCI Express or PCI-104 with AIFX assembly interface (label "**\F**" in the device name) requires proper connection of the Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface to the basic card!

3.3.2 PC Cards Mini PCI CIFX 90-XX\F

PC Card cifX	Description	
PC Cards cifX Mini PCI with AIFX Assembly Interface		
Real-Time Ethernet		
CIFX 90-RE\F	PC Card cifX Mini PCI Real-Time Ethernet Master or Slave, composed of - Basic card CIFX 90 and - Ethernet assembly interface (AIFX-RE).	
PROFIBUS		
CIFX 90-DP\F	PC Card cifX Mini PCI PROFIBUS DP Master or Slave or PROFIBUS MPI Devicet, composed of - Basic card CIFX 90 and - PROFIBUS assembly interface (AIFX-DP).	
CANopen		
CIFX 90-CO\F	PC Card cifX Mini PCI CANopen Master or Slave, composed of - Basic card CIFX 90 and - CANopen assembly interface (AIFX-CO).	
DeviceNet		
CIFX 90-DN\F	PC Card cifX Mini PCI DeviceNet Master or Slave, composed of - Basic card CIFX 90 and - DeviceNet assembly interface (AIFX-DN).	
Table 14: PC Cards Mini PCI CIFX 90-XX\F		

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013



Note: The device height and the power input of the PC cards CIFX 90-RE\F, CIFX 90-DP\F, CIFX 90-CO\F and CIFX 90-DN\F do not comply with the standard specifications.

3.3.3 PC Cards Mini PCI Express CIFX 90E-XX\F

PC Card cifX	Description		
PC Cards Mini PCI Express with AIFX Assembly Interface			
Real-Time Ethernet			
CIFX 90E-RE\F	PC Card cifX Mini PCI Express Real-Time Ethernet Master or Slave, composed of - Basic card CIFX 90E and - Ethernet assembly interface (AIFX-RE).		
PROFIBUS			
CIFX 90E-DP\F	PC Card cifX Mini PCI Express PROFIBUS DP Master or Slave or PROFIBUS MPI Device, composed of - Basic card CIFX 90E and - PROFIBUS assembly interface (AIFX-DP).		
CANopen			
CIFX 90E-CO\F	PC Card cifX Mini PCI Express CANopen Master or Slave, composed of - Basic card CIFX 90E and - CANopen assembly interface (AIFX-CO).		
DeviceNet			
CIFX 90E-DN\F	PC Card cifX Mini PCI Express DeviceNet Master or Slave, composed of - Basic card CIFX 90E and - DeviceNet assembly interface (AIFX-DN).		
	Table 15: PC Cards Mini PCI Express CIEX 00E XXIE		

Table 15: PC Cards Mini PCI Express CIFX 90E-XX\F



Note: The device height and the power input of the PC cards CIFX 90E-RE\F, CIFX 90E-DP\F, CIFX 90E-CO\F and CIFX 90E-DN\F do not comply with the standard specifications.

3.3.4 PC Cards cifX Mini PCI Express CIFX 90E-XX\ET\F

PC Card cifX	Description		
PC Cards Mini PCI Express with AIFX Assembly Interface and enlarged Temperature Range, ET'			
Real-Time Ethernet			
CIFX 90E-RE\ET\F	PC Card cifX Mini PCI Express Real-Time Ethernet Master or Slave with enlarged temperature range ,ET', composed of - Basic card CIFX 90E\ET and - Ethernet assembly interface (AIFX-RE).		
PROFIBUS			
CIFX 90E-DP\ET\F	PC Card cifX Mini PCI Express PROFIBUS DP Master or Slave or PROFIBUS MPI Device with enlarged temperature range ,ET', composed of - Basic card CIFX 90E\ET and - PROFIBUS assembly interface (AIFX-DP).		
CANopen			
CIFX 90E-CO\ET\F	PC Card cifX Mini PCI Express CANopen Master or Slave with enlarged temperature range ,ET', composed of - Basic card CIFX 90E\ET and - CANopen assembly interface (AIFX-CO).		
DeviceNet			
CIFX 90E-DN\ET\F	PC Card cifX Mini PCI Express DeviceNet Master or Slave with enlarged temperature range ,ET', composed of - Basic card CIFX 90E\ET and - DeviceNet assembly interface (AIFX-DN).		
	Table 16: PC Cards Mini PCI Express CIFX 90E-XX\ET\F		



Note: For the PC cards CIFX 90E-RE\ET\F, CIFX 90E-DP\ET\F, CIFX 90E-CO\ET\F, CIFX 90E-DN\ET\F the following is valid:

(1) The PC cards can be used in an enlarged temperature rage from -20°C to +70°C.
(2) The device height and the power input of the PC cards do not comply with the standard specifications.

3.3.5 PC Cards Mini PCI Express CIFX 90E-XX\MR\F

PC Card cifX	Description		
PC Cards Mini PCI Exp	oress with AIFX Assembly Interface and additional MRAM ,MR'		
Real-Time Ethernet			
CIFX 90E-RE\MR\F	PC Card cifX Mini PCI Express Real-Time Ethernet Master or Slave with additional MRAM ,MR', composed of - Basic card CIFX 90E\MR and - Ethernet assembly interface (AIFX-RE).		
PROFIBUS			
CIFX 90E-DP\MR\F	PC Card cifX Mini PCI Express PROFIBUS DP Master or Slave or PROFIBUS MPI Device with additional MRAM ,MR', composed of - Basic card CIFX 90E\MR and - PROFIBUS assembly interface (AIFX-DP).		
CANopen			
CIFX 90E-CO\MR\F	PC Card cifX Mini PCI Express CANopen Master or Slave with additional MRAM ,MR', composed of - Basic card CIFX 90E\MR and - CANopen assembly interface (AIFX-CO).		
DeviceNet			
CIFX 90E-DN\MR\F	PC Card cifX Mini PCI Express DeviceNet Master or Slave with additional MRAM ,MR', composed of - Basic card CIFX 90E\MR and - DeviceNet assembly interface (AIFX-DN).		
	Table 17: PC Cards Mini PCI Express CIFX 90E-XX\MR\F		
	Note: For the PC cards CIFX 90E-RE\MR\F, CIFX 90E-DP\MR\F, CIFX 90E-CO\MR\F and		



Note: For the PC cards CIFX 90E-RE\MR\F, CIFX 90E-DP\MR\F, CIFX 90E-CO\MR\F and CIFX 90E-DN\MR\F the following is valid:
(1) The PC cards are equipped with an MRAM (128Kbyte = 64K Words). For further information refer to section *PC Cards cifX with additional MRAM* on page 35.
(2) The device height and the power input of the PC cards do not comply with the standard specifications.

3.3.6 PC Cards Mini PCI Express CIFX 90E-XX\MR\ET\F

PC Card cifX	Description		
PC Cards Mini PCI Expr Range ,ET'	ess with AIFX Assembly Interface, additional MRAM ,MR' and enlarged Temperature		
Real-Time Ethernet			
CIFX 90E-RE\MR\ET\F	PC Card cifX Mini PCI Express Real-Time Ethernet Master or Slave with addition MRAM ,MR' and enlarged Temperature Range ,ET', composed of - Basic card CIFX 90E\MR\ET and - Ethernet assembly interface (AIFX-RE).		
PROFIBUS			
CIFX 90E-DP\MR\ET\F	PC Card cifX Mini PCI Express PROFIBUS DP Master or Slave or PROFIBUS MPI Devicet with additional MRAM ,MR' and enlarged Temperature Range ,ET', composed of - Basic card CIFX 90E\MR\ET and - PROFIBUS assembly interface (AIFX-DP).		
CANopen			
CIFX 90E-CO\MR\ET\F	PC Card cifX Mini PCI Express CANopen Master or Slave with additional MRAM ,MR' and enlarged Temperature Range ,ET', composed of - Basic card CIFX 90E\MR\ET and - CANopen assembly interface (AIFX-CO).		
DeviceNet			
CIFX 90E-DN\MR\ET\F	PC Card cifX Mini PCI Express DeviceNet Master or Slave with additional MRAM ,MR' and enlarged Temperature Range ,ET', composed of - Basic card CIFX 90E\MR\ET and - DeviceNet assembly interface (AIFX-DN).		
	Table 18: PC Cards Mini PCI Express CIFX 90E-XX\MR\ET\F		



Note: For the PC cards CIFX 90E-RE\MR\ET\F, CIFX 90E-DP\MR\ET\F, CIFX 90E-CO\MR\ET\F and CIFX 90E-DN\MR\ET\F the following is valid:
(1) The PC cards are equipped with an MRAM (128Kbyte = 64K Words). For further information refer to section *PC Cards cifX with additional MRAM* on page 35.
(2) The PC cards can be used in an enlarged temperature rage from -20 °C to +70 °C.
(3) The device height and the power input of the PC cards do not comply with the standard specifications.

3.3.7 PC Cards PCI-104: CIFX 104C-XX\F and CIFX 104C-XX-R\F

PC Card cifX	Description			
PC Cards PCI-104 with AIFX Assembly Interfaces				
Real-Time Ethernet				
CIFX 104C-RE\F	PC Card cifX PCI-104 for Real-Time Ethernet Master or Slave, composed of - Basic card CIFX 104C-RE\F and - Ethernet assembly interface (AIFX-RE).			
CIFX 104C-RE-R\F	PC Card cifX PCI-104 for Real-Time Ethernet Master or Slave (connectors at the left side), composed of - Basic card CIFX 104C-RE-R\F and - Ethernet assembly interface (AIFX-RE).			
PROFIBUS				
CIFX 104C-DP\F	PC Card cifX PCI-104 for PROFIBUS DP Master or Slave or PROFIBUS MPI Device, composed of - Basic card CIFX 104C-FB\F and - PROFIBUS assembly interface (AIFX-DP).			
CIFX 104C-DP-R\F	PC Card cifX PCI-104 for PROFIBUS DP Master or Slave or PROFIBUS MPI Device (connectors at the left side), composed of - Basic card CIFX 104C-FB-R\F and - PROFIBUS assembly interface (AIFX-DP).			
CANopen				
CIFX 104C-CO\F	PC Card cifX PCI-104 for CANopen Master or Slave, composed of - Basic card CIFX 104C-FB\F and - CANopen assembly interface (AIFX-CO).			
CIFX 104C-CO-R\F	PC Card cifX PCI-104 for CANopen Master or Slave (connectors at the left side), composed of - Basic card CIFX 104C-FB-R\F and - CANopen assembly interface (AIFX-CO).			
DeviceNet				
CIFX 104C-DN\F	PC Card cifX PCI-104 for DeviceNet Master or Slave, composed of - Basic card CIFX 104C-FB\F and - DeviceNet assembly interface (AIFX-DN).			
CIFX 104C-DN-R\F	PC Card cifX PCI-104 for DeviceNet Master or Slave (connectors at the left side), composed of - Basic card CIFX 104C-FB-R\F and - DeviceNet assembly interface (AIFX-DN).			

Table 19: PC Cards PCI-104: CIFX 104C-XX\F and CIFX 104C-XX-R\F

3.3.8 AIFX Assembly Interfaces

AIFX	Description	For the PC Cards cifX
AIFX-RE	Ethernet Assembly Interface (with Ethernet interface)	CIFX 90-RE\F, CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F, CIFX 104C-RE\F, CIFX 104C-RE-R\F
AIFX-DP	PROFIBUS Assembly Interface (with PROFIBUS interface)	CIFX 90-DP\F, CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F, CIFX 104C-DP\F, CIFX 104C-DP-R\F
AIFX-CO	CANopen Assembly Interface (with CANopen interface)	CIFX 90-CO\F, CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F
AIFX-DN	DeviceNet Assembly Interface (with DeviceNet interface)	CIFX 90-DN\F, CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F
AIFX-DIAG (optional)	Diagnostic Assembly Interface (with diagnostic interface)	CIFX 104C-RE\F, CIFX 104C-RE-R\F CIFX 104C-DP\F, CIFX 104C-DP-R\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F

Table 20: AIFX Assembly Interfaces for PC Cards cifX with Cable Connector

3.4 The Function "DMA Mode"

Device revisions which provide **DMA Mode** are listed separately in section *Hardware: PC Cards cifX, AIFX Assembly Interfaces* on page 11.

The DMA Mode is activated via the device driver cifX Device Driver.



For further information refer to the user manual **Software Installation for the PC Cards cifX** in section *Activating DMA Mode in the cifX Device Driver Setup.*

Requirements

For device revisions providing the **DMA Mode** the required versions of the firmware, the driver and the SYCON.net setup must be used:

PC Card cifX	From Hardware Revision	Firmware File	Protocol	From Firmware Version
CIFX 80-RE,	1	CIFXECM.NXF	EtherCAT Master	2.4.6.0
CIFX 90-RE\F,	,	CIFXECS.NXF	EtherCAT Slave	2.5.5.0
CIFX 90E-RE\F,	A	CIFXEIM.NXF	EtherNet/IP Scanner	2.2.x.x
CIFX 90E-RE\ET\F	1	CIFXEIS.NXF	EtherNet/IP Adapter	2.3.x.x
CIFX 90E-RE\MR\F	1	CIFXOMB.NXF	Open Modbus/TCP	2.4.x.x
CIFX 90E-RE\MR\ET\F	1	CIFXPLS.NXF	POWERLINK Controlled Node	2.1.24.0
CIFX 104C-RE, CIFX 104C-RE-R,	1	CIFXPNM.NXF	PROFINET IO Controller	2.3.x.x
CIFX 104C-RE\F,		CIFXPNS.NXF	PROFINET IO Device	3.4.x.x (V3)
CIFX 104C-RE-R\F		CIFXS3M.NXF	sercos Master	2.0.15.0
		CIFXS3S.NXF	sercos Slave	3.0.15.0
		CIFXVRS.NXF	VARAN Client	1.0.x.x
CIFX 80-DP,	1	CIFXDPM.NXF	PROFIBUS DP Master	2.3.x.x
CIFX 90-DP\F,	1	CIFXDPS.NXF	PROFIBUS DP Slave	2.3.x.x
CIFX 90E-DP\F, CIFX 90E-DP\ET\F CIFX 90E-DP\MR\F CIFX 90E-DP\MR\F CIFX 90E-DP\MR\ET\F CIFX 104C-DP, CIFX 104C-DP-R, CIFX 104C-DP\F, CIFX 104C-DP-R\F	5 1 1 1	CIFXMPI.NXF	PROFIBUS MPI Device	not supported
CIFX 80-CO,	1	CIFXCOM.NXF	CANopen Master	2.3.x.x
CIFX 90-CO\F, CIFX 90E-CO\F, CIFX 90E-CO\ET\F CIFX 90E-CO\MR\F CIFX 90E-CO\MR\ET\F CIFX 104C-CO, CIFX 104C-CO-R, CIFX 104C-CO\F, CIFX 104C-CO-R\F	1 4 1 1 1	CIFXCOS.NXF	CANopen Slave	2.3.x.x
CIFX 80-DN,	1	CIFXDNM.NXF	DeviceNet Master	2.2.x.x
CIFX 90-DN\F, CIFX 90E-DN\F, CIFX 90E-DN\ET\F CIFX 90E-DN\MR\F CIFX 90E-DN\MR\ET\F CIFX 104C-DN, CIFX 104C-DN-R, CIFX 104C-DN\F, CIFX 104C-DN-R\F	1 4 1 1 1	CIFXDNS.NXF	DeviceNet Slave	2.2.x.x

Table 21: Firmware Versions for the DMA Mode

Driver and Software		Version or higher
cifX Device Driver	cifX Device Driver Setup.exe	0.95x
SYCON.net	SYCONnet netX setup.exe	V1.201.x.x

Table 22: Versions Driver and SYCON.net for the DMA Mode

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

3.5 PC Cards cifX with additional MRAM

The PC cards CIFX 90E-XX\MR\F (CIFX 90E-RE\MR\F, CIFX 90E-DP\MR\F, CIFX 90E-CO\MR\F and CIFX 90E-DN\MR\F) or CIFX 90E-XX\MR\ET\F (CIFX 90E-RE\MR\ET\F, CIFX 90E-DP\MR\ET\F, CIFX 90E-CO\MR\ET\F and CIFX 90E-DN\MR\ET\F) are identical to the PC cards CIFX 90E-XX and work with the same firmware. However, the PC cards CIFX 90E-XX\MR or CIFX 90E-XX\MR\ET\F have an additional memory module for storing remanent data, MRAM with 128Kbyte (= 64K words). Using the cifX Device Driver (from Version 1.1.1.0) access from the application program to this memory is possible and it can be used as a remanent memory for the host system.

3.6 System Requirements

3.6.1 Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104

PC with slot (3.3 V) for PC cards cifX *Compact PCI, Mini PCI, Mini PCI Express* and *PCI-104*:

PC Cards cifX		PCI Bus [Pins]	Slot
CIFX 80-RE CIFX 80-DP CIFX 80-CO CIFX 80-DN		110	Compact PCI Slot (3.3 V)
CIFX 90-RE\F CIFX 90-DP\F CIFX 90-CO\F CIFX 90-CN\F		124	Mini PCI Socket (3.3 V), Type III System Connector
CIFX 90E-RE\F CIFX 90E-DP\F CIFX 90E-CO\F CIFX 90E-DN\F CIFX 90E-RE\MR\F CIFX 90E-DP\MR\F CIFX 90E-CO\MR\F CIFX 90E-DN\MR\F	CIFX 90E-RE\ET\F CIFX 90E-DP\ET\F CIFX 90E-CO\ET\F CIFX 90E-DN\ET\F CIFX 90E-RE\MR\ET\F CIFX 90E-DP\MR\ET\F CIFX 90E-CO\MR\ET\F CIFX 90E-DN\MR\ET\F	52	PCI Express Mini System Connector (3.3 V), X1/X2 ² = One Lane Note: As the basic card CIFX 90E (all variants 'MR' or 'ET') can be inserted correctly into the Mini PCI Express slot, the element height in the Mini PCI Express slot of the connecting device must meet to the standard specifications.
CIFX 104C-RE CIFX 104C-RE-R CIFX 104C-RE\F CIFX 104C-RE-R\F CIFX 104C-DP CIFX 104C-DP-R CIFX 104C-DP\F CIFX 104C-DP-R\F	CIFX 104C-CO CIFX 104C-CO-R CIFX 104C-CO\F CIFX 104C-CO-R\F CIFX 104C-DN CIFX 104C-DN-R CIFX 104C-DN\F CIFX 104C-DN-R\F	120	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Voltage 5 V or 3.3 V)

Table 23: Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104

 $^{^{2}}$ X1, X2 corresponds to the Hilscher convention for "interface" on the top or the bottom side of the PC Card cifX.

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

3.6.2 Notes on the Card Height - PC Card cifX Mini PCI and Mini PCIe

PC Cards cifX	Dimensions (L x B x D)				Note
CIFX 90-RE\F CIFX 90-DP\F CIFX 90-CO\F CIFX 90-DN\F	60,0 x 45 x 9,5 mm				The element heights <u>on top</u> of the PC cards CIFX 90-RE\F, CIFX 90-DP\F, CIFX 90-CO\F or CIFX 90-DN\F Mini PCI do not meet the standard specifications.
CIFX 90E-RE\F CIFX 90E-DP\F CIFX 90E-CO\F CIFX 90E-DN\F CIFX 90E-RE\ET\F CIFX 90E-DP\ET\F CIFX 90E-CO\ET\F CIFX 90E-DN\ET\F CIFX 90E-DP\MR\F CIFX 90E-CO\MR\F CIFX 90E-CO\MR\F CIFX 90E-CO\MR\ET\F CIFX 90E-DP\MR\ET\F CIFX 90E-CO\MR\ET\F CIFX 90E-CO\MR\ET\F CIFX 90E-DN\MR\ET\F	CIFX 90E-XX\F from hardw CIFX 90E-XX\MR\F from hardw S1 x (30,2 +/- 0,1) x 11 mm CIFX 90E-XX\ET\F from hard CIFX 90E-XX\MR\ET\F from 51 x (30,2 +/- 0,1) x 12,5 m 51 x (30,2 +/- 0,1) x 12,5 m Element height topside Height of the printed circuit Element height topside Height of the printed circuit Element height bottom side Max. total height (D) of the PC card cifX in milli meters	rdware re rdware rev n hardware	vision B:	: 	The element heights <u>on top</u> of the PC card CIFX 90E-XX\F Mini PCI Express (all variants) ³ do not meet the standard specifications. Note: The element height <u>at the bottom</u> <u>side</u> of the PC card CIFX 90E-XX\F (all variants) is in accordance with the standard specifications. As the PC card CIFX 90E-XX\F (all variants) can be inserted correctly into the Mini PCI Express slot, the element height in the Mini PCI Express slot of the connecting device must meet to the standard specifications. Note: The element heights of the cable connectors Ethernet (X4) or Fieldbus (X3) including the cable are appr. 8,5 mm over PCB each.

Table 24: Notes on the Card Height - PC Card cifX Mini PCI and Mini PCI Express

³ Variants of the PC card CIFX 90E-XX\F: CIFX 90E-XX\ET\F, CIFX 90E-XX\MR\F or CIFX 90E-XX\MR\ET\F (XX = RE, DP, CO or DN)

3.6.3 Panel Cutout for Installing AIFX

In order to connect the AIFX assembly interface to a PC card cifX **Mini PCI** and **Mini PCI Express** with cable connector Ethernet or fieldbus (labeling "\F"), make sure that **the housing panel of the PC or of the connecting device** has an appropriate cutout and holes for fastening the AIFX.

In order to connect the AIFX assembly interface to a PC card cifX **PCI-104** with cable connector Ethernet or fieldbus (labeling $_{n}F^{*}$), make sure that **the front plate of the PC cabinet** has an appropriate cutout and holes for fastening the AIFX.

PC Cards cifX	Panel Cutout
Mini PCI; Mini PCI Express	at the housing panel of the PC
PCI-104	at the front plate of the PC cabinet

Table 25: Panel Cutout at the at the Housing Panel of the PC or at the front plate of the PC cabinet

The panel cutout must be dimensioned sufficiently large for the interface, display or control elements placed on the AIFX. Partial standard cutouts can be used.

PC Cards cifX	AIFX	Panel Cutout and H	loles
CIFX 90-RE\F CIFX 90E-RE\F CIFX 90E-RE\ET\F	AIFX-RE	Required Cutout	for two RJ45 Sockets Important! The panel cutout layout must also cover the LEDs COM 0 and COM1 at the AIFX-RE.
CIFX 90E-RE\MR\F CIFX 90E-RE\MR\ET\F		Standard cutout	D-Sub-15
CIFX 104C-RE\F CIFX 104C-RE-R\F		Holes	2, distance between the holes 37,3 mm
		Further Information	In the data sheet <i>MOD JACK – MJIM</i> [2], as well as in section as in section <i>Ethernet - AIFX-RE</i> on page 59.
CIFX 90-DP\F	AIFX-DP	Required Cutout	for Dsub female Connector, 9 pin
CIFX 90E-DP\F CIFX 90E-DP\ET\F		Standard cutout	D-Sub-9
CIFX 90E-DP\MR\F		Holes	2, distance between the holes 25 mm
CIFX 90E-DP\MR\ET\F CIFX 104C-DP\F CIFX 104C-DP-R\F		Further Information	in section PROFIBUS - AIFX-DP on page 60.
CIFX 80-CO CIFX 90-CO\F	AIFX-CO	Required Cutout	for D-Sub male Connector, 9 pin
CIFX 90E-CO\F CIFX 90E-CO\ET\F		Standard cutout	D-Sub-9
CIFX 90E-CO\MR\F		Holes	2, distance between the holes 25 mm
CIFX 90E-CO\MR\ET\F CIFX 104C-CO\F CIFX 104C-CO-R\F		Further Information	in section CANopen - AIFX-CO on page 61.
CIFX 90-DN\F	AIFX-DN	Required Cutout	for CombiCon male Connector, 5 pin
CIFX 90E-DN\F CIFX 90E-DN\ET\F		Standard cutout	D-Sub-9
CIFX 90E-DN\MR\F		Holes	2x2, distance between the holes 24,94 mm
CIFX 90E-DN\MR\ET\F CIFX 104C-DN\F CIFX 104C-DN-R\F		Further Information	in section <i>DeviceNet - AIFX-DN</i> on page 62.
CIFX 104C-RE\F CIFX 104C-RE-R\F	AIFX-DIAG	Required Cutout	for the light channels, the rotary switches and the Mini B USB plug
CIFX 104C-DP\F CIFX 104C-DP-R\F		Standard cutout	-
		Holes	2, distance between the holes 47,1 mm
CIFX 104C-CO-R\F CIFX 104C-DN\F CIFX 104C-DN-R\F		Further Information	in section <i>Diagnostic - AIFX-DIAG</i> on page 63.

Table 26: Required Panel Cutout and Holes for AIFX

3.6.4 **Power Supply and Host Interface**

For the power supply and the host interface used for the PC cards cifX *Compact PCI, Mini PCI, Mini PCI Express* and *PCI-104* you must observe the following requirements:

PC Cards cifX		Supply Voltage	Signaling Voltage Host Interface	Host Interface (PCI slot)
CIFX 80-RE CIFX 80-DP CIFX 80-CO CIFX 80-DN		+3.3 V dc ±5 %/ Max. 1 A	5 V or 3.3 V	Compact PCI
CIFX 90-RE\F CIFX 90-DP\F CIFX 90-CO\F CIFX 90-DN\F		+3.3 V dc ±5 %/ Max. 1 A Note: The power input of the CIFX 90-XX\F does not comply with the standard specifications.	3.3 V (5 V only if 5 V are connected via pin 28. Refer also to section <i>Pin Assignment for Mini</i> <i>PCI Bus, X1</i> , page 109)	Mini PCI
CIFX 90E-RE\F CIFX 90E-DP\F CIFX 90E-CO\F CIFX 90E-CN\F CIFX 90E-RE\MR\F CIFX 90E-RE\MR\F CIFX 90E-DP\MR\F CIFX 90E-CO\MR\F	CIFX 90E-RE\ET\F CIFX 90E-DP\ET\F CIFX 90E-CO\ET\F CIFX 90E-DN\ET\F CIFX 90E-RE\MR\ET\F CIFX 90E-DP\MR\ET\F CIFX 90E-CO\MR\ET\F CIFX 90E-DN\MR\ET\F	+3.3 V dc \pm 5 %/ Max. 1 A Note: The power input of the CIFX 90E-XX\F (all varainats) ⁴ does not comply with the standard specifications.	PCIe compatible	Mini PCI Express
CIFX 104C-RE CIFX 104C-RE-R CIFX 104C-RE\F CIFX 104C-RE-R\F CIFX 104C-DP CIFX 104C-DP-R CIFX 104C-DP\F CIFX 104C-DP\F	CIFX 104C-CO CIFX 104C-CO-R CIFX 104C-CO\F CIFX 104C-CO-R\F CIFX 104C-DN CIFX 104C-DN-R CIFX 104C-DN\F CIFX 104C-DN-R\F	+5 V dc ±5 %/ Max. 750 mA or +3.3 V dc ±5 %/ Max. 1 A	5 V or 3.3 V	PCI-104

Table 27: Requirements Power Supply and Host Interface for PC Cards cifX Compact PCI, Mini PCI, Mini PCI Express and PCI-104

The data in the on page have the following meaning:

Supply Voltage

The required and permissible supply voltage at the PC cards cifX *Compact PCI, Mini PCI, Mini PCI Express* and *PCI-104*.



Note: To ensure that the compatibility between different systems is guaranteed, providing a maximum of 1 A (for +3,3 VDC \pm 5 %) or 750 mA (for +5 VDC \pm 5 %) is recommended.

The typical current depends on the type of the PC card cifX. For detailed values on the typical current see section *Technical Data PC Cards cifX* on page 113.

Signaling Voltage Host Interface

The required or tolerated signaling voltage at the I/O signal pins

- at the Compact PCI bus of the PC cards cifX Compact PCI,
- at the Mini PCI bus of the PC cards cifX Mini PCI,
- at the Mini PCI Express bus of the PC cards cifX Mini PCI Express
- as well as at the PCI-104 bus of the PC cards cifX PCI-104.

Host Interface (PCI slot) Type of the host interface

⁴ Variants of the PC card CIFX 90E-XX\F: CIFX 90E-XX\ET\F, CIFX 90E-XX\MR\F or CIFX 90E-XX\MR\ET\F (XX = RE, DP, CO or DN)

3.7 Requirements for Operation of the PC Cards cifX

Operating the PC cards cifX properly, the following described requirements must be fulfilled.

	must be fullilled.						
Protocols	EtherCAT Slave, EtherCAT Master, EtherNet/IP Adapter (Slave), EtherNet/IP Scanner (Master), Open Modbus/TCP, POWERLINK-Controlled-Node/Slave, PROFINET IO Device (Slave), PROFINET IO Controller (Master),	sercos Slave, sercos Master, VARAN Client (Slave) PROFIBUS DP Slave, PROFIBUS DP Master, PROFIBUS MPI Device, CANopen Slave, CANopen Master,	DeviceNet Slave, DeviceNet Master, AS-Interface Master; CC-Link Slave, CompoNet Slave				
Software Installation	 1. Driver for the Host Interface Host Interfaces: Compact PCI, Mini PCI, Mini PCI Express and PCI-104 The device driver cifX Device Driver must be installed (from V1.0.x.x). If you install the device into a PC, in general Windows[®] will be available as operating system. In this case the cifX Device Driver must be installed to communicate to the device and to exchange data via the dual-port memory. 						
	version indicated in section	versions of the cifX Device Driv Driver and Software on page 12					
	OR • If Windows [®] is not available as opera Driver Toolkit and this driver must be i • For the operating systems Linux, Window buy Device Driver at the company Hils <u>http://www.hilscher.com/</u> . 2. The configuration software SYCON Configuration Tool must be installed (Slave) can be parameterized.	installed. ndows [®] CE, VxWorks, QNX and scher Gesellschaft für Systemau I. net or alternatively the simple S	IntervalZero RTX ™ you can tomation mbH slave configuration tool netX				
How to use the Software	On how to use the software for the co the following notice:	nfiguration, the firmware downloa	ad and for the diagnosis, note				
	Important! The USB interface, the serial interface as well as the cifX Device Driver may only be used exclusively by one software, that is - the SYCON.net configuration software (with integrated ODMV3) or - the netX Configuration Tool or - the cifX Test Application or - the cifX Driver Setup Utility or - the application program. Never use the listed software simultaneously, otherwise this will result in communication problems with the device. If the SYCON.net configuration software was used on the PC, then stop the ODMV3 service before you use one of the other software listed above. Therefore, select Service > Stop from the context menu of the ODMV3 system tray icon.						
Firmware Download	3. Using the configuration software St tool netX Configuration Tool , the use						
Parameter Setting	 4. The PC card cifX must be parameterized using one of the following options: Configuration Software SYCON.net alternatively Slave configuration tool netX Configuration Tool (only Slave) Application program (programming required) 						
Communica- tion	5. For the communication of a PC card cifX (Slave) a Master device for the respective communication system is required. For the communication of a PC card cifX (Master) a Slave device for the respective communication system is required.						
Hardware Installation	Important! Operating the PC cards cifX with Cable Connector Ethernet or with the Cable Connector Fieldbus (label "\F" in its device name) requires proper connection of the Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface to the basic card!						
Environmental Conditions	Due to a plug element from ERNI the Real-Time Ethernet is 0 °C. This appli Ethernet.						
		anarata BC Carda aif V proparty					

Table 28: Requirements to operate PC Cards cifX properly

4 Getting Started

4.1 Installation and Configuration PC Card cifX

The following table describes the steps for the software and hardware installation and for the configuration of a PC card cifX Compact PCI, Mini PCI, Mini PCI Express and PCI-104 (Master and Slave) Real-Time Ethernet and fieldbus as it is typical for many cases. The Slave device can be configured using the corresponding Slave DTM in the configuration software **SYCON.net**. Alternatively, you can also use the simple Slave configuration tool **netX Configuration Tool**. The Master device can be configured using the corresponding Master DTM in the configuration software **SYCON.net**.

#	Step	Description	For detailed information see manual / section	Page
1	Installing Driver and Software			
1.1	Installing cifX Device Driver	Enter the Communication Solutions DVD in the PC and follow to the instructions of the installation wizard, to install the driver.	Refer to User Manual Software Installation for the PC Cards cifX	
1.2	Installing USB Driver Depending by device type / features	Only for PC Cards cifX Compact PCI, PCI-104 equipped with an USB interface or with the diagnostic assembly interface (AIFX-DIAG)		
1.3	Installing SYCON.net	For PC Cards cifX Master or Slave: Run the SYCON.net-Setup and follow to the instructions of the installation wizard.		
1.4	Installing netX	For PC Cards cifX Slave:		
	Configuration Tool	Start the netX Configuration Tool setup program to install the netX Configuration Tool .		
2	Preparing Hardware Installation			
2.1	Take precautions on Electrostatically sensitive Devices	NOTICE Electrostatically sensitive Devices Make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/ uninstall the PC Card cifX.	Electrostatically sensitive Devices	25
2.2	Glue sticker on the front plate.	For CIFX 80-RE	Fix Front Plate Sticker at CIFX 80-RE	66
2.3	Set PCI-104 Slot Number.	For PC Cards cifX PCI-104: Set physical PCI-104 Slot Number .	Rotary Switch for PCI- 104 Slot Number	104
3	Hardware Settings	Installing cifX. Take required safety precautions.	Hardware Installation and Uninstalling	64
3.1	Take safety precautions	Lethal Electrical Shock caused by parts with more than 50V! Disconnect the power plug of the PC or of the connecting device. Make sure, that the power supply is off at the PC or at the connecting device.	Electrical Shock Hazard	23

	gotarioa			
#	Step	Description	For detailed information see manual / section	Page
3.2	Open cabinet	Now open the cabinet of the PC or of the connecting device.	Installing PC Cards cifX Compact PCI,	67,
3.3	Installing cifX	Plug in and mount the PC card cifX.	Installing PC Cards cifX Mini PCI and Mini PCI Express	69
3.4	If so, plug module	For PC Cards cifX PCI-104: (a) Install the first PCI-104 module on the mainboard. (b) Install any other PCI-104 module on the respective underlying PCI-104 module.	Installing PC Cards cifX PCI-104	73
3.5	Connect AIFX (only for PC Cards cifX Mini PCI, Mini PCI Express and PCI-104 with label "\F" in its device name and with Cable Connector Ethernet X4 or Fieldbus X3)	Important! Operating the PC cards cifX with AIFX assembly interface requires proper connection of the AIFX-RE, AIFX-DP, AIFX-CO or AIFX-DN assembly interface to the basic card! If so, connect an Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface. If so, additionally connect a diagnostic	The Label "₩" in the Device Name	29
		assembly interface (AIFX-DIAG). To the basic card of each PC card cifX first connect the AIFX-RE, AIFX-DP, AIFX-CO, AIFX-DN or AIFX-DIAG assembly interface to the PC card cifX and check if the connector is plugged in correctly. For PC cards PCI-104: Only then plug another PCI-104 module.		
3.6	Close cabinet	Close the cabinet of the PC or connecting device.		
3.7	Plug the connecting cable to the Master or Slave	Note for all PC Cards cifX Real-Time Ethernet: Note! The RJ45 socket is only for use in LAN, not for telecommunication circuits. Note for PC Cards cifX PROFINET IO Controller:	Ethernet Interface	100
		Important for Hardware Wiring! Connect only ports with each other, which have different cross-over set- tings. Otherwise a connection bet- ween the devices can not be estab- lished. If the port settings of the PC card cifX PROFINET IO controller are not set to AUTO, then Port0 is switched uncrossed and Port1 crossed. Plug in the connecting cable from the PC card cifX to the PC card Master or Slave.	See corresponding user manual	
3.8	Connect the PC to the power / switch on.	Connect the PC or the connecting device to the power supply and switch it on.		
4	Hardware Settings	Hardware Settings in the Driver Setup		
4.1	DMA Mode in the cifX Device Driver Setup	Activate the DMA Mode in the cifX Device Driver Setup.	Refer to User Manual Software Installation for the PC Cards cifX	
5	Notice on how to use the Software	Use only one Software.		

#	Step	Description	For detailed information see	Page
			manual / section	
5.1	For the configuration, the firmware download and for the diagnosis, note:	Important! To avoid communication problems with the device, use the <u>USB interface</u> , the <u>serial interface</u> as well as the <u>cifX Device Driver</u> exclusively with one software, which is SYCON.net or netX Configuration Tool .	Requirements for Operation of the PC Cards cifX	39
6	Configuring Slave using SYCON.net	Download Firmware and Configuration Use the corresponding Slave DTM in the configuration software SYCON.net .		
6.1	Firmware Download	 Start configuration software SYCON.net, Create new project /Open existing project, Insert Slave into configuration, Select driver and assign device. Select and download the firmware. 	See corresponding user manual	45
	Firmware Slave:	EtherCAT Slave,PROFIBUS DP Slave,EtherNet/IP Adapter,PROFIBUS MPIOpen Modbus/TCP,Device,POWERLINK-CANopen Slave,Controlled-Node/Slave,DeviceNet Slave,PROFINET IO Device,CompoNet Slave,sercos Slave,CC-Link SlaveVARAN Client,CompoNet Slave		
6.2	Configuration cifX (Slave)	-Configure the PC card cifX (Slave).		
6.3	Download Configuration	- Download the configuration to the PC card cifX (Slave)		
7	OR Configuring Slave using netX Configuration Tool	Download Firmware and Configuration		
7.1	Downloading Firmware and Configuration (Slave)	If SYCON.net was already used on the PC, stop the ODMV3 service. Therefore, select Service > Stop from the context menu of the ODMV3 system tray icon. Start ODM Diagnostic Stop Restart Help About Close	Requirements for Operation of the PC Cards cifX	39
		In the netX Configuration Tool : - select the Firmware protocol, - Set the PC card cifX (Slave) parameters. - Select Apply . The selected firmware and the configuration are downloaded to the replacement card cifX. The configuration is saved to the hard disk of the PC.	See Operating Instruction Manual netX Configuration Tool for cifX, comX and netJACK	
8	Configuring Master using SYCON.net	Download Firmware and Configuration Use the corresponding Master DTM in the configuration software SYCON.net .		

Getting Started

#	Step	Description	For detailed information see manual / section	Page
8.1	Firmware Download	 Start configuration software SYCON.net, Create new project /Open existing project, Insert Master into configuration, Select driver and assign device. Select and download the firmware. 	See corresponding user manual Device Names in SYCON.net	45
	Firmware Master:	EtherCAT Master,PROFIBUS DP Master,EtherNet/IP Scanner,CANopen Master,PROFINET IODeviceNet Master,Controller,AS-Interface Master		
8.3	Configuration cifX (Master)	- Configure the PC card cifX (Master).	Notes for the Configuration of the	44
8.4	Download Configuration	- Download the configuration to the PC card cifX (Master).	Master Device	
9	Diagnosis by SYCON.net (Slave and Master)	Diagnosis, I/O Data Use the corresponding Slave or Master DTM in the configuration software SYCON.net .		
9.1	Diagnosis Steps (Master and Slave)	 In netDevice rightclick on device symbol. Select context menu entry Diagnosis, then select Diagnosis > General or Firmware Diagnosis, or select Diagnosis > Extended Diagnosis. 	See corresponding user manual	
9.2	I/O Monitor	 In netDevice rightclick on device symbol. Select context menu entry Diagnosis, then Tools > IO Monitor. Check the input or output data. 		
10	OR Slave Diagnosis by netX Configuration Tool (only Slave)	Diagnosis		
10.1	Diagnosis Steps (Slave)	If SYCON.net was already used on the PC, stop the ODMV3 service. Therefore, select Service > Stop from the context menu of the ODMV3 system tray icon.		
		In the netX Configuration Tool : - In the navigation area click on Diagnostic , - click in the Diagnostic pane to Start , to start the communication to the Master device and to run the diagnosis. - click on Extended , to run the extended diagnosis.	See Operating Instruction Manual netX Configuration Tool for cifX, comX and netJACK	

Table 29: Steps for the Software and Hardware Installation, the Configuration and for the Diagnosis of a PC Card cifX Compact PCI, Mini PCI, Mini PCI Express and PCI-104 (Master and Slave)

4.2 Note on Exchange Service (Replacement Case)

For the exchange service (replacement case) of a PC card cifX (Master and Slave) obey to the following note.



Important! For PC cards cifX in terms of a device exchange service (replacement case) you must manually download the same firmware and configuration into the replacement card cifX, as into the preceding cifX.

4.3 Notes for the Configuration of the Master Device

To configure the Master, a device description file is required. Please note the following notes for the configuration of the Master Device:

System	Note
EtherCAT Slave	To configure the Master, an XML file (device description file) is required. The settings in the used Master must comply with the settings in the Slave to establish communication. Important parameters are: Vendor ID, Product Code, Serial Number, Revision Number, Output and Input Data Bytes.
	If the XML file <i>Hilscher cifX RE ECS V2.2.x.xml</i> is use/updated, the firmware with the version <i>2.2.x</i> must be use/updated.
	The loadable firmware supports for the number of cyclic input data and for cyclic output data in total up to 400 bytes. If more than 200 bytes for input data or for output data should be exchanged via EtharCAT, then a customer specific XML file is necessary. Additionally the following formular applies: (number of input bytes + 3)/4 + (number of output bytes + 3)/4 must be less or equal to 100.
EtherNet/IP Adapter	To configure the Scanner/Master, an EDS file (device description file) is required. The settings in the used Scanner/Master must comply with the settings in the Adapter/Slave to establish communication. Important parameters are: Input, Output Data Bytes, Vendor ID, Product Type, Product Code, Major Rev, Minor Rev, IP Address and Netmask.
POWERLINK- Controlled- Node/Slave	To configure the Managing Node/Master, an XDD file (device description file) is required. The settings in the used Managing Node/Master must comply with the settings in the Controlled Node/Slave, to establish communication. Important parameters are: Vendor ID, Product Code, Serial Number, Revision Number, Node ID, Output and Input length.
PROFINET IO Device	To configure the Controller, a GSDML file (device description file) is required. The settings in the used Controller must comply with the settings in the Device to establish communication. Important parameters are: Station Name, Vendor ID, Device ID, Input and Output Data Bytes.
	Under Name of Station, the name must be typed which was also used in the configuration file of the master of this device. If no name chosen freely is used in the configuration file, then the name from the GSDML file is used.
sercos Slave	The sercos Master uses the sercos address to communicate with the slave. Some Masters will verify Device ID, Vendor Code, Input Data Size and Output Data Size and will do further communication to the Slave only if all these values match. Therefor the Master reads these parameters from the Slave and compares them with the configuration stored in the Master.
	The parameters Device ID, Vendor Code, Input Data Size and Output Data Size are part of the SDDML device description file. If for the configuration of the sercos Master SDDML files are used and a default value of one of these parameters was changed, then a SDDML file must be created in the configuration software via Export SDDML and then used in the configuration of the SERCOS Master.
PROFIBUS DP Slave	To configure the Master, a GSD file (device description file) is required. The settings in the used Master must comply with the settings in the Slave to establish communication. Important parameters are: Station Address, Ident Number, Baudrate and Config Data (the configuration data for the output and input length).
CANopen Slave	To configure the Master, an EDS file (device description file) is required. The settings in the used Master must comply with the settings in the Slave to establish communication. Important parameters are: Node Address and Baudrate.
DeviceNet Slave	To configure the Master, an EDS file (device description file) is required. The settings in the used Master must comply with the settings in the Slave to establish communication. Important parameters are: MAC ID, Baudrate, Produced Size, Consumed Size, Vendor ID, Product Type, Product Code, Major Rev, Minor Rev.
CC-Link Slave	To configure the Master, a CSP file (device description file) is required. The settings in the used Master must comply with the settings in the Slave to establish communication. Important parameters are: Slave Station Address, Baudrate, Station Type and Vendor Code.
CompoNet Slave	To configure the Master, an EDS file (device description file) is required. The settings in the used Master must comply with the settings in the Slave to establish communication. Important parameters are: Node Mode, MAC ID, Baudrate, Produced Data, Consumed Data, Vendor ID, Product Type, Product Code, Major Rev, Minor Rev.

Table 30: Notes for the Configuration of the Master Device

Further information to the device description files you find under section on *Device Description Files cifX* page 17.

4.4 Device Names in SYCON.net

The following table contains the device names displayed for the single communication protocols in the configuration software SYCON.net.

The table shows the PC card cifX and which protocol can be used. Furthermore, the table shows, for which protocol which device must be selected from the device catalog to configure the PC card cifX with SYCON.net.

PC Cards cifX	Protocol	DTM Specific Group	Device Name in SYCON.net
CIFX 80-RE,	EtherCAT Master	Master	CIFX RE/ECM
CIFX 90-RE\F, CIFX 90E-RE\F,	EtherCAT Slave	Gateway/Stand-Alone Slave	CIFX RE/ECS
CIFX 90E-RE\ET\F,	EtherNet/IP Scanner (Master)	Master	CIFX RE/EIM
CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F,	EtherNet/IP Adapter (Slave)	Gateway/Stand-Alone Slave	CIFX RE/EIS
CIFX 90E-RE(WIRKET), CIFX 104C-RE,	Open Modbus/TCP	Gateway/Stand-Alone Slave	CIFX RE/OMB
CIFX 104C-RE-R, CIFX 104C-RE\F,	POWERLINK-Controlled-Node/Slave	Gateway/Stand-Alone Slave	CIFX RE/PLS
CIFX 104C-RE-R\F	PROFINET IO-Controller	Master	CIFX RE/PNM
	PROFINET IO-Device	Gateway/Stand-Alone Slave	CIFX RE/PNS
	sercos Master	Master	CIFX RE/S3M
	sercos Slave	Gateway/Stand-Alone Slave	CIFX RE/S3S
	VARAN Client (Slave)	Gateway/Stand-Alone Slave	CIFX RE/VRS
CIFX 80-DP, CIFX 90-DP\F,	PROFIBUS DP Master	Master	CIFX DP/DPM
CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F,	PROFIBUS DP Slave	Gateway/ Stand-Alone Slave	CIFX DP/DPS
CIFX 90E-DP\MR\ET\F, CIFX 104C-DP, CIFX 104C-DP-R, CIFX 104C-DP-R, CIFX 104C-DP\F, CIFX 104C-DP-R\F	PROFIBUS MPI Device	Gateway/ Stand-Alone Slave	CIFX DP/MPI
CIFX 80-CO, CIFX 90-CO\F,	CANopen Master	Master	CIFX CO/COM
CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F, CIFX 104C-CO, CIFX 104C-CO-R, CIFX 104C-CO\F, CIFX 104C-CO-R\F	CANopen Slave	Gateway/ Stand-Alone Slave	CIFX CO/COS
CIFX 80-DN,	DeviceNet Master	Master	CIFX DN/DNM
CIFX 90-DN\F, CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F, CIFX 104C-DN, CIFX 104C-DN-R, CIFX 104C-DN\F, CIFX 104C-DN\F,	DeviceNet Slave	Gateway/ Stand-Alone Slave	CIFX DN/DNS

Table 31: Device Names in SYCON.net by Communication Protocol

4.5 Update for Firmware, Driver and Software



Note: As a pre-requirement for the software update the project files, the configuration files and firmware files are to be saved.

At existing hardware installation the firmware, the driver and the configuration software must be updated according to the versions given in section *Notes on Hardware, Firmware, Software and Driver* on page 10. The following graphic gives an overview:

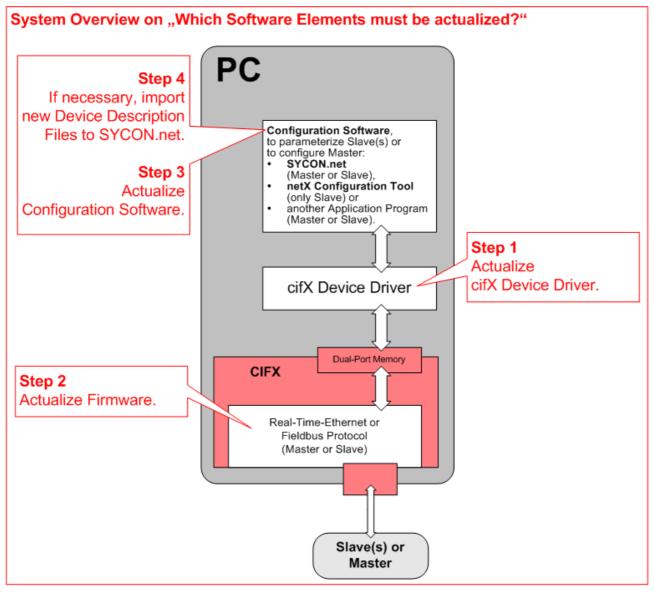


Figure 1: System Overview cifX to update Firmware, Driver and Software



Note the specific details for devices with **DMA Mode** in section *The Function "DMA Mode"* on page 34.

5 Device Drawings

5.1 PC Cards cifX Compact PCI

5.1.1 CIFX 80-RE

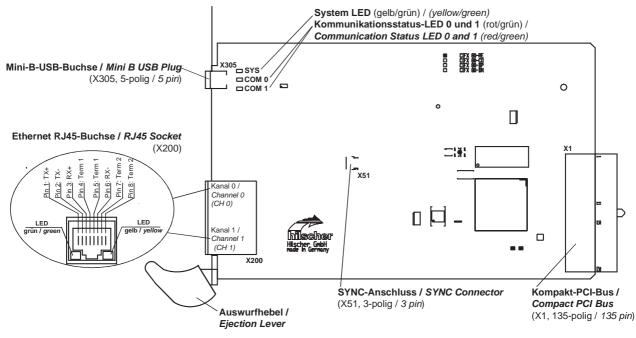


Figure 2: CIFX 80-RE*

 \rightarrow

Note: *Device supports Auto Crossover Function. With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the Ether-CAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For Open Modbus/TCP with V2.3.4.0 and higher both RJ45 channels can be used.

The figure below shows the front plate of the PC cards CIFX 80-RE:

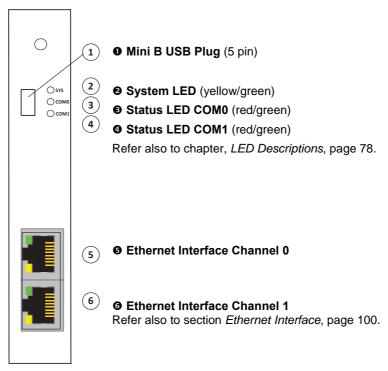


Figure 3: Front Plate CIFX 80-RE

5.1.2 CIFX 80-DP

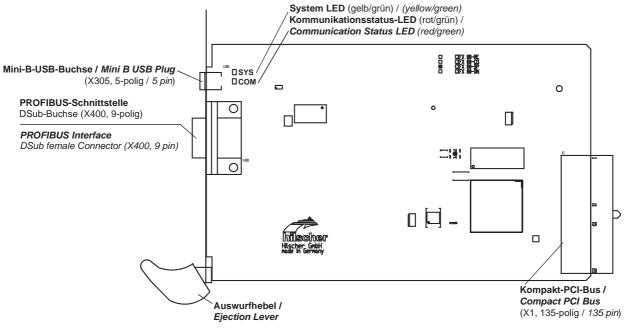


Figure 4: CIFX 80-DP

The figure below shows the front plate of the PC cards CIFX 80-DP:

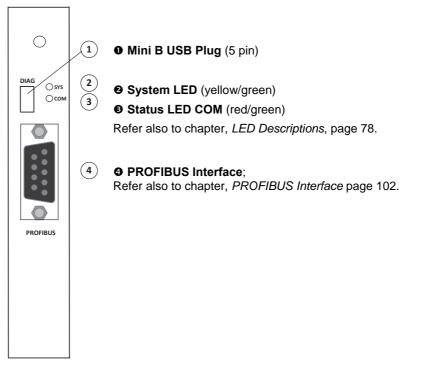


Figure 5: Front Plate CIFX 80-RE

5.1.3 CIFX 80-CO

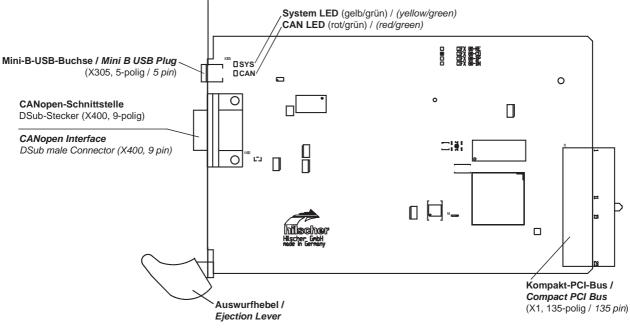


Figure 6: CIFX 80-CO

The figure below shows the front plate of the PC cards CIFX 80-CO:

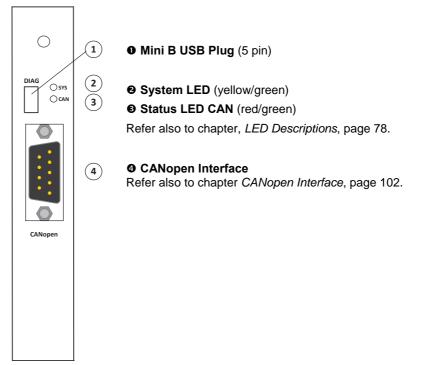


Figure 7: Front Plate CIFX 80-CO

5.1.4 CIFX 80-DN

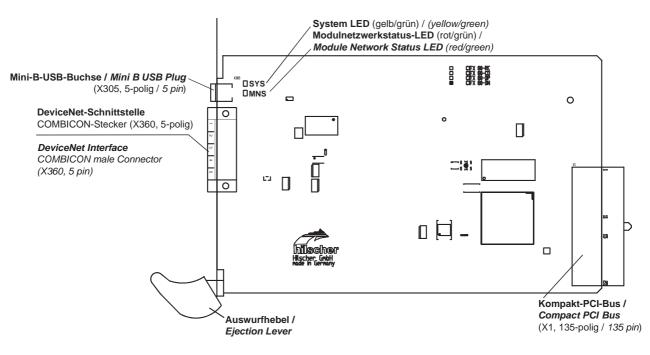


Figure 8: CIFX 80-DN



For the pin assignment of the **SYNC** Connector refer to section *Pin Assignment SYNC Connector, X51* on page 107. For further information on the **Mini-B USB** Connector refer to section *Mini-B USB Connector (5 Pin)* on page 103.



Note: The front plate cutout for the CombiCon male Connector is at the PCB side 0.5 mm outside of the standard front plate cutout.

The figure below shows the front plate of the PC cards CIFX 80-DN:

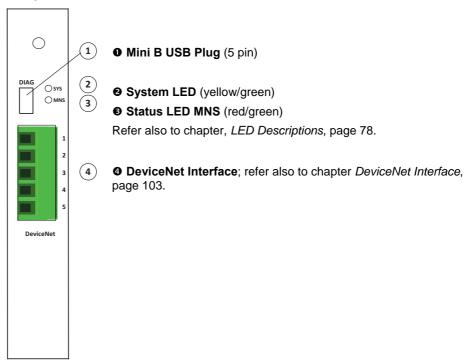


Figure 9: Front Plate CIFX 80-DN

5.2 PC Cards cifX Mini PCI and Mini PCI Express

5.2.1 CIFX 90-RE\F, CIFX 90E-RE\F and MR and/or ET Variants

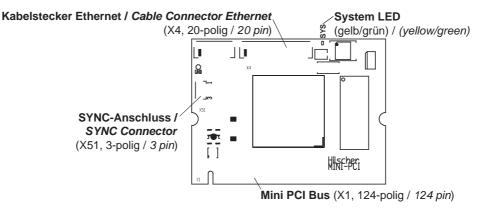


Figure 10: Basic Card CIFX 90 for CIFX 90-RE\F*

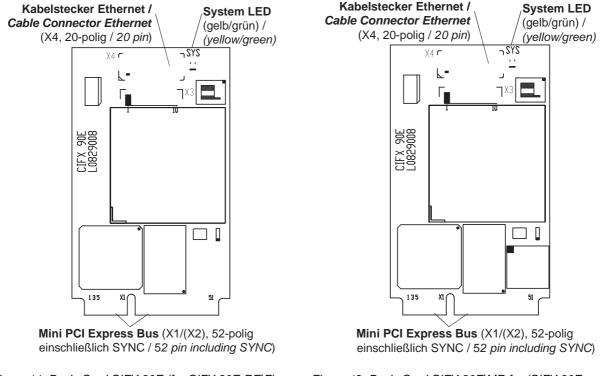
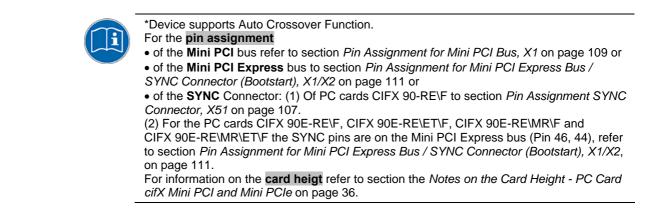


Figure 11: Basic Card CIFX 90E (for CIFX 90E-RE\F) respectively equally looking basic Card CIFX 90E\ET (for CIFX 90E-RE\ET\F*)

Figure 12: Basic Card CIFX 90E/MR for (CIFX 90E-REWR\F) respectively equally looking basic Card CIFX 90E/MR\ET (for CIFX 90E-RE/WR\ET\F*)



CIFX 90-FB\F, CIFX 90E-FB\F and MR and/or ET Variants 5.2.2

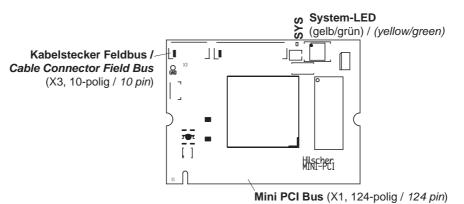
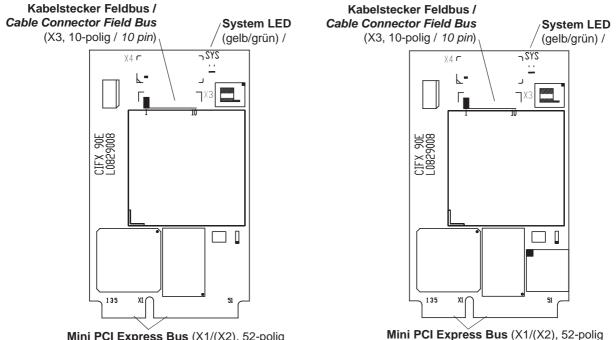


Figure 13: Basic Card CIFX 90 for CIFX 90-DP\F, CIFX 90-CO\F, CIFX 90-DN\F



Mini PCI Express Bus (X1/(X2), 52-polig

Figure 14: Basic Card CIFX 90E (for CIFX 90E-FB\F) respectively equally looking basic Card CIFX 90E\ET (for CIFX 90E-FB\ET\F)

Figure 15: Basic Card CIFX 90EWR for (CIFX 90E-FBWR\F) respectively equally looking basic Card CIFX 90E\MR\ET (for CIFX 90E-FB\MR\ET\F)



For the pin assignment

• of the Mini PCI bus refer to section Pin Assignment for Mini PCI Bus, X1 on page 109 or • of the Mini PCI Express bus to section Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bootstart), X1/X2 on page 111 or for information on the card heigt refer to section the Notes on the Card Height - PC Card cifX Mini PCI and Mini PCIe on page 36.

5.3 PC Cards cifX PCI-104

5.3.1 CIFX 104C-RE, CIFX 104C-RE-R

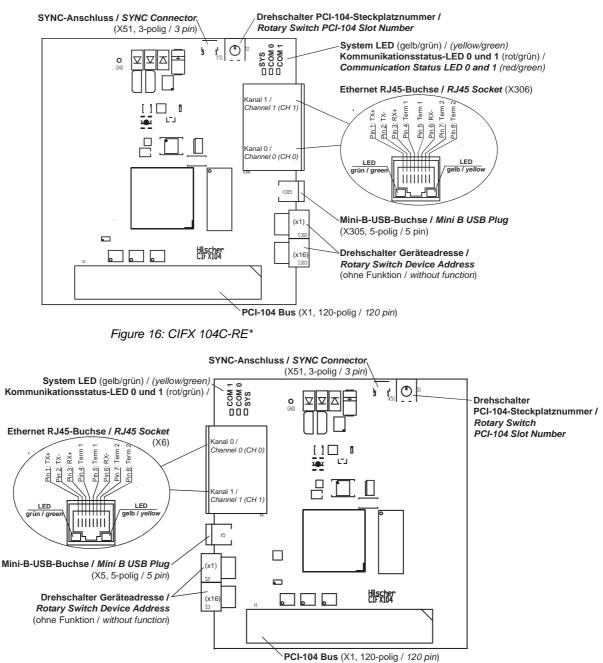


Figure 17: CIFX 104C-RE-R*



Note: *Device supports Auto Crossover Function. Note also: With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the EtherCAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For Open Modbus/TCP with V2.3.4.0 and higher both RJ45 channels can be used.



The meaning of the **LEDs** depends from the loaded firmware. See chapter *LED Descriptions* beginning from page 78.

For the pin assignment of the SYNC Connector refer to section *Pin Assignment SYNC Connector, X51* on page 107. For further information on the **Mini-B USB** Connector refer to section *Mini-B USB Connector (5 Pin)* on page 103.

5.3.2 CIFX 104C-RE\F, CIFX 104C-RE-R\F

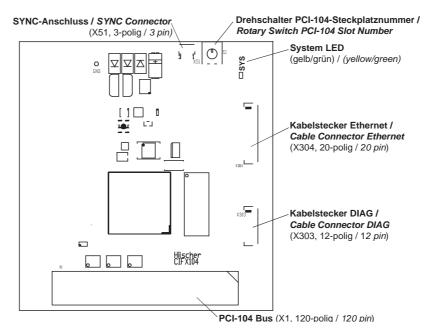


Figure 18: Basic Card for CIFX 104C-RE\F

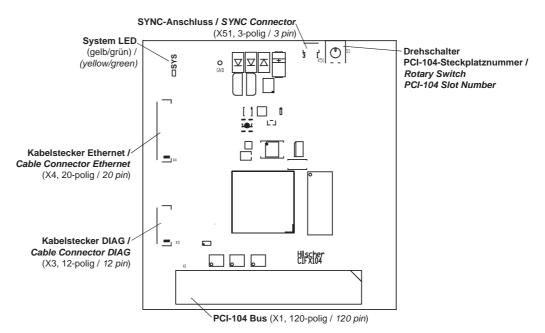


Figure 19: Basic Card for CIFX 104C-RE-R\F

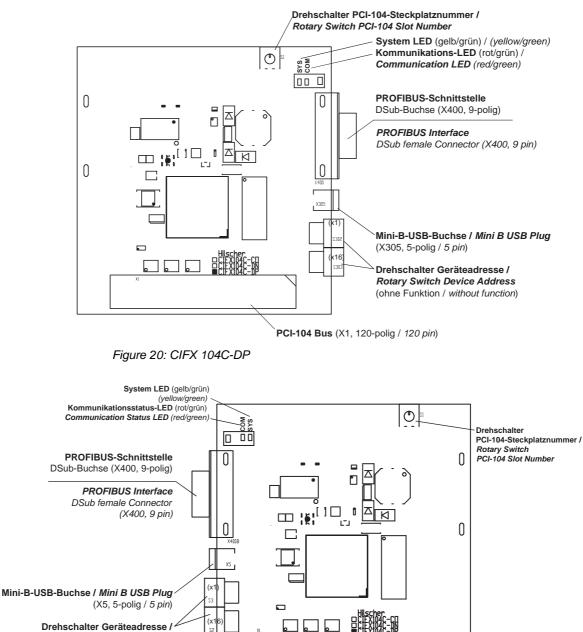


For the **SYNC** pin assignment of the SYNC Connector refer to section *Pin Assignment SYNC Connector, X51* on page 107.

 \rightarrow

Note: If the **AIFX-DIAG** diagnostic assembly interface is connected to the basic card for the PC card CIFX 104C-RE\F or CIFX 104C-RE-R\F, the **Mini-B USB** connector on the AIFX-DIAG can be used beginning with the hardware revision 5 of the PC card cifX.

5.3.3 CIFX 104C-DP, CIFX 104C-DP-R



PCI-104 Bus (X1, 120-polig / 120 pin)

Figure 21: CIFX 104C-DP-R



Rotary Switch Device Address (ohne Funktion / without function)

For further information on the **Mini-B USB** Connector refer to section *Mini-B USB Connector (5 Pin)* on page 103.

5.3.4 CIFX 104C-CO, CIFX 104C-CO-R

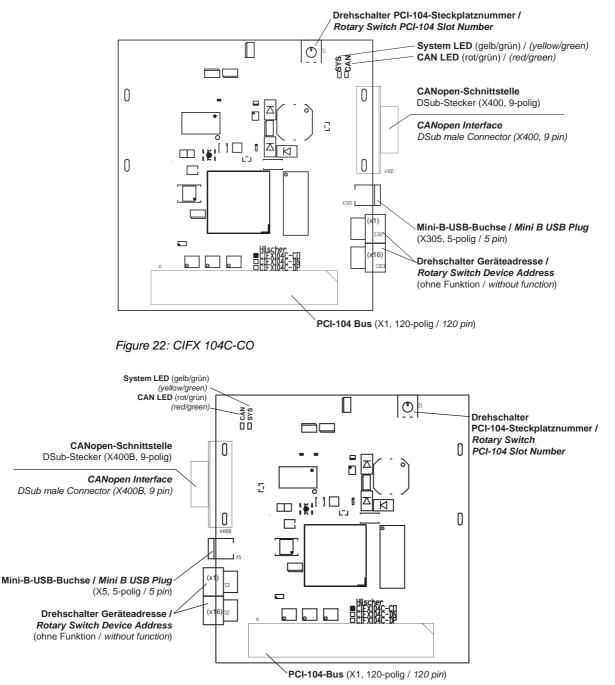


Figure 23: CIFX 104C-CO-R



For further information on the **Mini-B USB** Connector refer to section *Mini-B USB Connector (5 Pin)* on page 103.

5.3.5 CIFX 104C-DN, CIFX 104C-DN-R

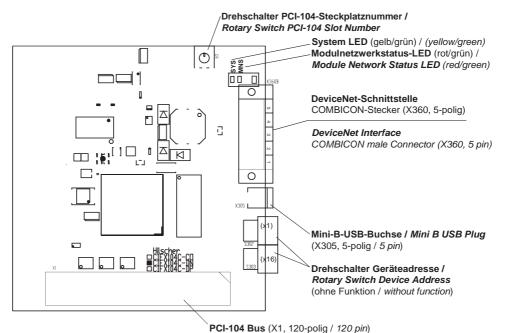


Figure 24: CIFX 104C-DN

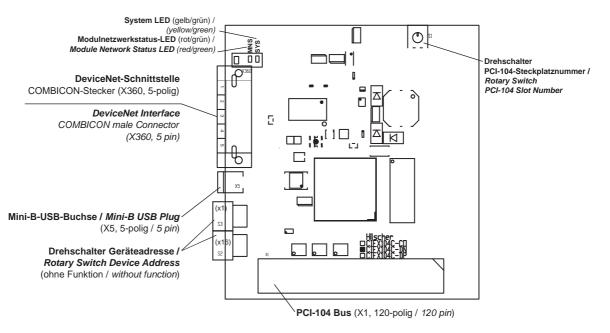
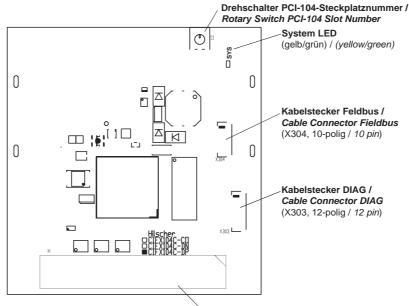


Figure 25: CIFX 104C-DN-R



For further information on the **Mini-B USB** Connector refer to section *Mini-B USB Connector* (5 *Pin*) on page 103.

5.3.6 CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DN\F



PCI-104 Bus (X1, 120-polig / 120 pin)

Figure 26: Basic Card CIFX 104C-FB\F for CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DP\F

5.3.7 CIFX 104C-DP-R\F, CIFX 104C-CO-R\F, CIFX 104C-DN-R\F

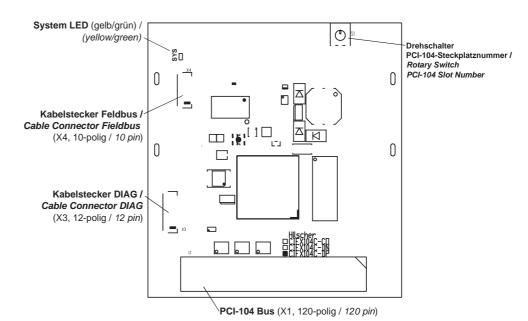


Figure 27: Basic Card CIFX 104C-FB-R\F for CIFX 104C-DP-R\F, CIFX 104C-CO-R\F, CIFX 104C-DN-R\F

5.4 AIFX Assembly Interfaces

5.4.1 Ethernet - AIFX-RE

Only for CIFX 90-RE\F, CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-REMR\ET\F, CIFX 104C-RE\F, CIFX 104C-RE-R\F.

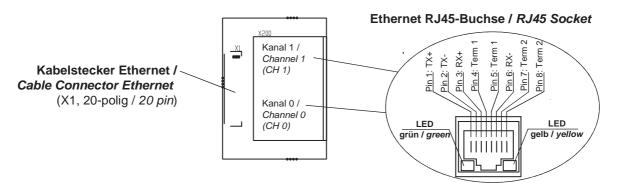


Figure 28: Ethernet Assembly Interface (AIFX-RE)*



Note: *Assembled device supports Auto Crossover Function. Note also: With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the EtherCAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For Open Modbus/TCP with V2.3.4.0 and higher both RJ45 channels can be used.



The meaning of the **LEDs COM0** and **COM1** at the reverse side of the AIFX-RE and the meaning of the green and yellow LEDs at RJ45Ch0 and RJ45Ch1 corresponds to the description in chapter *LED Descriptions* beginning from page 78.

Figure 29: Front Side Ethernet Assembly Interface (AIFX-RE)

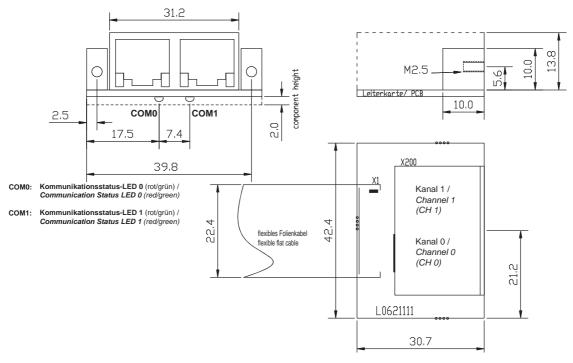


Figure 30: Dimensioning and LED Display Ethernet Assembly Interface (AIFX-RE)

5.4.2 PROFIBUS - AIFX-DP

Only for CIFX 90-DP\F, CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F, CIFX 104C-DP\F, CIFX 104C-DP-R\F.

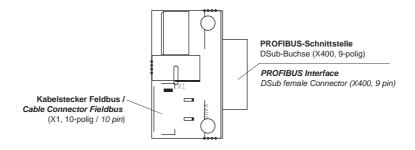


Figure 31: PROFIBUS Assembly Interface (AIFX-DP)

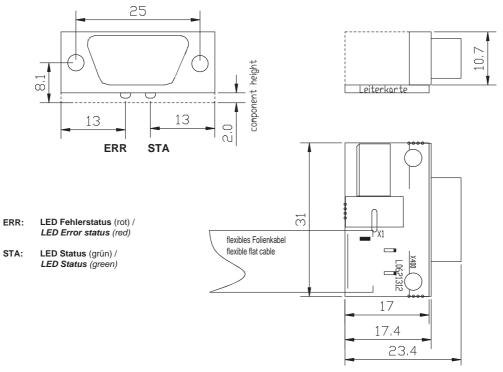


Figure 32: Dimensioning and LED Display PROFIBUS Assembly Interface (AIFX-DP)



The meaning of the **LEDs ERR** and **STA** at the reverse side of the AIFX-DP corresponds to the description in chapter *LED Descriptions* beginning from page 78.

5.4.3 CANopen - AIFX-CO

Only for CIFX 90-CO\F, CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F.

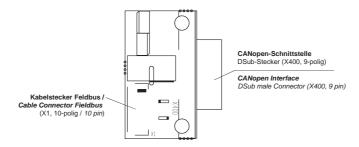


Figure 33: CANopen Assembly Interface (AIFX-CO)

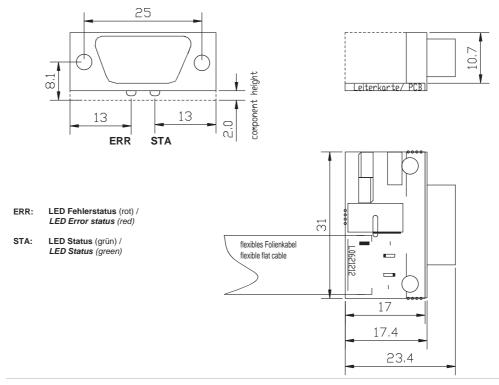


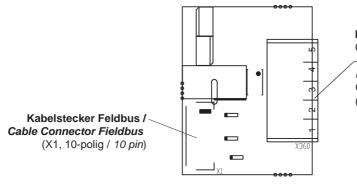
Figure 34: Dimensioning and LED Display CANopen Assembly Interface (AIFX-CO)



The meaning of the **LEDs ERR** and **RUN** at the reverse side of the AIFX-CO corresponds to the description in chapter *LED Descriptions* beginning from page 78.

5.4.4 DeviceNet - AIFX-DN

Only for CIFX 90-DN\F, CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F.



DeviceNet-Schnittstelle COMBICON-Stecker (X360, 5-polig)

DeviceNet Interface COMBICON male Connector (X360, 5 pin)

Figure 35: DeviceNet Assembly Interface (AIFX-DN)



Figure 36: Front Side DeviceNet Assembly Interface (AIFX-DN)

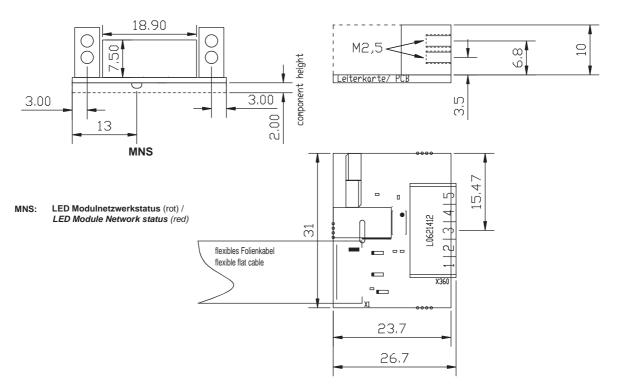


Figure 37: Dimensioning and LED Display DeviceNet Assembly Interface (AIFX-DN)



The meaning of the **LED MNS** at the reverse side of the AIFX-DN corresponds to the description in chapter *LED Descriptions* beginning from page 78.

5.4.5 Diagnostic - AIFX-DIAG

Only for CIFX 104C-RE\F, CIFX 104C-RE-R\F, CIFX 104C-DP\F, CIFX 104C-DP-R\F, CIFX 104C-CO\F, CIFX 104C-CO-R\, CIFX 104C-DN\F, CIFX 104C-DN-R\F.

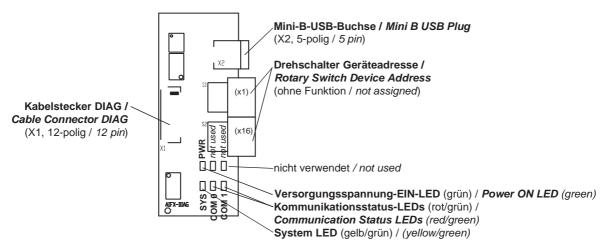


Figure 38: Diagnostic Assembly Interface (AIFX-DIAG)



The meaning of the **LEDs** at the **AIFX-DAIG** corresponds to the descriptions in chapter *LED Descriptions* beginning from page 78. For further information on the **Mini-B USB** Connector refer to section *Mini-B USB Connector* (*5 Pin*) on page103.



Figure 39: Front Side Diagnostic Assembly Interface (AIFX-DIAG)

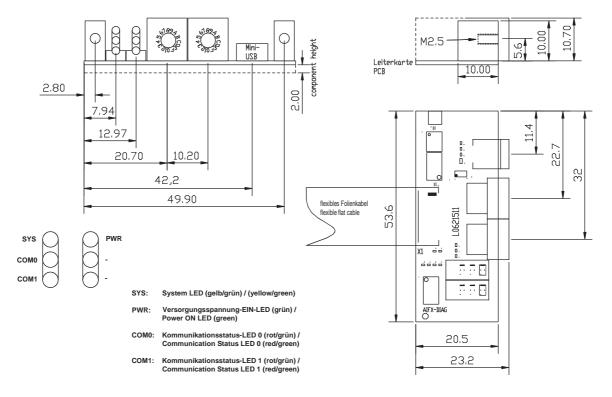


Figure 40: Dimensioning and LED Display Diagnostic Assembly Interface (AIFX-DIAG)

6 Hardware Installation and Uninstalling

To install / uninstall the PC cards cifX Compact PCI

- CIFX 80-RE
- CIFX 80-DP

Mini PCI

- CIFX 90-RE\F
- CIFX 90-DP\F
- CIFX 90-CO\F
- CIFX 90-DN\F

- CIFX 80-CO
- CIFX 80-DN

Mini PCI Express

- CIFX 90E-RE\F, CIFX 90E-RE\ET\F
- CIFX 90E-DP\F, CIFX 90E-DP\ET\F
- CIFX 90E-CO\F, CIFX 90E-CO\ET\F
- CIFX 90E-DN\F, CIFX 90E-DN\ET\F
- CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F
- CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F
- CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F
- CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F

and **PCI-104**

- CIFX 104C-RE
- CIFX 104C-RE-R
- CIFX 104C-RE\F
- CIFX 104C-RE-R\F
- CIFX 104C-DP
- CIFX 104C-DP-R
- CIFX 104C-DP\F
- CIFX 104C-DP-R\F
- CIFX 104C-CO
- CIFX 104C-CO-R
- CIFX 104C-CO\F
- CIFX 104C-CO-R\F
- CIFX 104C-DN
- CIFX 104C-DN-R
- CIFX 104C-DN\F
- CIFX 104C-DN-R\F

handle as described in the sections hereafter. The device drawing of your PC card cifX gives information on the manual control elements of your device.



For the installation, uninstalling and replacement of the PC card cifX check any notes in the overview in chapter *Getting Started* on page 40.

6.1 Safety Messages on Personal Injury

Obey to the following safety messages on personal injury, when installing, uninstalling or replacing the PC card cifX.

6.1.1 Electrical Shock Hazard



WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- HAZARDOUS VOLTAGE inside of the PC or of the connecting device.
- Strictly obey to all safety rules provided by the device's manufacturer in the documentation!
- First disconnect the power plug of the PC or of the connecting device, before you open the cabinet.
- Make sure, that the power supply is off at the PC or at the connecting device.
- Open the PC cabinet and install or remove the PC card cifX only after disconnecting power.

6.2 **Property Damage Messages**

Obey to the following property damage messages, when installing, uninstalling or replacing the PC card cifX.

6.2.1 Device Destruction by exceeding allowed Supply Voltage

Adhere for all PC cards cifX described in this manual the instruction hereafter:

NOTICE

Device Destruction!

- Use only the permissible supply voltage to operate the PC card cifX.
- Operating the PC card cifX with a supply voltage above of the specified range leads to device destruction.

6.2.2 Device Destruction by exceeding allowed Signaling Voltage

Adhere for all PC cards cifX described in this manual the instruction hereafter:

NOTICE

Device Destruction!

- All I/O signal pins at the PC card cifX tolerate only a specified signaling voltage!
- Operation the PC card cifX with a signaling voltage other than the specified signaling voltage may lead to severe damage to the PC card cifX!

For detailed information on the supply and signaling voltage of the PC cards cifX described in this manual, refer to section *Power Supply and Host* Interface on page 38.

6.2.3 Electrostatically sensitive Devices

Adhere to the necessary safety precautions for components that are vulnerable with electrostatic discharge.



NOTICE

Electrostatically sensitive Devices

• To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.

6.3 Fix Front Plate Sticker at CIFX 80-RE



Note: Your PC card CIFX 80-RE set contains a set of front plate stickers (9 different stickers). Depending from the loaded firmware the label on each sticker indicates for the respective system the following **LED names**:

- of the system and communication status LEDs (above)

- of the RJ45 Ethernet female connector LEDs (below).

Further information to this question you find also in chapter *LED Descriptions* beginning from page 78.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- Use the sticker according to the firmware and glue it on the front of the PC card CIFX 80-RE.

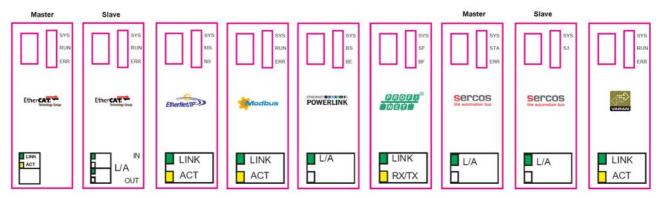


Figure 41: Front Plate Stickers for CIFX 80-RE

LED		EtherCAT Master	EtherCAT Slave	EtherNet/IP	Open Modbus/TCP	POWERLINK	PROFINET IO	sercos Master	sercos Slave	VARAN
SYS (yellow/green)	SYS	SYS	SYS	SYS	SYS	SYS	SYS	SYS	SYS
COM	0 (red/ green)	RUN	RUN	MS	RUN	BS	SF	STA	S3	RUN
COM	1 (red/ green)	ERR	ERR	NS	ERR	BE	BF	ERR	-	ERR
145 10	green	LINK	L/A IN	LINK	LINK	L/A	LINK	L/A	L/A	LINK
RJ45 Ch0	yellow	ACT	-	ACT	ACT	-	RX/TX	-	-	ACT
45 1	green	-	L/A OUT	LINK	LINK	L/A	LINK	L/A	L/A	LINK
RJ4 Ch1	yellow	-	-	ACT	ACT	-	RX/TX	-	-	ACT

Table 32: LED Labeling depending of the loaded Firmware

6.4 Installing PC Cards cifX Compact PCI

1. Adhere to the necessary safety precautions for components that are vulnerable with electrostatic discharge.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- 2. Fix front plate sticker (only for CIFX 80-RE).
- Use the sticker according to the firmware and glue it on the front of the PC card CIFX 80-RE (see section *Fix Front Plate Sticker at CIFX 80-RE* on page 66).
- 3. Take safety precautions.

WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- > Disconnect the power plug of the PC or of the connecting device.
- Make sure, that the power supply is off at the PC or at the connecting device.
- 4. Open cabinet.
- > Open the cabinet of the PC or of the connecting device.
- 5. Install PC card cifX Compact PCI.
- Possibly remove a blank plate.
- > Put down the ejection lever at the PC card cifX.
- > Plug the PC card cifX into a free Compact PCI slot.
- > Fasten the PC card cifX.
- > Tip up the lever and click in.
- Screw the PC card cifX with two screws on the wholes above and below.

After this:

- 6. Close cabinet.
- > Close the cabinet of the PC or connecting device.
- 7. Plug the connecting cable to the Master or Slave.
- ➢ For the PC cards CIFX 80-RE note:



Note: The RJ45 socket is only for use in LAN, not for telecommunication circuits. For further information refer to section *Ethernet Interface* on page 100.

- Plug the connecting cable from the PC card cifX to the PC card Master or Slave.
- 8. Connect the PC or the connecting device to the power supply and switch it on.

- > Connect the PC or the connecting device to the power supply.
- > Switch on the PC or the connecting device.

6.5 Uninstalling PC Cards cifX Compact PCI

1. Take safety precautions.

WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- > Disconnect the power plug of the PC or of the connecting device.
- Make sure, that the power supply is off at the PC or at the connecting device.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- 2. Remove the connecting cable to the Master or Slave.
- Remove the connecting cable between the PC card cifX and the PC card Master or Slave.
- 3. Open cabinet.
- > Open the cabinet of the PC or of the connecting device.
- 4. Uninstall PC card cifX Compact PCI.
- Unscrew the PC card cifX.
- > First press the grey button at the ejection lever.
- > Then press the ejection lever downwards.
- > Remove the PC card cifX from the Compact PCI slot.
- Possibly reinsert a blank plate.

After this:

- 5. Close cabinet.
- > Close the cabinet of the PC or connecting device.

6.6 Installing PC Cards cifX Mini PCI and Mini PCI Express

1. Take safety precautions.

WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- > Disconnect the power plug of the PC or of the connecting device.
- Make sure, that the power supply is off at the PC or at the connecting device.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- 2. Open cabinet.
- > Open the cabinet of the PC or of the connecting device.
- 3. Install the basic card of the PC card cifX Mini PCI.
- Plug the basic card into the Mini PCI socket on the mainboard until it snaps into place.
- Press the clamps on the sides at the Mini PCI socket until they snap into place, to fasten the basic card to the mainboard.
- 4. Install the basic card of the PC card cifX Mini PCI Express.
- Plug the basic card into the PCI Express Mini System Connector on the mainboard.
- Press down the basic card until it snaps into place.





Plug the basic card into the PCI Express Mini System Connector on the mainboard ...

... and press down the card until it snaps into place.

Figure 42: Mounting the Basic Card CIFX 90E into the PCI Express Mini System Connector

Connect AIFX Assembly Interface



Important! Operating the PC cards **Mini PCI** CIFX 90-RE\F, CIFX 90-DP\F, CIFX 90-CO\F and CIFX 90-DN\F or the PC cards **Mini PCI Express** CIFX 90E-XX\F (alle variants 'MR' or 'ET')⁵ requires proper connection of the Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface to the basic card!

- 5. Connect the Ethernet assembly interface (AIFX-RE) to the basic card:
- Connect the cable connector Ethernet X1 on the AIFX-RE with the cable.
- Connect the cable connector Ethernet X4 on the basic card CIFX 90 or CIFX 90E (all variants 'MR' or 'ET') with the cable.

AIFX-RE Assembly Interface with Cable Connector Ethernet X1



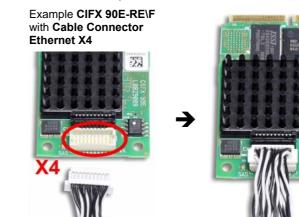


Figure 43: Connecting the Ethernet Assembly Interface (AIFX-RE) to the basic Card CIFX 90E (Example)

- 6. Alternatively connect the PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface:
- Connect the cable connector fieldbus X1 on the AIFX-DP, AIFX-CO or AIFX-DN assembly interface with the cable.
- Connect the cable connector fieldbus X3 on the basic card CIFX 90 or CIFX 90E (all variants 'MR' or 'ET') with the cable.

AIFX-CO Assembly Interface with Cable Connector Fieldbus X1

×1

cable must face up!

Important! The contacts on the connection

Example basic card CIFX 90E, Cable Connector Fieldbus X3

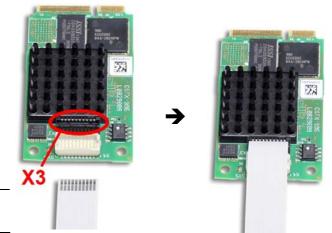


Figure 44: Connecting the CANopen Assembly Interface (AIFX-CO) to the basic Card CIFX 90E (Example)

⁵ Variants of the PC card CIFX 90E-XX\F: CIFX 90E-XX\ET\F, CIFX 90E-XX\MR\F or CIFX 90E-XX\MR\ET\F (XX = RE, DP, CO or DN)

After this:

- 7. Close cabinet.
- > Close the cabinet of the PC or connecting device.
- 8. Plug the connecting cable to the Master or Slave.
- > For the PC cards CIFX 90-RE\F or CIFX 90E-RE\F (all variants) note:



Note: The RJ45 socket is only for use in LAN, not for telecommunication circuits. For further information refer to section *Ethernet Interface* on page 100.

- Plug the connecting cable from the PC card cifX to the PC card Master or Slave.
- 9. Connect the PC or the connecting device to the power supply and switch it on.
- > Connect the PC or the connecting device to the power supply.
- > Switch on the PC or the connecting device.

6.7 Uninstalling PC Cards cifX Mini PCI, Mini PCI Express

1. Take safety precautions.

WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- > Disconnect the power plug of the PC or of the connecting device.
- Make sure, that the power supply is off at the PC or at the connecting device.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- 2. Remove the connecting cable to the Master or Slave.
- Remove the connecting cable between the PC card cifX to be replaced and the PC card Master or Slave.
- 3. Open cabinet.
- > Open the cabinet of the PC or of the connecting device.

Uninstall AIFX Assembly Interface

- 4. Uninstall the Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface:
- Remove the AIFX-RE, AIFX-DP, AIFX-CO or AIFX-DN assembly interface from the housing panel of the PC.
- Disconnect the cable from the basic card of the PC card cifX Mini PCI or Mini PCI Express; cable connector Ethernet X4 or cable connector fieldbus X3.

Remove PC Card cifX

- 5. Remove the basic card of the PC card cifX Mini PCI:
- To remove the PC card cifX from the mainboard, pull the clamps on the sides at the Mini PCI socket on the mainboard until the basic card flips up.
- > Remove the basic card form the Mini PCI socket.

Or:

- 6. Remove the basic card of the PC card cifX Mini PCI Express:
- Press down the fastening clips (on the upper edge of the PC card cifX), until the basic card flips up.





Press down the fastening clips (red arrows) of ... until the card flips up. the basic card ...

Figure 45: Demounting the Basic Card CIFX 90E⁶ from the PCI Express Mini System Connector (Example: Basic Card CIFX 90E)

Remove the PC card cifX from the PCI Express Mini System Connector.

After this:

- 7. Close cabinet.
- > Close the cabinet of the PC or connecting device.

⁶ all variants: CIFX 90E, CIFX 90E\ET, CIFX 90E\MR, CIFX 90E\MR\ET

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

6.8 Installing PC Cards cifX PCI-104 (PCI-104-Modules)



- **Note:** For PC cards cifX PCI-104 with AIFX assembly interface first install the basic card. Then connect the AIFX assembly interface to the basic card.
- 1. Adhere to the necessary safety precautions for components that are vulnerable with electrostatic discharge.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- 2. Set the physical PCI-104 Slot Number.
- Set the physical PCI-104 Slot Number for each PCI-104 module. For the PC cards cifX PCI-104 use the Rotary Switch for PCI-104 Slot Number on the PC card cifX.



Note: At maximum four PCI-104 modules can be plugged one upon the other and each switch position may only be used once. The PCI-104 module plugged next to the host controller gets the CLK number 0, the following PCI-104 modules respectively get the next higher CLK number. For further information refer to section *Rotary Switch for PCI-104 Slot Number* on page 104.

3. Take safety precautions.

WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- > Disconnect the power plug of the PC or of the connecting device.
- Make sure, that the power supply is off at the PC or at the connecting device.
- 4. Open cabinet.
- > Open the cabinet of the PC or of the connecting device.



Note: If several PCI-104 modules shall be put together in a stack: (a) Install the first PCI-104 module on the mainboard.

(b) Only for the basic cards CIFX 104C-RE\F and CIFX 104C-RE-R\F or the basic cards CIFX 104C-FB\F and CIFX 104C-FB-R\F: Connect the AIFX-RE, AIFX-DP, AIFX-CO, AIFX-DN assembly interface and respectively AIFX-DIAG to the basic card for the first PCI-104 module.

(c) Connect any other PCI-104 module on the respective underlying PCI-104 module.

- 5. Install PC card cifX PCI-104.
- Plug the PC card cifX into a free PCI-104 slot (or if so, to the underlying PCI-104 module).
- Fix the PC card cifX using 4 spacing bolts and screws intended to the mainboard (or if so, to the underlying PCI-104 module). The scope of delivery does not include spacing bolts and screws.

Only for the basic cards for CIFX 104C-RE\F and CIFX 104C-RE-R\F or the basic cards CIFX 104C-FB\F and CIFX 104C-FB-R\F:



Note: First connect the AIFX-RE, AIFX-DP, AIFX-CO or AIFX-DN assembly interface to each basic card PCI-104, before plugging another PCI-104 module. Just so you can check exactly whether the AIFX is properly connected to the basic card.

Important! Operating the PC cards CIFX 104C-XX\F or CIFX 104C-XX-R\F requires proper connection of the Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface to the basic card!

- 6. Connect the Ethernet assembly interface (AIFX-RE) to the basic card.
- Connect the cable connector Ethernet X1 on the AIFX-RE with the cable.
- Connect the cable connector Ethernet X4 (or X304) on the basic card CIFX 104C-RE\F or CIFX 104C-RE-R\F with the cable.

AIFX-RE Assembly Interface with Cable Connector Ethernet X1



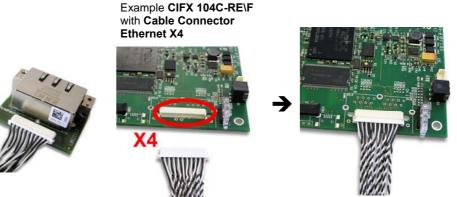
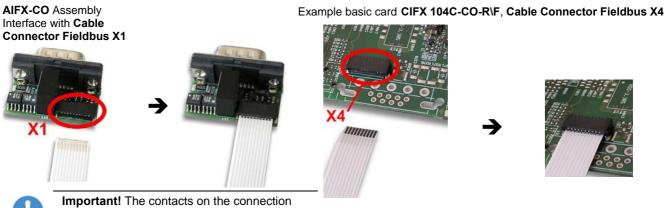


Figure 46: Connecting the Ethernet Assembly Interface (AIFX-RE) to the Basic Card CIFX 104C-RE\F (Example)

- 7. Or connect the PROFIBUS (AIFX-DP), CANopen (AIFX-CO) or DeviceNet (AIFX-DN) assembly interface to the basic card.
- Connect the cable connector fieldbus X1 on the assembly interface with the cable.
- Connect the cable connector fieldbus X4 (or X304) on the PC card CIFX 104C-FB\F or CIFX 104C-FB-R\F Fieldbus with the cable.



cable must face up!

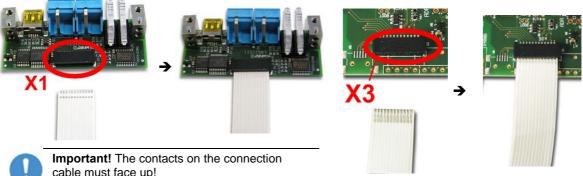
Figure 47: Connecting the CANopen Assembly Interface (AIFX-CO) to the Basic Card 104C-FB-R\F (Example)

Install the AIFX-RE, AIFX-DP, AIFX-CO or AIFX-DN assembly interface at the front plate of the PC cabinet.

Connect AIFX-DIAG

Only for the basic cards for CIFX 104C-RE\F and CIFX 104C-RE-R\F or the basic cards CIFX 104C-FB\F and CIFX 104C-FB-R\F:

- 8. If so, connect the diagnostic assembly interface (AIFX-DIAG):
- Connect the cable connector DIAG X1 on the diagnostic assembly interface (AIFX-DIAG) with the cable.
- Connect the cable connector DIAG X3 (or X303) on the PC card cifX with the cable.
- Install the assembly interface AIFX-DIAG at the front plate of the PC cabinet.



cable must face up!

Figure 48: Connecting the Diagnostic Assembly Interface (AIFX-DIAG) to the Basic Card CIFX 104C-FB-R\F (Example)

After this:

- 9. Close cabinet.
- > Close the cabinet of the PC or connecting device.
- 10. Plug the connecting cable to the Master or Slave.
- ➢ For the PC cards CIFX 104C-RE\F or CIFX 104C-RE-R\F note:



Note: The RJ45 socket is only for use in LAN, not for telecommuni-cation circuits. For further information refer to section *Ethernet Interface* on page 100.

- Plug the connecting cable from the PC card cifX to the PC card Master or Slave.
- 11. Connect the PC or the connecting device to the power supply and switch it on.
- > Connect the PC or the connecting device to the power supply.
- > Switch on the PC or the connecting device.

6.9 Uninstalling PC Cards cifX PCI-104

1. Take safety precautions.

WARNING

Lethal Electrical Shock caused by parts with more than 50V!

- > Disconnect the power plug of the PC or of the connecting device.
- Make sure, that the power supply is off at the PC or at the connecting device.

NOTICE

Electrostatically sensitive Devices

- To prevent damage to the PC and the PC card cifX, make sure, that the PC card cifX is grounded via the endplate and the PC and make sure, that you are discharged when you install/uninstall the PC card cifX.
- 2. Remove the connecting cable to the Master or Slave.
- Remove the connecting cable between the PC card cifX to be replaced and the PC card Master or Slave.
- 3. Open cabinet.
- > Open the cabinet of the PC or of the connecting device.



Note: If a PC card CIFX 104C-XX\F or CIFX 104C-XX-R\F shall be uninstalled from a stack of PCI-104 modules:

(a) Remove all PCI-104 modules above from the PC card cifX and the PC card cifX. For each PC card cifX first remove the AIFX assembly interfaces from the basic cards.(b) Reinstall the removed PCI-104 modules.

Uninstall AIFX Assembly Interfaces

Only for PC cards PCI-104 with AIFX Assembly Interface CIFX 104C-XX\F and CIFX 104C-XX-R\F:

- Uninstall the Ethernet (AIFX-RE), PROFIBUS (AIFX-DP), CANopen (AIFX-CO), DeviceNet (AIFX-DN) and diagnostic (AIFX-DIAG) assembly interface:
- > Remove the assembly interfaces from the front plate of the PC cabinet.
- Disconnect the cables from the PC card cifX PCI-104; cable connector Ethernet X4 (or X304) or cable connector fieldbus X4 (or X304) and cable connector DIAG X3 (or X303).

Remove PC Card cifX

- 5. Remove PC card cifX PCI-104.
- > Loosen the four fastening screws of the PC card cifX.
- Remove the PC card cifX.

After this:

- 6. Close cabinet.
- > Close the cabinet of the PC or connecting device.

7 Troubleshooting

7.1 Instructions for Problem Solving

In case of any error, follow the instructions for problem solving given here:

<u>General</u>

Check the PC card cifX operating requirements according to the requirements given in the section *Requirements for Operation* on page 39.

LINK-LEDs

Check using the LINK LEDs status whether a connection to the Ethernet is established. Therefore use the descriptions on the LINK LEDs in the chapter *LED Descriptions* beginning from page 78.

<u>Cable</u>

Check that the pin assignment of the cable is correct. This means, the cable by which you connect the PC card cifX to the PC card Master or Slave.

Configuration

Check the configuration in the Master device and the Slave device. The configuration has to match.

Diagnostic using the Configuration Software SYCON.net

With the menu **Online > Diagnosis**, the diagnostic information of the device is shown. The shown diagnostic information depends on the used protocol.



Note: More information about the device diagnosis and its functions you find in the operating manual of the corresponding Real-Time Ethernet or fieldbus system.

Diagnostic using the netX Configuration Tool

With the menu **netX Configuration Tool > Diagnostics**, the diagnostic information of the device is shown. The shown diagnostic information depends on the used protocol or fieldbus.

8 LED Descriptions

8.1 Overview LEDs Real-Time Ethernet Systems



Note: The meaning of the communication status and the RJ45 LEDs at the PC card cifX is defined by the loaded cifX firmware of the protocol.

	Naming				LED N	aming by P	Protocol			
in the Device Drawing		EtherCAT Master	EtherCAT Slave	EtherNet/IP	Open- Modbus/TCP	POWERLINK	PROFINET IO	sercos Master	sercos Slave	VARAN
SYS (System Status)		SYS	SYS	SYS	SYS	SYS	SYS	SYS	SYS	SYS
CON		RUN	RUN	MS	RUN	BS	SF	STA	S3	RUN
(Communi- cation Status) (red/green) (orange = red/green simultaneously)		(Run)	(Run)	(Module Status)	(Run)	(Bus Status)	(System Failure)	(Status)	(Status/ Error)	(Run)
CON		ERR	ERR	NS	ERR	BE	BF	ERR	-	ERR
catio	nmuni- on Status) green)	(Error)	(Error)	(Networ k Status)	(Error)	(Bus Error)	(Bus Failure)	(Error)		(Error, <i>Fehler</i>)
оч	0	LINK	L/A IN	LINK	LINK	L/A	LINK	L/A	L/A	LINK
RJ45Ch0	(green)	(Link)	(Link/ Activity Input)	(Link)	(Link)	(Link/ Activity)	(Link)	(Link/ Activity)	(Link/ Activity)	(Link)
	0	ACT	-	ACT	ACT	-	RX/TX	-	-	ACT
	(yellow)	(Activity)		(Activity)	(Activity)		(Receive/ Transmit)			(Activity)
RJ45Ch1	(green)	-	L/A OUT	LINK	LINK	L/A	LINK	L/A	L/A	LINK
RJ4£			(Link/ Activity Output)	(Link)	(Link)	(Link/ Activity)	(Link)	(Link/ Activity)	(Link/ Activity)	(Link)
	0	-	-	ACT	ACT	-	RX/TX	-	-	ACT
	(yellow)			(Activity)	(Activity)		(Receive/ Transmit)			(Activity)

Table 33: Overview LEDs Real-Time Ethernet Systems

8.2 Overview LEDs Fieldbus Systems

LED				Label b	y Fieldbus	System			
	PROFIBUS DP or PROFIBUS MPI (1 Duo-LED)	PROFIBUS DP (1 Duo LED/channel, for 2-Channel Device)	PROFIBUS DP or PROFIBUS MPI (2 LEDs, AIFX-DP is conneced)	CANopen (1 Duo-LED)	CANopen (2 LEDs, AIFX-CO is conneced)	DeviceNet (1 Duo-LED)	AS-Interface (Master) (1 Duo-LED/CH)	CC-Link (Slave) (2 LEDs)	CompoNet (Slave) (2 Duo-LEDs)
System Status	SYS	SYS	SYS	SYS	SYS	SYS	SYS	SYS	SYS
Communica-	СОМ	COM0	ERR	CAN	ERR	MNS	CH0	L RUN/	MS
tion Status 0 (red/green)	Commu- nication Status	Commu- nication Status 0	(Error, only red)	CANopen Status	(Error, only red)	Module Network Status	Chan- nel 0	L Run	Module Status
Communica-	-	COM1	STA	-	RUN	-	CH1	L ERR	NS
tion Status 1 (red/green)		Commu- nication Status 1	Status, only green		Run, only green		Chan- nel 1	L Error	Network Status

Table 34: Overview LEDs by Fieldbus System

8.3 System LED

The subsequent table describes the meaning of the system LED.

LED	Color	State	Meaning				
SYS	Duo LED yel	LED yellow/green					
	🥥 (green)	On	Operating System running				
	(green/ yellow)	Blinking green/ yellow	Second stage bootloader is waiting for firmware				
	🧡 (yellow)	Static	Bootloader netX (= romloader) is waiting for second stage bootloader				
	(off)	Off	Power supply for the device is missing or hardware defect.				

Table 35: System Status LED

8.4 Power On LED

The subsequent table describes the meaning of the Power On LED.

LED	Color	State	Meaning
PWR	LED green		
) (green)	On	Power supply for the device on.
	(off)	Off	Power supply for the device is missing.

Table 36: Power On LED

8.5 EtherCAT Master

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet Master when the firmware of the EtherCAT Master protocol is loaded to the device.

LED	Color	State	Meaning
RUN	Duo LED re	d/green	
Name in the	(off)	Off	INIT: The device is in state INIT
device	🥥 (green)	Blinking	PRE-OPERATIONAL : The device is in PRE-OPERATIONAL state
drawing: COM 0	🥥 (green)	Flickering	BOOT: Device is in Boot mode
	🥥 (green)	Single Flash	SAFE-OPERATIONAL: The device is in SAFE-OPERATIONAL state
	🥥 (green)	On	OPERATIONAL: The device is in OPERATIONAL state
ERR	Duo LED re	d/green	
Name in the device drawing: COM 1	(off)	Off	Master has no errors
	e (red)	On	Master has detected a communication error. The error is indicated in the DPM
LINK / RJ45	LED green		
Ch0	🥥 (green)	On	A link is established
	(off)	Off	No link established
ACT/	LED yellow		
RJ45 Ch0	🥮 (yellow)	Flickering	The device sends/receives Ethernet frames

Table 37: LEDs EtherCAT Master

LED State Definition for EtherCAT Master for the RUN and ERR LEDs

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.
Flickering	The indicator turns on and off with a frequency of approximately 10 Hz: on for approximately 50 ms, followed by off for 50 ms.
Single Flash	The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms).

Table 38: LED State Definition for EtherCAT Master for the RUN and ERR LEDs

8.6 EtherCAT Slave

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet Slave when the firmware of the EtherCAT Slave protocol is loaded to the device.

LED	Color	State	Meaning
RUN	Duo LED re	d/green	
Name in the	(off)	Off	INIT: The device is in state INIT
device	igreen)	Blinking	PRE-OPERATIONAL : The device is in state PRE-OPERATIONAL
drawing: COM 0	(green)	Single Flash	SAFE-OPERATIONAL: The device is in state SAFE-OPERATIONAL
	(green)	On	OPERATIONAL: The device is in state OPERATIONAL
ERR	Duo LED re	d/green	
Name in the	(off)	Off	No error : The EtherCAT communication of the device is in working condition
device drawing: COM 1	(red)	Blinking	Invalid Configuration: General Configuration Error
			Possible reason: State change commanded by master is impossible due to register or object settings.
	(red)	Single Flash	Local Error : Slave device application has changed the EtherCAT state autonomously.
			Possible reason 1: A host watchdog timeout has occurred.
			Possible reason 2: Synchronization Error, device enters Safe- Operational automatically.
	(red)	Double Flash	Application Watchdog Timeout: An application watchdog timeout has occurred.
			Possible reason: Sync Manager Watchdog timeout.
L/A IN/	LED green		
RJ45 Ch0	🥥 (green)	On	A link is established
L/A	(green)	Flickering	The device sends/receives Ethernet frames
OUT / RJ45 Ch1	(off)	Off	No link established
RJ45	LED yellow	•	·
Ch0 RJ45	🤶 (yellow)	-	-
Ch1		T // 00 / 50	

Table 39: LEDs EtherCAT Slave

LED State Definition for EtherCAT Slave for the RUN and ERR LEDs

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.
Flickering	The indicator turns on and off with a frequency of approximately 10 Hz: on for approximately 50 ms, followed by off for 50 ms.
Single Flash	The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms).
Double Flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).

Table 40: LED State Definition for EtherCAT Slave for the RUN and ERR LEDs

8.7 EtherNet/IP Scanner (Master)

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet (Master) when the firmware of the EtherNet/IP Scanner (Master) protocol is loaded to the device.

LED	Color	State	Meaning		
MS	Duo LED ree	d/green			
Name in the device	🥥 (green)	On	Device operational : If the device is operating correctly, the module status indicator shall be steady green.		
drawing: COM 0	🥥 (green)	Flashing	Standby : If the device has not been configured, the module status indicator shall be flashing green.		
	(red)	On	Major fault : If the device has detected a non-recoverable major fault, the module status indicator shall be steady red.		
	e (red)	Flashing	Minor fault : If the device has detected a recoverable minor fault, the module status indicator shall be flashing red. NOTE: An incorrect or inconsistent configuration would be considered a minor fault.		
	(red/green)	Flashing	Self-test : While the device is performing its power up testing, the module status indicator shall be flashing green/red.		
) (off)	Off	No power : If no power is supplied to the device, the module status indicator shall be steady off.		
NS	Duo LED red/green				
Name in the device) (green)	On	Connected : If the device has at least one established connection (even to the Message Router), the network status indicator shall be steady green.		
drawing: COM 1	🥥 (green)	Flashing	No connections : If the device has no established connections, but has obtained an IP address, the network status indicator shall be flashing green.		
	(red)	On	Duplicate IP : If the device has detected that its IP address is already in use, the network status indicator shall be steady red.		
	e (red)	Flashing	Connection timeout : If one or more of the connections in which this device is the target has timed out, the network status indicator shall be flashing red. This shall be left only if all timed out connections are reestablished or if the device is reset.		
	(red/green)	Flashing	Self-test : While the device is performing its power up testing, the network status indicator shall be flashing green/red.		
	(off)	Off	Not powered, no IP address : If the device does not have an IP address (or is powered off), the network status indicator shall be steady off.		
LINK/RJ45	LED green				
Ch0 & Ch1	🥥 (green)	On	A connection to the Ethernet exists		
	(off)	Off	The device has no connection to the Ethernet		
ACT/RJ45	LED yellow				
Ch0 & Ch1	🥮 (yellow)	Flashing	The device sends/receives Ethernet frames		

Table 41: LEDs EtherNet/IP Scanner (Master)

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet (Slave) when the firmware of the EtherNet/IP Adapter (Slave) protocol is loaded to the device.

LED	Color	State	Meaning			
MS	Duo LED red	d/green				
Name in the device drawing:	(green)	On	Device operational : If the device is operating correctly, the module status indicator shall be steady green.			
COM 0	🥥 (green)	Flashing	Standby : If the device has not been configured, the module status indicator shall be flashing green.			
	(red)	On	Major fault : If the device has detected a non-recoverable major fault, the module status indicator shall be steady red.			
	e (red)	Flashing	Minor fault : If the device has detected a recoverable minor fault, the module status indicator shall be flashing red. NOTE: An incorrect or inconsistent configuration would be considered a minor fault.			
	(red/green)	Flashing	Self-test : While the device is performing its power up testing, the module status indicator shall be flashing green/red.			
	(off)	Off	No power : If no power is supplied to the device, the module status indicator shall be steady off.			
NS	Duo LED red/green					
Name in the device	🥥 (green)	On	Connected : If the device has at least one established connection (even to the Message Router), the network status indicator shall be steady green.			
drawing: COM 1) (green)	Flashing	No connections : If the device has no established connections, but has obtained an IP address, the network status indicator shall be flashing green.			
	(red)	On	Duplicate IP : If the device has detected that its IP address is already in use, the network status indicator shall be steady red.			
	(red)	Flashing	Connection timeout : If one or more of the connections in which this device is the target has timed out, the network status indicator shall be flashing red. This shall be left only if all timed out connections are reestablished or if the device is reset.			
	(red/green)	Flashing	Self-test : While the device is performing its power up testing, the network status indicator shall be flashing green/red.			
	(off)	Off	Not powered, no IP address : If the device does not have an IP address (or is powered off), the network status indicator shall be steady off.			
LINK/RJ45	LED green					
Ch0 & Ch1) (green)	On	A connection to the Ethernet exists			
	(off)	Off	The device has no connection to the Ethernet			
ACT/RJ45	LED yellow	•				
Ch0 & Ch1	🤶 (yellow)	Flashing	The device sends/receives Ethernet frames			

Table 42: LEDs EtherNet/IP Adapter (Slave)

8.9 Open Modbus/TCP

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet when the firmware of the Open Modbus/TCP protocol is loaded to the device.

LED	Color	State	Meaning				
RUN	Duo LED re	d/green					
Name in the	(off)	Off	Not Ready OMB task is not ready				
device drawing: COM 0	🥥 (green)	Flashing cyclic with 1Hz	Ready, not configured yet OMB task is ready and not configured yet				
	🥥 (green)	Flashing cyclic with 5Hz	Waiting for Communication: OMB task is configured				
	🥥 (green)	On	Connected: OMB task has communication – at least one TCP connection is established				
ERR	Duo LED red/green						
Name in the	(off)	Off	No communication error				
device drawing: COM 1	e (red)	Flashing cyclic with 2Hz (On/Off Ratio = 25 %)	System error				
	(red)	On	Communication error active				
LINK/RJ	LED green						
45 Ch0 & Ch1	🥥 (green)	On	A connection to the Ethernet exists				
	(off)	Off	The device has no connection to the Ethernet				
ACT/RJ	LED yellow						
45 Ch0 & Ch1	🤶 (yellow)	Flashing	The device sends/receives Ethernet frames				

Table 43: LEDs Open Modbus/TCP

8.10 POWERLINK Controlled Node/Slave

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet (Slave) when the firmware of the POWERLINK Controlled Node Controlled Node/Slave protocol is loaded to the device.

LED	Color	State	Meaning	
BS	Duo LED red/green			
Name in the device	(off)	Off	Slave initializing	
drawing:) (green)	Flickering	Slave is in 'Basic Ethernet' state	
COM 0		Single Flash	Slave is in 'Pre-Operational 1' state	
		Double Flash	Slave is in 'Pre-Operational 2' state	
		Triple Flash	Slave is in 'ReadyToOperate' state	
		On	Slave is in 'Operational' state	
		Blinking	Slave is in 'Stopped' state	
BE	Duo LED re	d/green		
Name in the device	(off)	Off	Slave has no error	
drawing: COM 1	(red)	On	Slave has detected an error	
L/A/	LED green			
RJ45 Ch0 & Ch1	🥥 (green)	On	Link: A connection to the Ethernet exists	
	🥥 (green)	Flickering	Activity: The device sends/receives Ethernet frames	
	(off)	Off	The device has no connection to the Ethernet	
RJ45	LED yellow			
Ch0 & Ch1	-	-	This LED is not used.	

Table 44: LEDs POWERLINK Controlled Node Controlled Node/Slave

LED State Definition for POWERLINK Controlled Node Controlled Node/Slave for the BS/BE LEDs

Indicator state	Definition	
On	The indicator is constantly on.	
Off	The indicator is constantly off.	
Blinking	The indicator turns on and off with a frequency of approximately 2,5 Hz: on for approximately 200 ms, followed by off for 200 ms. Red and green LEDs shall be on alternately.	
Flickering	The indicator turns on and off with a frequency of approximately 10 Hz: on for approximately 50 ms, followed by off for 50 ms. Red and green LEDs shall be on alternately.	
Single Flash	The indicator shows one short flash (approximately 200 ms) followed by a long off phase (approximately 1,000 ms).	
Double Flash	The indicator shows a sequence of two short flashes (each approximately 200 ms), separated by a short off phase (approximately 200 ms). The sequence is finished by a long off phase (approximately 1,000 ms).	
Triple Flash	The indicator shows a sequence of three short flashes (each approximately 200 ms), separated by a short off phase (approximately 200 ms). The sequence is finished by a long off phase (approximately 1,000 ms).	
	Table 45: LED State Definition for POWERLINK Controlled Node Controlled Node/Slave for	

Table 45: LED State Definition for POWERLINK Controlled Node Controlled Node/Slave for the BS/BE LEDs

8.11 **PROFINET IO-Controller**

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet (Master) when the firmware of the PROFINET IO-Controller protocol is loaded to the device.

LED	Color	State	Meaning	
SF	Duo LED red/green			
Name in the device	(off)	Off	No error	
drawing:	(red)	On	(together with BF "red ON")	
COM 0	. ,		No valid Master license	
	(red)	Flashing cyclic at 2 Hz	System error: Invalid configuration, Watchdog error or internal error	
BF	Duo LED re	d/green		
Name in the device	(off)	Off	No error	
device drawing:	(red)	On	No Connection: No Link.	
COM 1			or (together with SF "red ON")	
			No valid Master license	
	e (red)	Flashing cyclic at 2 Hz	Configuration fault: not all configured IO-Devices are connected.	
LINK/RJ45	LED green			
Ch0 & Ch1	🥥 (green)	On	A connection to the Ethernet exists	
	(off)	Off	The device has no connection to the Ethernet	
RX/TX/ RJ45 Ch0 & Ch1	LED yellow			
	🥯 (yellow)	Flashing	The device sends/receives Ethernet frames	

Table 46: LEDs PROFINET IO-Controller

8.12 PROFINET IO-Device

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet (Slave) when the firmware of the PROFINET IO-Device protocol is loaded to the device

LED	Color	State	Meaning	
SF	Duo LED red/green			
Name in the device) (off)	Off	No error	
drawing: COM 0	(red)	On	Watchdog timeout; channel, generic or extended diagnosis present; system error	
	e (red)	Flashing cyclic at 2 Hz (for 3 sec.)	DCP signal service is initiated via the bus	
BF	Duo LED red/green			
Name in the device) (off)	Off	No error	
drawing: COM 1	e (red)	On	No configuration; or low speed physical link; or no physical link	
COMIT	(red)	Flashing cyclic at 2 Hz	No data exchange	
LINK/RJ45	LED green			
Ch0 & Ch1	🥥 (green)	On	A connection to the Ethernet exists	
	(off)	Off	The device has no connection to the Ethernet	
RX/TX /RJ45 Ch0 & Ch1	LED yellow			
	🥮 (yellow)	Flashing	The device sends/receives Ethernet frames	

Table 47: LEDs PROFINET IO-Device

8.13 sercos Master

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet Master when the firmware of the sercos Master protocol is loaded to the device.

LED	Color	State	Meaning
STA	Duo LED red/green		
Name in	(green)	Blinking	CP0: Communication phase 0
the device drawing: COM 0	🥥 (green)	Flickering	Master isn't configured and is in NRT. After a status change this isn't indicated again
	(green)	Single Flash	CP1: Communication phase 1
	(green)	Double Flash	CP2: Communication phase 2
	(green)	Triple Flash	CP3: Communication phase 3
	(green)	On	CP4: Communication phase 4
	(off)	Off	NRT: Non Real-time Mode
ERR	Duo LED rec	d/green	
Name in	(red)	Blinking	Error in the configuration database.
the device	(red)	Flickering	Boot-up was stopped due to an error.
drawing: COM 1	(red)	Double Flickering	Slave is missing.
	(red)	Single Flickering	Channel Init was executed at the Master.
	(red)	Quadruple Flash	No Master license present in the device
	(red)	Triple Flash	DPM Watchdog has expired.
	(red)	Double Flash	Internal Stop of the bus cycle
	(red)	Single Flash	Bus Sync Error Threshold
	(off)	Off	No error
L/A/RJ45	LED green		
Ch0 & Ch1	(green)	On	Link: A connection to the Ethernet exists
	(green)	Flickering	Activity: The device sends/receives Ethernet frames
	(off)	Off	The device has no connection to the Ethernet
RJ45	LED yellow		
Ch0 & Ch1	-	-	This LED is not used.

Table 48: LEDs sercos Master

LED State Definition for sercos Master for STA and ERR LEDs

Indicator state	Definition	
Off	The indicator is constantly off.	
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.	
Single Flickering	The indicator turns on and off with a frequency of approximately 10 Hz: on for approximately 50 ms, followed by off for 50 ms.	
Double Flickering	The indicator turns on / off / on each for approximately 50 ms, followed by off for 500 ms.	
Flickering	The indicator turns on and off once: on for approximately 50 ms, followed by off for 50 ms.	
Single Flash	The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms).	
Double Flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).	
Triple Flash	The indicator shows a sequence of three short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).	
Quadruple Flash	The indicator shows a sequence of four short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).	

Table 49: LED State Definition for sercos Master for the STA and ERR LEDs

8.14 sercos Slave

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet Slave when the firmware of the sercos Slave protocol is loaded to the device.

LED	Color	State	Meaning
S3			
SS Name in	0444.0	Off	ange = red/green simultaneously) NRT-Mode : No sercos Communication
the device	(off)	On	CP4: Communication phase 4, Normal operation, no error
drawing: COM 0	(green)		
	🥥 (green)	Flashing (4 Hz)	Loopback : The network state has changed from "fast-forward" to "loopback".
	(green/	Flashing (1 x green / 3 s)	CP1 : Communication phase 1: Flashing green for 250 ms, then orange on for 2 second and 750 ms
	orange)	Flashing (2 x green / 3 s)	CP2 : Communication phase 2: Flashing green / orange / green, each for 250 ms, then orange on for 2 seconds and 250 ms
		Flashing (3 x green / 3 s)	CP3 : Communication phase 3: Flashing green / orange / green / orange / green, each for 250 ms, then orange on for 1 second and 750 ms
	(orange/	Flashing (4 Hz)	HP0 : Hot-plug mode (not yet implemented): Flashing orange permanently
	green)	Flashing (1 x orange / 3 s)	HP2 : Hot-plug mode (not yet implemented): Flashing orange for 250 ms, then green on for 2 second an 750 ms
		Flashing (2 x orange / 3 s)	HP3 : Hot-plug mode (not yet implemented): Flashing orange / green / orange, each for 250 ms, then green on for 2 seconds and 250 ms
	(orange)	On	CP0: Communication phase 0
	(orange)	Flashing (4 Hz)	Identification : Corresponds to C-DEV.Bit 15 in the Slave's Device Control indicating remote address allocation or configuration errors between Master and Slaves (for details refer to sercos Slave V3 Protocol API Manual).
	(green/ red)	Flashing (4 Hz), The LED flashes at least for 2 seconds from green to red.	MST losses ≥ (S-0-1003/2): Depends on IDN S-0-1003 (for details refer to sercos Slave Protocol API manual). Corresponds to S-DEV.Bit 15 in the Device Status indicating a communication warning (Master SYNC telegrams have not been received)
	(red /orange)	Flashing (4 Hz)	Application error (C1D): See GDP & FSP Status codes class error. See sercos Slave V3 Protocol API Manual.
	(red)	Flashing (4 Hz)	Watchdog error: Application is not running (not yet implemented)
	(red)	On	Communication Error (C1D) : Error detected according to sercos Class 1 Diagnosis, see SCP Status codes class error. See sercos Slave V3 Protocol API Manual.
Name in	Duo LED re	d/green	
the device drawing: COM 1	-	-	This LED is not used.
L/A/RJ45	LED green	1	
Ch0 & Ch1	(green)	On	Link: A connection to the Ethernet exists
	(green)	Flickering	Activity: The device sends/receives Ethernet frames
	(off)	Off	The device has no connection to the Ethernet
RJ45	LED yellow		
Ch0 & Ch1	-	-	This LED is not used.

Table 50: LEDs sercos Slave

Indicator state	Definition	
On	The indicator is constantly on.	
Off	The indicator is constantly off.	
Flashing (4 Hz)	The indicator turns on and off with a frequency of 4 Hz: first color for appr. 125 ms, followed by the second color for appr. 125 ms.	
Flickering	The indicator turns on and off with a frequency of approximately 10 Hz: on for approximately 50 ms, followed by off for 50 ms.	

LED State Definition for sercos Slave for the S3 LED

Table 51: LED State Definition for sercos Slave for the S3 LED

8.15 VARAN Client (Slave)

The subsequent table describes the meaning of the LEDs for the PC card cifX Real-Time Ethernet Slave when the firmware of the VARAN Client (Slave) protocol is loaded to the device.

LED	Color	State	Meaning	
RUN	Duo LED red/green			
Name in the device) (green)	Blinking	Configured and communication is inactive.	
drawing:) (green)	On	Configured and communication is active.	
COM 0	(off)	Off	Not configured.	
ERR	Duo LED re	d/green		
Name in the device) (off)	Off	Configured.	
drawing: COM 1	(red)	Blinking	Not configured.	
COMIT	(red)	On	Communication error occurred.	
LINK	LED green			
RJ45 Ch0 & Ch1) (green)	On	A connection to the Ethernet exists	
	(off)	Off	The device has no connection to the Ethernet	
ACT	LED yellow			
RJ45 Ch0 & Ch1	🤶 (yellow)	Flashing	The device sends/receives Ethernet frames	

Table 52: LEDs VARAN Client

LED State Definition for VARAN Client for the RUN and ERR LEDs

Indicator state	Definition	
On	The indicator is constantly on.	
Off	The indicator is constantly off.	
Blinking	The indicator turns on and off with a frequency of 5 Hz: on for 100 ms, followed by off for 100 ms.	

Table 53: LED State Definition for VARAN Client for the RUN and ERR LEDs

8.16 PROFIBUS DP Master

8.16.1 1 Communication Status LED

The subsequent table describes the meaning of the LEDs for the PC card cifX PROFIBUS DP Master when the firmware of the PROFIBUS DP Master protocol is loaded to the device.

LED	Color	State	Meaning
cifX with 1 0	Communicatio	on Status LED (cur	rent Hardware Revision)
СОМ	Duo LED red/green		
) (green)	Flashing acyclic	No configuration or stack error
	🥥 (green)	Flashing cyclic	Profibus is configured, but bus communication is not yet released from the application
	(green)	On	Communication to all Slaves is established
	(red)	Flashing cyclic	Communication to at least one Slave is disconnected
	(red)	On	Communication to one/all Slaves is disconnected or annother serious error has occured.
			Redundant Mode: The active Master was not found.

Table 54: LEDs PROFIBUS DP Master – 1 Communication Status LED (current Hardware Revision)

8.16.2 2 Communication Status LEDs

The subsequent table describes the meaning of the LEDs for the PC card cifX PROFIBUS DP Master when the firmware of the PROFIBUS DP Master protocol is loaded to the device.

LED	Color	State	Meaning
cifX with 2 0	Communicatio	on Status LEDs (Al	FX-DP is connected or for prior Hardware Revisions)
STA	LED green		
	(green)	Flashing acyclic	No configuration or stack error
	(green)	Flashing cyclic	Profibus is configured, but bus communication is not yet released from the application
	(green)	On	Communication to all Slaves is established
ERR	LED red		
	(red)	Flashing cyclic	Communication to at least one Slave is disconnected
	(red)	Static on	Communication to one/all Slaves is disconnected or annother serious error has occured.
			Redundant Mode: The active Master was not found.

Table 55: LEDs PROFIBUS DP Master – 2 Communication Status LEDs (AIFX-DP connected or prior Hardware Revision)

8.17 PROFIBUS DP Slave

8.17.1 1 Communication Status LED

The subsequent table describes the meaning of the LEDs for the PC card cifX PROFIBUS DP Slave when the firmware of the PROFIBUS DP Slave protocol is loaded to the device.

LED	Color	State	Meaning
cifX with 1	Communicat	tion Status LED (cu	urrent Hardware Revision)
СОМ	Duo LED red/green		
) (green)	On	RUN, cyclic communication
	lred)	On	Wrong configuration at PROFIBUS-DPside.
	(red)	Flashing cyclic	STOP, no communication, connection error
	(red)	Flashing acyclic	not configured

Table 56: LEDs PROFIBUS DP Slave – 1 Communication Status LED (current Hardware Revision)

8.17.2 2 Communication Status LEDs

The subsequent table describes the meaning of the LEDs for the PC card cifX PROFIBUS DP Slave when the firmware of the PROFIBUS DP Slave protocol is loaded to the device.

LED	Color	State	Meaning		
cifX with 2	cifX with 2 Communication Status LEDs (AIFX-DP is connected or for prior Hardware Revisions)				
STA	LED red				
	🥥 (green)	On	RUN, cyclic communication		
ERR	LED red				
	(red)	On	Wrong configuration at PROFIBUS-DPside.		
	(red)	Flashing cyclic	STOP, no communication, connection error		
	(red)	Flashing acyclic	not configured		

Table 57: LEDs PROFIBUS DP Slave – 2 Communication Status LEDs (AIFX-DP connected or prior Hardware Revision)

8.18 PROFIBUS MPI Device

8.18.1 1 Communication Status LED

The subsequent table describes the meaning of the LEDs for the PC Card cifX PROFIBUS MPI device when the firmware of the PROFIBUS MPI protocol is loaded to the device.

LED	Color	Color State Meaning			
cifX with 1	Communicatio	on Status LED			
СОМ	LED green				
	🥥 (green)	On	Status The device currently holds the PROFIBUS token and is able to transfer telegrams of data.		
	(green)	Blinking (regularly) 5 Hz	Status The device is configured to be a part of the PROFIBUS ring, but it must share the PROFIBUS token with other PROFIBUS-Master devices present on the PROFIBUS ring.		
	(green)	Blinking (regularly) 0.5 Hz	Status Automatic baudrate detection is running		
	(off)	Off	Status The device has not been integrated into the PROFIBUS ring, i.e. it has not been configured correctly or has a wrong configuration or has not received the PROFIBUS token.		

Table 58: LEDs PROFIBUS MPI – 1 Communication Status LED

8.18.2 2 Communication Status LEDs

The subsequent table describes the meaning of the LEDs for the PC Card cifX PROFIBUS MPI device when the firmware of the PROFIBUS MPI protocol is loaded to the device.

LED	Color	State	Meaning		
cifX with 2 C	cifX with 2 Communication Status LEDs (AIFX-DP is connected)				
STA	LED green				
	🥮 (green)	On	Status The device currently holds the PROFIBUS token and is able to transfer telegrams of data.		
	🥌 (green)	Blinking (regularly) 5 Hz	Status The device is configured to be a part of the PROFIBUS ring, but it must share the PROFIBUS token with other PROFIBUS-Master devices present on the PROFIBUS ring.		
) (green)	Blinking (regularly) 0.5 Hz	Status Automatic baudrate detection is running		
	(off)	Off	Status The device has not been integrated into the PROFIBUS ring, i.e. it has not been configured correctly or has a wrong configuration or has not received the PROFIBUS token.		
ERR	R LED red		·		
	-	-	This LED is not used.		

Table 59: LEDs PROFIBUS MPI – 2 Communication Status LEDs (AIFX-DP connected)

8.19 CANopen Master

8.19.1 1 Communication Status LED

The subsequent table describes the meaning of the LEDs for the PC card cifX CANopen Master when the firmware of the CANopen Master protocol is loaded to the device.

LED	Color	State	State Meaning	
cifX with 1	cifX with 1 Communication Status LED (current Hardware Revision)			
CAN	Duo LED red/green			
) (off)	Off	RESET: The device is executing a reset	
	🥥 (green)	Single flash	STOPPED: The device is in STOPPED state	
	🥥 (green)	Blinking	PREOPERATIONAL: The device is in the PREOPERATIONAL state	
	🥥 (green)	On	OPERATIONAL: The device is in the OPERATIONAL state	
	🥮 (red)	Single flash	Warning Limit reached: At least one of the error counters of the CAN controller has reached or exceeded the warning level (too many error frames).	
	🥌 (red)	Double flash	Error Control Event: A guard event (NMT Slave or NMT-master) or a heartbeat event (Heartbeat consumer) has occurred.	
	(red)	On	Bus Off: The CAN controller is bus off	

Table 60: LEDs CANopen Master – 1 Communication Status LED (current Hardware Revision)

LED State Definition	for CANopen Master	for the CAN LED

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Flickering	The indicator turns on and off with a frequency of 10 Hz: on for 50 ms, followed by off for 50 ms.
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.
Single Flash	The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms).
Double Flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).

Table 61: LED State Definition for CANopen Master for the CAN LED

8.19.2 2 Communication Status LEDs

The subsequent table describes the meaning of the LEDs for the PC card cifX CANopen Master when the firmware of the CANopen Master protocol is loaded to the device.

LED	Color State Meaning				
cifX with 2	cifX with 2 Communication Status LEDs (AIFX-CO is connected or for prior Hardware Revisions)				
RUN	LED green				
	(off)	Off	RESET: The device is executing a reset		
	🥥 (green)	Single flash	STOPPED: The device is in STOPPED state		
	(green)	Blinking	PREOPERATIONAL: The device is in the PREOPERATIONAL state		
	🥥 (green)	On	OPERATIONAL: The device is in the OPERATIONAL state		
ERR	LED red				
	e (red)	Single flash	Warning Limit reached: At least one of the error counters of the CAN controller has reached or exceeded the warning level (too many error frames).		
	(red)	Double flash	Error Control Event: A guard event (NMT Slave or NMT-master) or a heartbeat event (Heartbeat consumer) has occurred.		
	(red)	On	Bus Off: The CAN controller is bus off		

Table 62: LEDs CANopen Master – 2 Communication Status LEDs (AIFX-CO connected or prior Hardware Revision)

LED State Definition for CANopen Master for the CAN or RUN/ERR LEDs

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Flickering	The indicator turns on and off with a frequency of 10 Hz: on for 50 ms, followed by off for 50 ms.
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.
Single Flash	The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms).
Double Flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).

Table 63: LED State Definition for CANopen Master for the CAN or RUN/ERR LEDs

8.20 CANopen Slave

8.20.1 1 Communication Status LED

The subsequent table describes the meaning of the LEDs for the PC card cifX CANopen Slave when the firmware of the CANopen Slave protocol is loaded to the device.

LED	Color	State Meaning			
cifX with 1	cifX with 1 Communication Status LED (current Hardware Revision)				
CAN	Duo LED red/green				
	(off)	Off	RESET: The device is executing a reset		
	🥥 (green)	Single flash	STOPPED: The device is in STOPPED state		
	🥥 (green)	Blinking	PREOPERATIONAL: The device is in the PREOPERATIONAL state		
	🥥 (green)	On	OPERATIONAL: The device is in the OPERATIONAL state		
	(red/green)	Flickering (alternatively red / green)	Auto Baud Rate Detection active: The Device is in the Auto Baud Rate Detection mode		
	e (red)	Single flash	Warning Limit reached: At least one of the error counters of the CAN controller has reached or exceeded the warning level (too many error frames).		
	(red)	Double flash	Error Control Event: A guard event (NMT Slave or NMT-master) or a heartbeat event (Heartbeat consumer) has occurred.		
	(red)	On	Bus Off: The CAN controller is bus off		

Table 64: LEDs CANopen Slave – 1 Communication Status LED (current Hardware Revision)

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Flickering	The indicator turns on and off with a frequency of 10 Hz: on for 50 ms, followed by off for 50 ms.
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.
Single Flash	The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms).
Double Flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).

Table 65: LED State Definition for CANopen Slave for the CAN LED

8.20.2 2 Communication Status LEDs

The subsequent table describes the meaning of the LEDs for the PC card cifX CANopen Slave when the firmware of the CANopen Slave protocol is loaded to the device.

LED	Color	State	Meaning			
cifX with 2	cifX with 2 Communication Status LEDs (AIFX-CO is connected or for prior Hardware Revisions)					
RUN	LED green					
	(off)	Off	RESET: The device is executing a reset			
	(green)	Single flash	STOPPED: The device is in STOPPED state			
	(green)	Blinking	PREOPERATIONAL: The device is in the PREOPERATIONAL state			
	(green) On OPERATIO		OPERATIONAL: The device is in the OPERATIONAL state			
	(green)	Flickering (alternatively with ERR LED)	Auto Baud Rate Detection active: The Device is in the Auto Baud Rate Detection mode			
ERR LED red						
	(red)	Single flash	Warning Limit reached: At least one of the error counters of the CAN controller has reached or exceeded the warning level (too many error frames).			
	(red)	Double flash	Error Control Event: A guard event (NMT Slave or NMT-master) or a heartbeat event (Heartbeat consumer) has occurred.			
	(red)	On	Bus Off: The CAN controller is bus off			
	(red)	Flickering (alternatively with RUN LED)	Auto Baud Rate Detection active: The Device is in the Auto Baud Rate Detection mode			

Table 66: LEDs CANopen Slave – 2 Communication Status LEDs (AIFX-CO connected or prior Hardware Revision)

LED State Definition for CANopen Slave for the CAN or RUN/ERR LEDs

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Flickering	The indicator turns on and off with a frequency of 10 Hz: on for 50 ms, followed by off for 50 ms.
Blinking	The indicator turns on and off with a frequency of 2,5 Hz: on for 200 ms, followed by off for 200 ms.
Single Flash The indicator shows one short flash (200 ms) followed by a long off phase (1,000 ms	
Double Flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).

Table 67: LED State Definition for CANopen Slave for the CAN or RUN/ERR LEDs

8.21 DeviceNet Master

The subsequent table describes the meaning of the LEDs for the PC card cifX DeviceNet Master when the firmware of the DeviceNet Master protocol is loaded to the device.

LED	Color	State	Meaning	
MNS	Duo LED red/g	reen		
	🥥 (green)	On	Device Operational AND On-line, Connected	
			Device is online and has established all connections with all Slaves.	
	🥥 (green)	Flashing (1 Hz)	Device Operational AND On-line	
			Device is online and has established no connection in the established state.	
			- Configuration missing, incomplete or incorrect.	
	(green/red/off)	Flashing Green/Red/Off	Selftest after power on: Green on for 250 ms, then red on for 250 ms, then off.	
	(red)	Flashing (1 Hz)	Minor Fault and/or Connection Time-Out	
			Device is online and has established one or more connections in the established state. It has data exchange with at least one of the configured Slaves.	
			Minor or recoverable fault: No data exchange with one of the configured Slaves. One or more Slaves are not connected.	
			Connection timeout	
	(red)	On	Critical Fault or Critical Link Failure	
			Critical connection failure; device has detected a network error: duplicate MAC-ID or severe error in CAN network (CAN-bus off).	
	(off)	Off	Device is not powered	
	(011)		- The device may not be powered.	
			Device is not on-line and/or No Network Power	
			 The device has not completed the Dup_MAC_ID test yet. The device is powered, but the network power is missing. 	

Table 68: LEDs DeviceNet Master

LED State Definition for DeviceNet Master for the MNS LED

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Flashing (1 Hz) greenThe indicator turns on and off with a frequency of appr. 1 Hz: on for appr. 500 ms, followed by off for appr. 500 ms.	
Flashing (1 Hz) red	The indicator turns on and off with a frequency of appr. 1 Hz: on for appr. 500 ms, followed by off for appr. 500 ms.

Table 69: LED State Definition for DeviceNet Master for the MNS LED

8.22 DeviceNet Slave

The subsequent table describes the meaning of the LEDs for the PC card cifX DeviceNet Slave when the firmware of the DeviceNet Master protocol is loaded to the device.

LED	Color	State	Meaning		
MNS	Duo LED red/green				
	(green)	On	Device Operational AND On-line, Connected		
			Device is online and has established all connections with all Slaves.		
	igreen)	Flashing (1 Hz)	Device Operational AND On-line		
			Device is online and has established no connection in the established state.		
			- Configuration missing, incomplete or incorrect.		
	(green/red/off)	Flashing Green/Red/Off	Selftest after power on: Green on for 250 ms, then red on for 250 ms, then off.		
	(red)	Flashing (1 Hz)	Minor Fault and/or Connection Time-Out		
			Device is online and has established one or more connections in the established state. It has data exchange with at least one of the configured Slaves.		
			Minor or recoverable fault: No data exchange with one of the configured Slaves. One or more Slaves are not connected.		
			Connection timeout		
	(red)	On	Critical Fault or Critical Link Failure		
			Critical connection failure; device has detected a network error: duplicate MAC-ID or severe error in CAN network (CAN-bus off).		
	(off)	Off	Device is not powered		
			- The device may not be powered.		
			Device is not on-line and/or No Network Power		
			 The device has not completed the Dup_MAC_ID test yet. The device is powered, but the network power is missing. 		

Table 70: LEDs DeviceNet Slave

LED State Definition for DeviceNet Slave for the MNS LED

Indicator state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Flashing (1 Hz) green	The indicator turns on and off with a frequency of appr. 1 Hz: on for appr. 500 ms, followed by off for appr. 500 ms.
Flashing (1 Hz) red	The indicator turns on and off with a frequency of appr. 1 Hz: on for appr. 500 ms, followed by off for appr. 500 ms.

Table 71: LED State Definition for DeviceNet Slave for the MNS LED

9 Device Connections and Switches

9.1 Ethernet Interface

For the Ethernet interface use RJ45 plugs and twisted pair cable of category 5 (CAT5) or higher, which consists of 4 twisted cores and has a maximum transmission rate of 100 MBit/s (CAT5).

9.1.1 Ethernet Pin Assignment at the RJ45 Socket

Note: The device supports the **Auto Crossover** function. Due to this fact RX and TX can be switched. The following figure shows the RJ45 standard pin assignment.

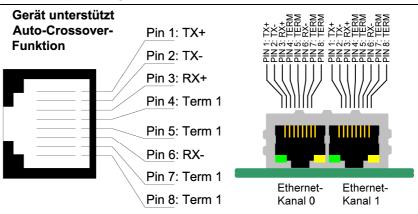


Figure 49: Ethernet Pin Assignment at the RJ45 Socket for cifX or AIFX

Pin	Signal	Meaning	
1	TX+	Transmit Data +	
2	TX–	Transmit Data –	
3	RX+	Receive Data +	
4	Term 1	Connected to each other and	
5	Term 1	terminated to PE through RC circuit*	
6	RX–	Receive Data –	
7	Term 2	Connected to each other and	
8	Term 2	terminated to PE through RC circuit*	
		* Bob Smith Termination	

Table 72: Ethernet Pin Assignment at the RJ45 Socket for cifX or AIFX



Further Notes:

(1) The RJ45 socket is only for use in LAN, not for telecommunication circuits.

(2) With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the EtherCAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For the Open Modbus/TCP firmware with V2.3.4.0 and higher both RJ45 RJ45channels can be used.

9.1.2 Ethernet Connection Data

Medium 2 x 2 Twisted-Pair cupric cable, CAT5 (100 MBit/s)		
Length of cable Typ. 100 m		
Transmission rate	10 MBit/s/100 MBit/s	

Table 73: Ethernet Connection Data

9.1.3 Use of Hubs and Switches

For the corresponding communication systems, the use of hubs and/or switches is either forbidden or allowed. The following table shows the acceptable use of hubs and switches by each communication system:

Communication System	Hub	Switch	
EtherCAT	forbidden	only allowed between EtherCAT Master and first EtherCAT Slave (100 MBit/s, Full Duplex)	
EtherNet/IP	allowed	allowed (10 MBit/s/100 MBit/s, Full or Half Duplex, Auto-Negotiation)	
Open Modbus/TCP	allowed	allowed (10 MBit/s/100 MBit/s, Full or Half Duplex, Auto-Negotiation)	
POWELINK	allowed	forbidden	
PROFINET IO	forbidden	Only allowed if the switch supports ,Priority Tagging' and LLDP (100 MBit/s, Full Duplex)	
sercos	forbidden	forbidden	
VARAN*	forbidden	forbidden	

Table 74: Use of Hubs and Switches

*Instead of hubs and switches VARAN uses splitter. [3]

9.2 **PROFIBUS Interface**

Isolated RS-485 interface:

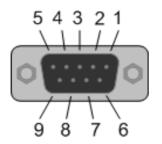


Figure 50: PROFIBUS Interface (DSub female connector, 9 pin), X400

Connection with DSub female connector	Signal	Meaning
3	RxD/TxD-P	Receive/Send Data-P respectively connection B plug
5	DGND	Reference potential
6	VP	Positive supply voltage
8	RxD/TxD-N	Receive/Send Data-N respectively connection A plug

Table 75: PROFIBUS Interface, X400

9.3 CANopen Interface

Isolated ISO 11898 interface:

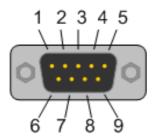


Figure 51: CANopen Interface (DSub male connector, 9 pin), X400

Connection with DSub male connector	Signal	Description
2	CAN_L	CAN_Low Bus Line
3	CAN_GND	CAN Ground
7	CAN_H	CAN High Bus Line
1, 4, 5, 6, 8, 9		Do not connect!

Table 76: CANopen Interface, X400

9.4 DeviceNet Interface

Isolated ISO 11898 interface:

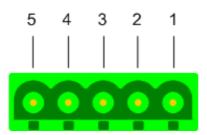


Figure 52: DeviceNet Interface (CombiCon male Connector, 5 pin), X360

Connection with CombiCon male connector	Signal	Color	Description
1	V-	Black	Reference potential DeviceNet supply voltage
2	CAN_L	Blue	CAN Low-Signal
3	Drain		Shield
4	CAN_H	White	CAN High-Signal
5	V+	Red	+24 V DeviceNet supply voltage

Table 77: DeviceNet Interface, X360

9.5 Mini-B USB Connector (5 Pin)

The Mini-B USB connector is provided for the following PC cards cifX: CIFX 80-RE, CIFX 80-DP, CIFX 80-CO, CIFX 80-DN, CIFX 104C-RE, CIFX 104C-DP, CIFX 104C-CO, CIFX 104C-DN, CIFX 104C-RE-R, CIFX 104C-DP-R, CIFX 104C-CO-R, CIFX 104C-DN-R

In addition a Mini-B USB connector will be available for the following PC cards cifX if the AIFX-DIAG is connected to the PC card cifX: CIFX 104C-RE\F*, CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DN\F, CIFX 104C-RE-R\F*, CIFX 104C-DP-R\F, CIFX 104C-CO-R\F, CIFX 104C-DN-R\F



Note! *From the hardware revision 5 of the PC cards CIFX 104C-RE\F and CIFX 104C-RE-R\F if the diagnostic **AIFX-DIAG** assembly interface is connected, the **Mini-B USB** connector on the AIFX-DIAG can be used.

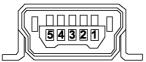


Figure 53: Mini-B USB Connector (5 Pin)

Pin	Name	Description
1	USB_EXT	USB Bus Power (+5 V dc, supplied externally)
2	D-	Data -
3	D+	Data +
4	ID	Not connected
5	GND	Ground

Table 78: Pin Assignment Mini-B USB Connector

9.6 Rotary Switch for PCI-104 Slot Number

Only for CIFX 104C cards.

The **Rotary Switch for PCI-104 Slot Number** is used for setting the physical PCI-104 Slot Number. At maximum four CIFX 104C cards can be plugged as modules one upon the other and each switch position must be used only once. The module plugged next to the host controller gets the CLK number 0, the following modules respectively get the next higher CLK number.

Switch Position	Module No. PCI-104 Slot	CLK No. (Clock)	ID-Select	INT
0, 4, 8	1	CLK 0	IDSEL 0	INTA
1, 5, 9	2	CLK 1	IDSEL 1	INTB
2, 6	3	CLK 2	IDSEL 2	INTC
3, 7	4	CLK 3	IDSEL 3	INTD

Table 79: Rotary Switch for PCI-104 Slot Number, S1

9.7 Rotary Switch Device Address

The **Rotary Switch Device Address** at the PC cards CIFX 104C-RE, CIFX 104C-RE-R, CIFX 104C-RE\F, CIFX 104C-RE-R\F; CIFX 104C-DP, CIFX 104C-DP-R, CIFX 104C-DP\F, CIFX 104C-DP-R\F, CIFX 104C-CO, CIFX 104C-CO-R, CIFX 104C-CO\F, CIFX 104C-CO-R\F, CIFX 104C-DN, CIFX 104C-DN-R, CIFX 104C-DN\F, CIFX 104C-DN-R\F currently is unassigned. The Slave address setting is done via the configuration software.

9.8 Cable Connector

9.8.1 Pin Assignment for Cable Connector Ethernet

Only for CIFX 90-RE\F (X4), CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F (X4), CIFX 104C-RE\F (X304), CIFX 104C-RE-R\F (X4).

Pin Assignment for Cable Connector Ethernet X4 or X304 - Cable 20 pin Ethernet and Status LEDs:

Pin	Signal	Pin	Signal
1	GND	11	CH0_TXP
2	+3V3 Analog	12	CH0_TXN
3	STA0_green (<i>RE LED COM 0</i>)	13	CH0_RXP
4	STA0_red (<i>RE LED COM 0</i>)	14	CH0_RXN
5	XM0_TX	15	CH1_TXP
6	STA1_green (RE LED COM 1)	16	CH1_TXN
7	CH0_LINKn	17	CH1_RXP
8	CH0_ACTIVITY	18	CH1_RXN
9	AIFINIT	19	CH1_LINKn
10	STA1_red (RE LED COM 1)	20	CH1_ACTIVITY

Table 80: Pin Assignment for Cable Connector Ethernet X4 or X304

Cable Connector Ethernet:



Figure 54: 1x20 Pins for CIFX 104C-RE\F, CIFX 104C-RE-R\F, CIFX 90-RE\F or CIFX 90-RE\F 00-RE\F

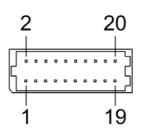


Figure 55: 2x20 Pins for CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F or CIFX 90E-RE\MR\F

9.8.2 Pin Assignment for Cable Connector Fieldbus X3, X304, X4

Only for

CIFX 90-DP\F, CIFX 90-CO\F, CIFX 90-DN\F (X3) and CIFX 90E-DP\F, CIFX 90E-CO\F, CIFX 90E-DN\F, CIFX 90E-DP\ET\F, CIFX 90E-CO\ET\F, CIFX 90E-DN\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-CO\MR\F, CIFX 90E-DN\MR\F, CIFX 90E-DP\MR\ET\F, CIFX 90E-CO\MR\ET\F, CIFX 90E-DN\MR\F, CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DN\F: (X304); CIFX 104C-DP-R\F, CIFX 104C-CO-R\F, CIFX 104C-DN-R\F: (X4).

Pin Assignment for Cable connector Fieldbus X3, X304 or X4, Cable 10 pin Fieldbus:

Pin	Signal
1	GND
2	+3V3 Analog
3	I2C_CLK/PIO 4
4 I2C_DATA/ PIO	
5 XMAC2_TX	
6	XMAC2_RX
7	XMAC2_IO0
8	XMAC2_IO1
9	/RSTOUT
10	(not used)

Table 81: Pin Assignment for Cable connector Fieldbus X3, X304 or X4

9.8.3 Pin Assignment for Cable Connector DIAG

Only for CIFX 104C-RE\F (X303), CIFX 104C-RE-R\F (X3), CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DN\F: (X303)

Pin Assignment for Cable connector DIAG X3 or X303 -Cable 12 pin USB + Status LEDs

Pin	Signal (Feldbus)	Signal (Ethernet)
1	GND	GND
2	+3V3	+3V3
3	STA2 (FB LED COM 0)	STA2 (not used)
4	STA3 (FB LED COM 1)	STA3 (not used)
5	USB_POS	USB_POS
6	USB_NEG	USB_NEG
7	RDYn	RDYn
8	RUNn	RUNn
9	STA0_green (<i>not used</i>)	STA0_green (<i>RE LED COM 0</i>)
10	STA0_red (<i>not used</i>)	STA0_red (<i>RE LED COM 0</i>)
11	STA1_green (not used)	STA1_green (RE LED COM 1)
12	STA1_red (not used)	STA1_red (<i>RE LED COM 1</i>)

Table 82: Pin Assignment for Cable connector DIAG X3 or X303

9.9 SYNC Connector (Pin-Assignment, Hardware/Firmware)

9.9.1 Pin Assignment SYNC Connector, X51 (CIFX 80 90 104C)

Only for CIFX 80-RE, CIFX 90-RE\F, CIFX 104C-RE, CIFX 104C-RE-R, CIFX 104C-RE\F, CIFX 104C-RE-R\F.

Pin	Signal	
1	GND	
2	IO_SYNC0	
3	IO_SYNC1	

Table 83: Pin Assignment for SYNC Connector, X51

CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F and CIFX 90E-RE\MR\ET\F: The SYNC pins are on the Mini PCI Express bus (Pin 46, 44), refer to section *Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bootstart), X1/X2*, on page 111.

9.9.2 Items on Hardware

Item	Explanation	
SYNC Signal	3,3 V (LVTTL), maximum load 6 mA	
Connector	SYNC connector, X51 (for the PC cards cifX, as indicated under section <i>Pin Assignment SYNC Connector, X51</i> on page 107.) Female connector, 3 pin, pitch spacing 1.25 mm (for example, the type Molex series 51021) and female crimp contacts in design (e. g. type Molex series 50079/50058)	
Max. Cable Length	Recommendation: Max. 50 mm Note : Take EMC into consideration for the cable laying	

Table 84: SYNC Connector: SYNC Signal, Connector, Max. Cable Length

9.9.3 Items on Firmware

The loaded firmware determines if the signal is an input signal or output signal. The following table shows the meaning of the SYNC signals for the real-time Ethernet protocols currently offering SYNC signal support.

Protocol	Signal IO_SYNC0 Input/Output	Signal IO_SYNC1 Input/Output	From Firmware Version	Remarks
EtherCAT Slave	SYNC 0	SYNC 1	-	Configurable
	Output	Output		
PROFINET IO	Bus cycle start (PROFINET IRT)	-	3.4.x.x	-
Device	Output			
sercos III Master	External trigger to start bus cycle	-	2.0.8.0	-
	Input			
	Rising edge			
sercos III Slave	CON_CLK	DIV_CLK	3.0.10.0	Configurable
	Output	Output		

Table 85: Meaning of the SYNC Signals for each Protocol

9.10 Pin Assignment at the PCI Bus

9.10.1 Overview

For the PC cards cifX *Compact PCI*, *Mini PCI*, *Mini PCI Express* and *PCI-104* the table below gives an overview about the pin assignment at the bus.

cifX	Hardware	PCI Bus	PCI	Pin Assignm	nent at the PCI Bus	PCI Specifi-
	Revision	[Pins]	according to the standard	Compare Section, page	cation	
CIFX 80-RE CIFX 80-DP CIFX 80-CO CIFX 80-DN	3 3 3 3	Compact PCI	110	yes	-	[bus spec 4]
CIFX 90-RE\F CIFX 90-DP\F CIFX 90-CO\F CIFX 90-DN\F	4 4 4 4	Mini PCI	124	yes	Pin Assignment for Mini PCI Bus, X1, 109	[bus spec 5]
CIFX 90E-RE\F CIFX 90E-DP\F CIFX 90E-DN\F CIFX 90E-DN\F CIFX 90E-RE\ET\F CIFX 90E-DP\ET\F CIFX 90E-DP\ET\F CIFX 90E-DN\ET\F CIFX 90E-DP\MR\F CIFX 90E-DN\MR\F CIFX 90E-DN\MR\F CIFX 90E-DP\MR\ET\F CIFX 90E-DN\MR\ET\F CIFX 90E-CO\MR\ET\F CIFX 90E-DN\MR\ET\F	B B 1 1 1 8 B B 1 1 1	Mini PCI Express	52	no	Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bootstart), X1/X2, 111	[bus spec 2, Rev. 1.0a], [bus spec 6]
CIFX 104C-RE CIFX 104C-RE-R CIFX 104C-RE-R CIFX 104C-RE-R CIFX 104C-DP CIFX 104C-DP-R CIFX 104C-DP-R CIFX 104C-DP-R CIFX 104C-CO-R CIFX 104C-CO-R CIFX 104C-CO-R CIFX 104C-CO-R CIFX 104C-DN CIFX 104C-DN-R CIFX 104C-DN-R CIFX 104C-DN-R	5 5 5 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	PCI-104	120	yes	-	[bus spec 7]

Table 86: Pin Assignment at the PCI Bus

9.10.2 References PCI Specifications

No.	Specification	Revision	Version	De	www
[bus spec 2]	PCI Express [®] Base Specification	2.0	-	January 15, 2007	pcisig.com
[bus spec 4]	CompactPCI [™] Specification Short Form	2.1	2.0	September 2, 1997	picmg.org
[bus spec 5]	Mini PCI Specification	1.0	-	Oct, 25th 1999	pcisig.com
[bus spec 6]	PCI Express Mini Card Electromechanical	1.1	-	March 28, 2005	
	Specification	1.2	-	October 26, 2007	
		2.0	-	April 21, 2012	
[bus spec 7]	PCI-104 Specification		1.0	November 2003	

Table 87: References PCI Specifications

9.10.3 Pin Assignment for Mini PCI Bus, X1

Pin (Top)	Signal	Pin (Bottom)	Signal
1	(not used)	2	(not used)
	Кеу		Кеу
3	(not used)	4	(not used)
5	(not used)	6	(not used)
7	(not used)	8	(not used)
9	(not used)	10	(not used)
11	(not used)	12	(not used)
13	(not used)	14	(not used)
15	(not used)	16	(not used)
17	(not used)	18	(not used)
19	3.3V	20	INTA#
21	(not used)	22	(not used)
23	GROUND	24	(not used)
25	CLK	26	RST#
27	GROUND	28	VIO: 3.3V or 5 V, de- pends on signal voltage
29	REQ#	30	GNT#
31	3.3V	32	GROUND
33	AD[31]	34	(not used)
35	AD[29]	36	(not used)
37	GROUND	38	AD[30]
39	AD[27]	40	3.3V
41	AD[25]	42	AD[28]
43	(not used)	44	AD[26]
45	C/BE[3]#	46	AD[24]
47	AD[23]	48	IDSEL
49	GROUND	50	GROUND
51	AD[21]	52	AD[22]
53	AD[19]	54	AD[20]
55	GROUND	56	PAR
57	AD[17]	58	AD[18]

Pin (Top)	Signal	Pin (Bottom)	Signal
59	C/BE[2]#	60	AD[16]
61	IRDY#	62	GROUND
63	3.3V	64	FRAME#
65	CLKRUN#	66	TRDY#
67	SERR#	68	STOP#
69	GROUND	70	3.3V
71	PERR#	72	DEVSEL#
73	C/BE[1]#	74	GROUND
75	AD[14]	76	AD[15]
77	GROUND	78	AD[13]
79	AD[12]	80	AD[11]
81	AD[10]	82	GROUND
83	GROUND	84	AD[09]
85	AD[08]	86	C/BE[0]#
87	AD[07]	88	(not used)
89	3.3V	90	AD[06]
91	AD[05]	92	AD[04]
93	(not used)	94	AD[02]
95	AD[03]	96	AD[00]
97	(not used)	98	(not used)
99	AD[01]	100	(not used)
101	GROUND	102	GROUND
103	(not used)	104	(not used)
105	(not used)	106	(not used)
107	(not used)	108	(not used)
109	(not used)	110	(not used)
111	(not used)	112	(not used)
113	(not used)	114	GROUND
115	(not used)	116	(not used)
117	(not used)	118	(not used)
119	(not used)	120	(not used)
121	(not used)	122	MPCIACT#, over 120 w to GROUND
123	(not used)	124	(not used)

Table 88: Pin Assignment for Mini PCI Bus, X1

The pin assignment described in *Table 88* originates from the standard pin assignment for Mini PCI Connector III [bus spec 4, page 14] (refer to section *References PCI Specifications* on page 109).

Pin Assignment for Mini PCI Express Bus / SYNC Connector 9.10.4 (Bootstart), X1/X2

Only for Hardware Revision B: CIFX 90E-RE\F*, CIFX 90E-DP\F, CIFX 90E-CO\F, CIFX 90E-DN\F,

CIFX 90E-RE\MR\F*, CIFX 90E-DP\MR\F, CIFX 90E-CO\MR\F, CIFX 90E-DN\MR\F,

And for Hardware Revison 1:

CIFX 90E-RE\ET\F*, CIFX 90E-DP\ET\F, CIFX 90E-CO\ET\F, CIFX 90E-DN\ET\F, CIFX 90E-RE\MR\ET\F*, CIFX 90E-DP\MR\ET\F, CIFX 90E-CO\MR\ET\F, CIFX 90E-DN\MR\ET\F

*The SYNC connection is realized via the mini PCI Express bus.

Pin Assignment Mini PCI Express Bus / SYNC Connector, X1/X2

51 (not used) 52 $+3.3V$ 49 (not used) 50 GND 47 (not used) 48 (not used) 45 (not used) 46 IO_SYNC0 (not used with fieldbus protocols) 43 (not used) 44 IO_SYNC1 (not used with fieldbus protocols) 41 (not used) 44 IO_SYNC1 (not used with fieldbus protocols) 39 (not used) 42 Bootstart 39 (not used) 38 USB_D+ (disabled - not used) 37 (not used) 36 USB_D - (disabled - not used) 33 PERp0 34 GND 31 PERn0 32 (not used) 29 GND 30 (not used) 27 GND 28 (not used) 23 PETn0 26 GND 21 GND 22 PERST# 19 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 9 GND 9 GND 10 (not used) 11 REFCLK- 8 (not used) 9 GND 10 (not used) 12 (not used) 10 13 REFCLK- 8 9 GND 10 10 (not used) 10 (not used) 10 (not used)	Pin (X1)	Signal	Pin (X2)	Signal
47 (not used) 45 (not used) 43 (not used) 43 (not used) 44 IO_SYNC0 (not used with fieldbus protocols) 41 (not used) 44 IO_SYNC1 (not used with fieldbus protocols) 41 (not used) 42 Bootstart 39 (not used) 37 (not used) 36 GND 37 (not used) 38 USB_D+ (disabled – not used) 36 USB_D - (disabled – not used) 37 GND 38 USB_D - (disabled – not used) 39 (not used) 30 (not used) 31 PERp0 32 (not used) 33 PERp0 34 GND 25 PETp0 26 GND 27 GND 28 (not used) 29 (not used) 14 (not used) 15 GND 16 (not used) <t< td=""><td>51</td><td>(not used)</td><td>52</td><td>+3.3V</td></t<>	51	(not used)	52	+3.3V
45 (not used) 43 (not used) 44 IO_SYNC0 (not used with fieldbus protocols) 41 (not used) 39 (not used) 37 (not used) 35 GND 36 USB_D+ (disabled – not used) 37 (not used) 38 USB_D+ (disabled – not used) 33 PERp0 31 PERn0 29 GND 30 (not used) 32 (not used) 33 PERp0 34 GND 35 GND 36 USB_D - (disabled – not used) 37 (not used) 38 USB_D - (disabled – not used) 39 GND 30 (not used) 21 GND 22 PERST# 19 (not used) 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 15 GND 16 (not used)	49	(not used)	50	GND
43 (not used) 44 IO_SYNC1 (not used with fieldbus protocols) 41 (not used) 42 Bootstart 39 (not used) 40 GND 37 (not used) 38 USB_D+ (disabled – not used) 35 GND 36 USB_D - (disabled – not used) 33 PERp0 34 GND 31 PERn0 32 (not used) 29 GND 30 (not used) 27 GND 28 (not used) 23 PETn0 26 GND 21 GND 22 PERST# 19 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	47	(not used)	48	(not used)
41 (not used) 42 Bootstart 39 (not used) 40 GND 37 (not used) 38 USB_D+ (disabled - not used) 35 GND 36 USB_D - (disabled - not used) 33 PERp0 34 GND 31 PERn0 32 (not used) 29 GND 30 (not used) 27 GND 28 (not used) 23 PETn0 26 GND 21 GND 22 PERST# 19 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	45	(not used)	46	IO_SYNC0 (not used with fieldbus protocols)
39 (not used) 39 (not used) 37 (not used) 35 GND 33 PERp0 31 PERn0 29 GND 27 GND 28 (not used) 27 GND 28 (not used) 29 PETn0 21 GND 21 GND 19 (not used) 17 (not used) 15 GND 13 REFCLK+ 11 REFCLK- 9 GND 7 CLKREQ#	43	(not used)	44	IO_SYNC1 (not used with fieldbus protocols)
37 (not used) 35 GND 35 GND 33 PERp0 31 PERn0 29 GND 27 GND 25 PETp0 23 PETn0 21 GND 19 (not used) 17 (not used) 18 GND 13 REFCLK+ 11 REFCLK- 9 GND 7 CLKREQ#	41	(not used)	42	Bootstart
35 GND 36 USB_D - (disabled - not used) 33 PERp0 34 GND 31 PERn0 32 (not used) 29 GND 30 (not used) 27 GND 28 (not used) 25 PETp0 26 GND 23 PETn0 24 (not used) 21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	39	(not used)	40	GND
33 PERp0 34 GND 31 PERn0 32 (not used) 29 GND 30 (not used) 27 GND 28 (not used) 25 PETp0 26 GND 23 PETn0 24 (not used) 21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	37	(not used)	38	USB_D+ (disabled – not used)
31 PERn0 29 GND 27 GND 25 PETp0 23 PETn0 21 GND 19 (not used) 17 (not used) 15 GND 13 REFCLK+ 11 REFCLK- 9 GND 7 CLKREQ#	35	GND	36	USB_D - (disabled – not used)
29 GND 30 (not used) 27 GND 28 (not used) 25 PETp0 26 GND 23 PETn0 24 (not used) 21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	33	PERp0	34	GND
27 GND 28 (not used) 25 PETp0 26 GND 23 PETn0 24 (not used) 21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	31	PERn0	32	(not used)
25 PETp0 26 GND 23 PETn0 24 (not used) 21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	29	GND	30	(not used)
23 PETn0 24 (not used) 21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	27	GND	28	(not used)
21 GND 22 PERST# 19 (not used) 20 (not used) 17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	25	PETp0	26	GND
19 (not used) 20 (not used) 17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	23	PETn0	24	(not used)
17 (not used) 18 GND 15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	21	GND	22	PERST#
15 GND 16 (not used) 13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	19	(not used)	20	(not used)
13 REFCLK+ 14 (not used) 11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	17	(not used)	18	GND
11 REFCLK- 12 (not used) 9 GND 10 (not used) 7 CLKREQ# 8 (not used)	15	GND	16	(not used)
9 GND 10 (not used) 7 CLKREQ# 8 (not used)	13	REFCLK+	14	(not used)
7 CLKREQ# 8 (not used)	11	REFCLK-	12	(not used)
	9	GND	10	(not used)
	7	CLKREQ#	8	(not used)
5 (not used) 6 (not used)	5	(not used)	6	(not used)
3 (not used) 4 GND	3	(not used)	4	GND
1 (not used) 2 3.3V	1	(not used)	2	3.3V

Table 89: Pin Assignment Mini PCI Express Bus / SYNC Connector, X1/X2

Unless otherwise noted, the pin assignment for Mini PCI Express bus, X1/X2 described in Table 89 corresponds to the bus specification for Mini PCI Express [bus spec 6, Rev. 1.2, Section 3.3].



Note! Please note the following characteristics for the pin assignment of the Mini PCI Express Bus, X1/X2, described in Table 89:

- The Pins 6, 28, 48 and Pin 24 are 'not used'.
- The Pins 36 and 38 are 'not used'.
- The pin assignment of the **Pins 42, 44, 46** deviates from the standard specification Mini PCI Express.



For the reference for [bus spec 6] for the bus specification for Mini PCI Express refer to section *References PCI Specifications* on page 109 of this manual.

About earlier Hardwar Revisions refer to section *Notes on earlier Hardeware Revisions* from page 175.

Pins 6, 28, 48

For the PC cards CIFX 90E-XXF (alle variants 'MR' or 'ET')⁷ the **Pins 6**, **28**, **48** are '**not used**', as listed in the *Table 89* on page 111.

<u>Pin 24</u>

For the PC cards CIFX 90E-XX\F (all variants 'MR' or 'ET') the **Pin 24** is **'not used'**.

Note: Due to how **pin 24** is used, the PC cards CIFX 90E-XX\F (all variants 'MR' or 'ET') can be used together with main boards according to any earlier revision (1.1 and 1.2) of the Mini PCI Express specification [bus spec 6], as well as to the latest revision (2.0).

Pins 36 and 38 (USB Connector)

The USB connector at the Mini PCI Express Bus of the PC cards CIFX 90E-XX\F (all variants 'MR' or 'ET') is directly connected to the CPU of the PC and is not used for external diagnosis.

• The **Pins 36** and **38** are disabled and are **'not used'**. When you start the PC, the operating system does not ask for an USB driver.

Pin 42 (Bootstart) and 44, 46 (SYNC)

- Pin 42 (Bootstart): For all PC cards CIFX 90E-XX\F (all variants 'MR' or 'ET') pin 42 is used for Bootstart.
- Pins 44 , 46 (SYNC): For the PC cards CIFX 90E-RE\F (all Real-Time Ethernet variants 'MR' or 'ET')depending by the protocol the SYNC connection is realized via the pins 44 and 46 of the mini PCI Express bus. For further information to the SYNC pins (Pin 46, 44) refer to section SYNC Connector (Pin-Assignment, Hardware/Firmware) on page 107.

According to the bus specification for Mini PCI Express [bus spec 6] the pins are used to realize the LED status (Pin 42 "WWAN#", Pin 44 "WLAN#", Pin 46 "WPAN#").

⁷ Variants of the PC card CIFX 90E-XX\F: CIFX 90E-XX\ET\F, CIFX 90E-XX\MR\F or CIFX 90E-XX\MR\ET\F (XX = RE, DP, CO or DN)

10 Technical Data

10.1 Technical Data PC Cards cifX

Note: All technical data are temporarily and can be altered without notice.

10.1.1 CIFX 80-RE

CIFX 80-RE	Parameter	Value
Part	Name	CIFX 80-RE
	Part No.	1280.100
	Description	PC Card cifX Compact PCI for Real-Time Ethernet Master or Slave
	Function	Communication interface with Compact PCI and Ethernet interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Compact PCI, according to [bus spec 4], refer to section <i>Overview</i> , page 108.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
Ethernet	Supported Real-Time Ethernet	EtherCAT Master, EtherCAT Slave
Communication	communication systems (determined by the loaded firmware)	EtherNet/IP Scanner (Master), EtherNet/IP Adapter (Slave),
		Open Modbus/TCP
		POWERLINK Controlled Node/Slave
		PROFINET IO-Controller (Master), PROFINET IO-Device (Slave)
		sercos Master, sercos Slave
		VARAN Client (Slave)
	Ethernet Frame Types	Ethernet II
Ethernet Interface	Transmission rate	100 MBit/s, 10 MBit/s (depending on loaded firmware)
	Interface Type	100 BASE-TX, 10 BASE-T (depending on loaded firmware), refer to section <i>Ethernet Interface</i> , page 100.
	Galvanic Isolation	isolated
	Isolation Voltage	1000 VDC (tested for 1 minute)
	Half duplex/Full duplex	depending on loaded firmware, supported (at 100 MBit/s)
	Auto-Negotiation	depending on loaded firmware
	Auto-Crossover	depending on loaded firmware
	Connector	2* RJ45 Socket

	Parameter	Value
CIFX 80-RE	Parameter	
	Channel 0 and 1	With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the EtherCAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For Open Modbus/TCP with V2.3.4.0 and higher both RJ45 channels can be used.
Diagnostic Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.
Display	LED Display	SYS System Status LED
		The meaning of the following LEDs depends on the loaded firmware:
		COM 0 LED Communication Status 0 (duo LED)
		COM 1 LED Communication Status 1 (duo LED)
		LED yellowat RJ45Ch0 and RJ45Ch1,LED greenfor Ethernet Link status, Ethernet Aktivity status and additional status
		Refer to chapter LED Descriptions, page 78.
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Compact PCI Bus
Environmental	Operating temperature range*	0 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	0 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	162,2 x 100 x 20 mm
	Mounting/Installation	Compact PCI slot (3,3 V), refer to section <i>Slot for the PC</i> <i>Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net

CIFX 80-RE	Parameter	Value
	Configuration Software Slave	netX Configuration Tool

Table 90: Technical Data CIFX 80-RE

10.1.2 CIFX 80-DP

CIFX 80-DP	Parameter	Value
Part	Name	CIFX 80-DP
	Part No.	1280.410
	Description	PC Card cifX Compact PCI PROFIBUS DP Master or Slave and PROFIBUS MPI Device
	Function	Communication interface with Compact PCI and PROFIBUS interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Compact PCI, according to [bus spec 4], refer to section <i>Overview</i> , page 108.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
PROFIBUS Communication	Supported communication standard/ protocol (determined by the loaded firmware)	PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device
PROFIBUS Interface	Transmission rate	9,6 kBit/s, 19,2 kBit/s, 31,25 kBit/s, 45,45 kBit/s, 93,75 kBit/s, 187,5 kBit/s, 500 kBit/s, 1,5 MBit/s, 3 MBit/s, 6 MBit/s, 12 MBit/s
	Interface Type	RS 485, refer to section PROFIBUS Interface page 102.
	Galvanic Isolation	isolated
	Isolation Voltage	1000 VDC (tested for 1 minute)
	Connector	DSub female Connector, 9 pin
Diagnostic Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.
Display	LED Display	SYS System Status LED
		COM LED Communication Status (duo LED)
		The meaning of the LED COM depends on the loaded firmware. Refer to chapter <i>LED Descriptions</i> , page 78.
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Compact PCI Bus
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.

CIFX 80-DP	Parameter	Value
Device	Dimensions (L x W x H)	162,2 x 100 x 20 mm
	Mounting/Installation	Compact PCI slot (3,3 V), refer to section <i>Slot for the PC</i> <i>Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 91: Technical Data CIFX 80-DP

10.1.3 CIFX 80-CO

CIFX 80-CO	Parameter	Value
Part	Name	CIFX 80-CO
	Part No.	1280.500
	Description	PC Card cifX Compact PCI CANopen Master or Slave
	Function	Communication interface with Compact PCI and CANopen interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Compact PCI, according to [bus spec 4], refer to section <i>Overview</i> , page 108.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
CANopen Communication	Supported communication standard/ protocol (determined by the loaded firmware)	CANopen Master, CANopen Slave
CANopen Interface	Transmission rate	10 kBit/s, 20 kBit/s, 50 kBit/s, 100 kBit/s, 125 kBit/s, 250 kBit/s, 500 kBit/s, 800 kBit/s, 1 MBit/s
	Interface Type	ISO-11898, refer to section CANopen Interface, page 102.

CIFX 80-CO	Parameter	Value
	Galvanic Isolation	optically isolated
	Isolation Voltage	1000 VDC (tested for 1 minute)
	Connector	DSub female Connector, 9 pin
Diagnostic Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.
Display	LED Display	SYS System Status LED
		CAN CANopen Status (duo LED)
		The meaning of the LED CAN depends on the loaded firmware. Refer to chapter <i>LED Descriptions</i> , page 78.
Power supply	Supply Voltage	+3,3 V dc \pm 5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Compact PCI Bus
Environmental Conditions	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	162,2 x 100 x 20 mm
	Mounting/Installation	Compact PCI slot (3,3 V), refer to section <i>Slot for the PC</i> <i>Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated,
		radio-frequency, electromagnetic field test) EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast
		transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net

Table 92: Technical Data CIFX 80-CO

CIFX 80-DN	Parameter	Value
Part	Name	CIFX 80-DN
	Part No.	1280.510
	Description	PC Card cifX Compact PCI DeviceNet Master or Slave
	Function	Communication interface with Compact PCI and DeviceNet interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Compact PCI, according to [bus spec 4], refer to section <i>Overview</i> , page 108.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
DeviceNet Communication	Supported communication standard/ protocol (determined by the loaded firmware)	DeviceNet Master, DeviceNet Slave
DeviceNet Interface	Transmission rate	125 kBit/s, 250 kBit/s, 500 kBit/s
	Interface Type	ISO-11898 according to DeviceNet specification, refer to section <i>DeviceNet Interface</i> , page 103.
	Galvanic Isolation	optically isolated
	Isolation Voltage	1000 VDC (tested for 1 minute)
	Connector	CombiCon male Connector, 5 pin
Diagnostic Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.
Display	LED Display	SYS System Status LED
		MNS Module Network Status (duo LED)
		The meaning of the LED MNS depends on the loaded firmware. Refer to chapter <i>LED Descriptions</i> , page 78.
Power supply	Supply Voltage	+3,3 V dc \pm 5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Compact PCI Bus
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	162,2 x 100 x 20 mm
	Mounting/Installation	Compact PCI slot (3,3 V), refer to section <i>Slot for the PC</i> <i>Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

CIFX 80-DN	Parameter	Value
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 93: Technical Data CIFX 80-DN

10.1.5 CIFX 90-RE\F

CIFX 90-RE	Parameter	Value
Part	Name	CIFX 90-RE
	Part No.	1290.100
	Description	PC Card cifX Mini PCI for Real-Time Ethernet Master or Slave - with cable connector Ethernet for Ethernet assembly interface (AIFX-RE)
		Note: The device height and the power input of the PC Card cifX Mini PCI CIFX 90-RE\F do not comply with the standard specifications.
	Function	Communication interface with Mini PCI and Ethernet interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Mini PCI, according to [bus spec 5], refer to section <i>Overview</i> , page 108 and <i>Pin Assignment for Mini PCI Bus,</i> <i>X1</i> , page 109.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
Ethernet	Supported Real-Time Ethernet	EtherCAT Master, EtherCAT Slave
Communication	communication systems (determined by the loaded firmware)	EtherNet/IP Scanner (Master), EtherNet/IP Adapter (Slave),
		Open Modbus/TCP
		POWERLINK Controlled Node/Slave
		PROFINET IO-Controller (Master), PROFINET IO-Device (Slave)

CIFX 90-RE	Parameter	Value
		sercos Master, sercos Slave
		VARAN Client (Slave)
	Ethernet Frame Types	Ethernet II
Ethernet Interface	Transmission rate	100 MBit/s, 10 MBit/s (depending on loaded firmware)
	Interface Type	100 BASE-TX, 10 BASE-T (depending on loaded firmware), refer to section <i>Ethernet Interface</i> , page 100.
	Half duplex/Full duplex	depending on loaded firmware, supported (at 100 MBit/s)
	Auto-Negotiation	depending on loaded firmware
	Auto-Crossover	depending on loaded firmware
	Ethernet Assembly Interface	AIFX-RE, refer to section <i>AIFX-RE</i> , page 149. Important! Operating the PC card CIFX 90-RE\F requires proper connection of the Ethernet assembly interface (AIFX-RE) to the basic card!
	Connector AIFX-RE	Cable Connector Ethernet X4 (JST SM20B-SRSS-TB(LF)(SN), Pitch 1.0 mm)
	Channel 0 and 1	With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the EtherCAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For Open Modbus/TCP with V2.3.4.0 and higher both RJ45 channels can be used.
Display	LED Display	SYS System Status LED
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Mini PCI PCI Bus
Environmental	Operating temperature range*	0 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	0 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	60 x 45 x 9,5 mm
	Mounting/Installation	Mini PCI Socket (3.3 V), Type III System Connector, refer to section <i>Slot for the PC Cards cifX Compact PCI,</i> <i>Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test) EN 61000-6-2:2005 + B1:2011 (for industrial

CIFX 90-RE	Parameter	Value
		environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 94: Technical Data CIFX 90-RE\F

10.1.6 CIFX 90-DP\F

CIFX 90E-DP\F	Parameter	Value
Part	Name	CIFX 90-DP\F
	Part No.	1290.410
	Description	PC Card cifX Mini PCI PROFIBUS DP Master or Slave and PROFIBUS MPI Device - with cable connector fieldbus for PROFIBUS assembly interface (AIFX-DP)
		Note: The device height and the power input of the PC Card cifX Mini PCI CIFX 90-DP\F do not comply with the standard specifications.
	Function	Communication interface with Mini PCI-Socket and PROFIBUS interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Mini PCI, according to [bus spec 5], refer to section <i>Overview</i> , page 108 and <i>Pin Assignment for Mini PCI Bus,</i> <i>X1</i> , page 109.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
PROFIBUS Communication	Supported communication standard/ protocol (determined by the loaded firmware)	PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device
PROFIBUS Interface	Transmission rate	9,6 kBit/s, 19,2 kBit/s, 31,25 kBit/s, 45,45 kBit/s, 93,75 kBit/s, 187,5 kBit/s, 500 kBit/s, 1,5 MBit/s, 3 MBit/s, 6 MBit/s, 12 MBit/s
	Interface Type	RS 485, refer to section PROFIBUS Interface page 102.
	PROFIBUS Assembly Interface	AIFX-DP, refer to section AIFX-DP page 150.
		Important! Operating the PC card CIFX 90-DP\F requires proper connection of the PROFIBUS assembly interface (AIFX-DP) to the basic card!
	Connector AIFX-DP	Connector Fieldbus X3 (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)
Display	LED Display	SYS System Status LED
Power supply	Supply Voltage	+3,3 V dc \pm 5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Mini PCI Bus
Environmental	Operating temperature range*	-20 °C +70 °C

CIFX 90E-DP\F	Parameter	Value
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	60 x 45 x 9,5 mm
	Mounting/Installation	Mini PCI Socket (3.3 V), Type III System Connector, refer to section <i>Slot for the PC Cards cifX Compact PCI,</i> <i>Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 95: Technical Data CIFX 90-DP\F

10.1.7 CIFX 90-CO\F

CIFX 90-CO\F	Parameter	Value
Part	Name	CIFX 90-CO\F
	Part No.	1290.500
	Description	PC Card cifX Mini PCI CANopen Master or Slave - with cable connector fieldbus for CANopen assembly interface (AIFX-CO) Note: The device height and the power input of the PC Card cifX Mini PCI CIFX 90-CO\F do not comply with the standard specifications.
	Function	Communication interface with Mini PCI-Socket and CANopen interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Mini PCI, according to [bus spec 5], refer to section <i>Overview</i> , page 108 and <i>Pin Assignment for Mini PCI Bus,</i> <i>X1</i> , page 109.

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

CIFX 90-CO\F	Parameter	Value
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
CANopen Communication	Supported communication standard/ protocol (determined by the loaded firmware)	CANopen Master, CANopen Slave
CANopen Interface	Transmission rate	10 kBit/s, 20 kBit/s, 50 kBit/s, 100 kBit/s, 125 kBit/s, 250 kBit/s, 500 kBit/s, 800 kBit/s, 1 MBit/s
	Interface Type	ISO-11898, refer to section CANopen Interface, page 102.
	CANopen Assembly Interface	AIFX-CO, refer to section <i>AIFX-CO</i> , page 151. Important! Operating the PC card CIFX 90-CO\F requires proper connection of the CANopen assembly interface (AIFX-CO) to the basic card!
	Connector AIFX-CO	Connector Fieldbus X3 (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)
Display	LED Display	SYS System Status LED
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Mini PCI Bus
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	60 x 45 x 9,5 mm
	Mounting/Installation	Mini PCI Socket (3.3 V), Type III System Connector, refer to section Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104, page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test) EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net

CIFX 90-CO\F	Parameter	Value
	Configuration Software Slave	netX Configuration Tool

Table 96: Technical Data CIFX 90-CO\F

10.1.8 CIFX 90-DN\F

CIFX 90-DN\F	Parameter	Value
Part	Name	CIFX 90-DN\F
	Part No.	1290.510
	Description	PC Card cifX Mini PCI DeviceNet Master or Slave - with cable connector fieldbus for DeviceNet assembly interface (AIFX-DN)
		Note: The device height and the power input of the PC Card cifX Mini PCI CIFX 90-DN\F do not comply with the standard specifications.
	Function	Communication interface with Mini PCI-Socket and DeviceNet interface
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM
	FLASH	4 MB serial Flash EPROM
	Size of the Dual-Port Memory	64 KByte
System Interface	Bus Type	Mini PCI, according to [bus spec 5], refer to section <i>Overview</i> , page 108 and <i>Pin Assignment for Mini PCI Bus</i> , <i>X1</i> , page 109.
	Transmission Rate	33 MHz
	Data Access	DPM or DMA (Direct Memory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit
DeviceNet Communication	Supported communication standard/ protocol (determined by the loaded firmware)	DeviceNet Master, DeviceNet Slave
DeviceNet Interface	Transmission rate	125 kBit/s, 250 kBit/s, 500 kBit/s
	Interface Type	ISO-11898 according to DeviceNet specification, refer to section <i>DeviceNet Interface</i> , page 103
	DeviceNet Assembly Interface	AIFX-DN, refer to section AIFX-DN, page 152.
		Important! Operating the PC card CIFX 90-DN\F requires proper connection of the DeviceNet assembly interface (AIFX-DN) to the basic card!
	Connector AIFX-DN	Connector Fieldbus X3 (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)
Display	LED Display	SYS System Status LED
Power supply	Supply Voltage	+3,3 V dc \pm 5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at 3,3 V (typically)	650 mA
	Connector	Via Mini PCI Bus
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.

CIFX 90-DN\F	Parameter	Value
Device	Dimensions (L x W x H)	60 x 45 x 9,5 mm
	Mounting/Installation	Mini PCI Socket (3.3 V), Type III System Connector, refer to section Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104, page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 97: Technical Data CIFX 90-DN\F

10.1.9 CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\F, CIFX

CIFX 90E-RE\F, CIFX 90E-RE\ET\F CIFX 90E-RE\MR\F CIFX 90E-RE\MR\ET\F	Parameter	Value	
Part	Name	CIFX 90E-RE\F	CIFX 90E-RE\ET\F
	Part No.	1291.100	1291.104
	Name	CIFX 90E-RE\MR\F	CIFX 90E-RE\MR\ET\F
	Part No.	1291.102	1291.106
	Description	PC Card cifX Mini PCI Express for Real-Time Ethernet Master or Slave - with cable connector Ethernet for Ethernet assembly interface (AIFX-RE) Note: The device height and the power input of the PC Card cifX Mini PCI Express CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E- RE\MR\F and CIFX 90E-RE\MR\ET\F do not comply with the standard specifications.	
	Function	Communication Interface with Mini PCI Express and Ethernet interface	
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
	MRAM (only CIFX 90E-RE\MR\F	128Kbyte (= 64K Words); Note: Using the cifX Device I	Driver (from Version 1.1.1.0)

CIFX 90E-RE\F, CIFX 90E-RE\ET\F CIFX 90E-RE\MR\F CIFX 90E-RE\MR\ET\F	Parameter	Value	
	and CIFX 90E-RE\MR\ET\F)	access to this memory is pos remanent memory.	sible and it can be used as a
System Interface	Bus Type	Mini PCI Express, One Lane [bus spec 2], refer to section Assignment for Mini PCI Exp (Bootstart), X1/X2, page 111	Overview, page 108 and Pin ress Bus / SYNC Connector
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA* (Direct Memore *CIFX 90E-RE\F from Hardware CIFX 90E-RE\MR\F from Hardware CIFX 90E-RE\ET\F and CIFX 90 Revision 1	Revision A, are Revision B,
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
Ethernet	Supported Real-Time Ethernet	EtherCAT Master, EtherCAT	Slave
Communication	communication systems (determined by the loaded firmware)	EtherNet/IP Scanner (Master EtherNet/IP Adapter (Slave),	
	iiiiiwale)	Open Modbus/TCP	
		POWERLINK Controlled Noc	le/Slave
		PROFINET IO-Controller (Master), PROFINET IO-Device (Slave)	
		sercos Master, sercos Slave	
		VARAN Client (Slave)	
	Ethernet Frame Types	Ethernet II	
Ethernet Interface	Transmission Rate	100 MBit/s, 10 MBit/s (depending on loaded firmware)	
	Interface Type	100 BASE-TX, 10 BASE-T (depending on lo refer to section <i>Ethernet Inte</i>	
	Half duplex/Full duplex	depending on loaded firmwar	re, supported (at 100 MBit/s)
	Auto-Negotiation	depending on loaded firmwar	re
	Auto-Crossover	depending on loaded firmwar	re
	Ethernet Assembly Interface	AIFX-RE, refer to section AIF Important! Operating the PC ca 'MR' or 'ET') requires proper con interface (AIFX-RE) to the basic	rd CIFX 90E-RE\F (all variants nection of the Ethernet assembly
	Connector AIFX-RE	Cable Connector Ethernet X4 (JST BM20B-SRDS-G-TFC, Pitch 1,0 mm)	
	Channel 0 and 1	0 can be used, channel 1 is de EtherCAT Master firmware ver	sion 3 channel 1 can be tivated. For Open Modbus/TCP
Display	LED Display	SYS System Statu	is LED
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Power Supply and Host</i> <i>Interface</i> , page 38.	
	Maximum Current at at 3,3 V (typically)	800 mA	
	Connector	Via Mini PCI Express Bus	
Environmental Conditions		CIFX 90E-RE\F, CIFX 90E-RE\MR\F	CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\ET\F
	Operating temperature range*	0 °C +55 °C	0 °C +70 °C
	*Air flow during measurment	0,5m/s	

	Deveryoter	Value	
CIFX 90E-RE\F, CIFX 90E-RE\ET\F CIFX 90E-RE\MR\F CIFX 90E-RE\MR\ET\F	Parameter	Value	
	Storage temperature range	0 °C +70 °C	
	Humidity	10 95% relative humidity, I	no condensation permitted
	Environment	For UL compliant usage: The device must be used in a environment.	pollution degree 2
Device	Dimensions (L x W x H)	CIFX 90E-RE\F ^{1, 2} , CIFX 90E-RE\MR\F ²	CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\ET\F
		51 x 30,2 +/- 0,1 x 11 mm	51 x 30,2 +/- 0,1 x 12,5 mm
		² Beginning with hardware rev - 0,1 mm x 11 mm; [W = 30,1 circuit board (PCB) has beco 0.1 mm 0.3 mm, thus devis	mm 30,3 mm: The printed me wider on the right side by ating from the norm].
		¹ For hardware revision 9: 51 mm: The printed circuit board the right side by 0.2 mm, thus	(PCB) has become wider on
		For further details on the eleli Notes on the Card Height - P Mini PCIe on page 36.	
	Mounting/Installation	PCI Express Mini System Co X1/X2 ⁸ = One Lane, refer to s <i>cifX Compact PCI, Mini PCI</i> ,	section Slot for the PC Cards
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission	EN 55011:2009 + A1:2010, C (Radio disturbance character measurement)	
	Immunity	EN 61000-4-2:2009 (Electros	tatic discharge test)
		EN 61000-4-3:2006 + A1:200 radio-frequency, electromagn	
		EN 61000-4-4:2004 + A1:201 transients/burst test)	0 (Burst Electrical fast
		EN 61000-4-5:2006 (Surge te	
		EN 61000-4-6:2009 (to condu by radio- frequency fields)	ucted disturbances, induced
		EN 61000-4-8:2010 (power fr	equency magnetic field test)
		EN 61000-6-2:2005 + B1:201 environments)	1 (for industrial
UL Certification	The device CIFX 90E-RE\F is certified according to UL 508	UL-File-Nr. E221530	
Configuration	Configuration Software Master and Slave	SYCON.net	
	Configuration Software Slave	netX Configuration Tool	

Table 98: Technical Data CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F

 $^{^{8}}$ X1, X2 corresponds to the Hilscher convention for "interface" on the top or the bottom side of the PC Card cifX.

10.1.10 CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\F, CIFX

CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F	Parameter	Value	
Part	Name	CIFX 90E-DP\F	CIFX 90E-DP\ET\F
	Part No.	1291.410	1291.414
	Name	CIFX 90E-DP\MR\F	CIFX 90E-DP\MR\ET\F
	Part No.	1291.112	1291.116
	Description	PC Card cifX Mini PCI Express Slave and PROFIBUS MPI D fieldbus for PROFIBUS asser Note: The device height and the Mini PCI Express CIFX 90E-DP\/ DP\MR\F and CIFX 90E-DP\MR\ standard specifications.	evice - with cable connector mbly interface (AIFX-DP) power input of the PC Card cifX F, CIFX 90E-DP\ET\F, CIFX 90E-
	Function	Communication Interface with PROFIBUS interface	n Mini PCI Express and
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
	MRAM (only CIFX 90E-DP\MR\F and CIFX 90E-DP\MR\ET\F)	128Kbyte (= 64K Words); Note: Using the cifX Device I access to this memory is pos remanent memory.	
System Interface	Bus Type	Mini PCI Express One Lane Port, according to [bus spec 2] and [bus spec 6], refer to section Overview, page 108 and Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bootstart), X1/X2, page 111.	
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA* (Direct Memor *CIFX 90E-DP\F from Hardware CIFX 90E-DP\MR\F from Hardwa CIFX 90E-DP\ET\F and CIFX 90 Revision 1	Revision A, are Revision B,
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
PROFIBUS Communication	Supported communication standard/ protocol (determined by the loaded firmware)	PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device	
PROFIBUS interface	Transmission Rate	9,6 kBit/s, 19,2 kBit/s, 31,25 kBit/s, 45,45 kBit/s, 93,75 kBit/s, 187,5 kBit/s, 500 kBit/s, 1,5 MBit/s, 3 MBit/s, 6 MBit/s, 12 MBit/s	
	Interface Type	RS 485, refer to section PRC	FIBUS Interface page 102.
	PROFIBUS Assembly Interface	AIFX-DP, refer to section AIF Important! Operating the PC can 'MR' or 'ET') requires proper con assembly interface (AIFX-DP) to	rd CIFX 90E-DP\F (all variants nection of the PROFIBUS
	Connector AIFX-DP	Cable Connector Fieldbus X3 (JST 10FMN-BMT-A-TF(LF)(
Display	LED Display	SYS System Statu	IS LED
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to secti Interface, page 38.	on Power Supply and Host
	Maximum Current at at 3,3 V (typically)	600 mA	

CIFX 90E-DP\F,	Parameter	Value	
CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F			
	Connector	Via Mini PCI Express Bus	
Environmental Conditions		CIFX 90E-DP\F, CIFX 90E-DP\MR\F	CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\ET\F
	Operating temperature range*	0 °C +55 °C	-20 °C +70 °C
	Storage temperature range	0 °C +70 °C	-10 °C +70 °C
	*Air flow during measurment	0,5m/s	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The pollution degree 2 environme	
Device	Dimensions (L x W x H)	CIFX 90E-DP\F ^{1, 2} , CIFX 90E-DP\MR\F ²	CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\ET\F
		51 x 30,2 +/- 0,1 x 11 mm	51 x 30,2 +/- 0,1 x 12,5 mm
		² Beginning with hardware rev - 0,1 mm x 11 mm; [W = 30,1 circuit board (PCB) has beco 0.1 mm 0.3 mm, thus devi	mm 30,3 mm: The printed me wider on the right side by
		¹ For hardware revision 9: 51 mm: The printed circuit board the right side by 0.2 mm, thus	I (PCB) has become wider on
		For further details on the elelment hights refer to section Notes on the Card Height - PC Card cifX Mini PCI and Mini PCIe on page 36.	
	Mounting/Installation	PCI Express Mini System Connector (3,3 V), X1/X2 ⁹ = One Lane, refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission	EN 55011:2009 + A1:2010, C (Radio disturbance character measurement)	CISPR 11:2009, Class A istics - Limits and methods of
	Immunity	EN 61000-4-2:2009 (Electros	tatic discharge test)
		EN 61000-4-3:2006 + A1:200 radio-frequency, electromagn	
		EN 61000-4-4:2004 + A1:201 transients/burst test)	0 (Burst Electrical fast
		EN 61000-4-5:2006 (Surge te	est)
		EN 61000-4-6:2009 (to condu by radio- frequency fields)	ucted disturbances, induced
		EN 61000-4-8:2010 (power fr	equency magnetic field test)
		EN 61000-6-2:2005 + B1:201 environments)	1 (for industrial
UL Certification	The device CIFX 90E-DP\F is certified according to UL 508	UL-File-Nr. E221530	
Configuration	Configuration Software Master and Slave	SYCON.net	
	Configuration Software Slave	netX Configuration Tool	

Table 99: Technical Data CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\F, CIFX

 $^{^{9}}$ X1, X2 corresponds to the Hilscher convention for "interface" on the top or the bottom side of the PC Card cifX.

10.1.11 CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\F, CIFX

CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F	Parameter	Value	
Part	Name	CIFX 90E-CO\F	CIFX 90E-CO\ET\F
	Part No.	1291.500	1291.504
	Name	CIFX 90E-CO\MR\F	CIFX 90E-CO\MR\ET\F
	Part No.	1291.502	1291.506
	Description	PC Card cifX Mini PCI Expre - with cable connector fieldbu interface (AIFX-CO)	ss CANopen Master or Slave is for CANopen assembly
		Mini PCI Express CIFX 90E-CO	power input of the PC Card cifX F, CIFX 90E-CO\ET\F, CIFX D\MR\ET\F do not comply with the
	Function	Communication Interface with CANopen interface	h Mini PCI Express and
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
	MRAM (only CIFX 90E-CO\MR\F and CIFX 90E-CO\MR\ET\F)	128Kbyte (= 64K Words); Note: Using the cifX Device I access to this memory is pos remanent memory.	Driver (from Version 1.1.1.0) sible and it can be used as a
System Interface	Bus Type	Mini PCI Express One Lane I spec 2] and [bus spec 6], refe 108 and Pin Assignment for I Connector (Bootstart), X1/X2	er to section Overview, page Mini PCI Express Bus / SYNC
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA* (Direct Memor *CIFX 90E-CO\F from Hardware CIFX 90E-CO\MR\F from Hardw CIFX 90E-CO\ET\F and CIFX 90 Revision 1	Revision Á, are Revision B,
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
CANopen Communication	Supported communication standard/ protocol (determined by the loaded firmware)	CANopen Master, CANopen Slave	
CANopen interface	Transmission Rate	10 kBit/s, 20 kBit/s, 50 kBit/s, 250 kBit/s, 500 kBit/s, 800 kB	
	Interface Type	ISO-11898, refer to section C	CANopen Interface, page 102.
	CANopen Assembly Interface	AIFX-CO, refer to section All	FX-CO, page 151.
		Important! Operating the PC ca 'MR' or 'ET') requires proper con assembly interface (AIFX-CO) to	rd CIFX 90E-CO\F (all variants nection of the CANopen
	Connector AIFX-CO	Cable Connector Fieldbus X3 (JST 10FMN-BMT-A-TF(LF)(
Display	LED Display	SYS System Statu	is LED

CIFX 90E-CO\F,	Parameter	Value	
CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F			
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Interface</i> , page 38.	ion Power Supply and Host
	Maximum Current at at 3,3 V (typically)	600 mA	
	Connector	Via Mini PCI Express Bus	
Environmental Conditions		CIFX 90E-CO\F, CIFX 90E-CO\MR\F	CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\ET\F
	Operating temperature range*	0 °C +55 °C	-20 °C +70 °C
	Storage temperature range	0 °C +70 °C	-10 °C +70 °C
	*Air flow during measurment	0,5m/s	
	Humidity	10 95% relative humidity,	no condensation permitted
	Environment	For UL compliant usage: The device must be used in a environment.	a pollution degree 2
Device	Dimensions (L x W x H)	CIFX 90E-CO\F ^{1, 2} , CIFX 90E-CO\MR\F ²	CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\ET\F
		51 x 30,2 +/- 0,1 x 11 mm	51 x 30,2 +/- 0,1 x 12,5 mm
		- 0,1 mm x 11 mm; [W = 30,1	vision A: $51 \times 30,2 \text{ mm} + 0,1 / \text{mm} \dots 30,3 \text{ mm}$: The printed me wider on the right side by ating from the norm].
		¹ For hardware revision 9: 51 x 30,2 x 11 mm; [W = $30,2$ mm: The printed circuit board (PCB) has become wider the right side by 0.2 mm, thus deviating from the norm].	
		For further details on the elel Notes on the Card Height - F Mini PCIe on page 36.	
	Mounting/Installation	PCI Express Mini System Connector (3,3 V), X1/X2 ¹⁰ = One Lane, refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods measurement)	
	Immunity	EN 61000-4-2:2009 (Electros	static discharge test)
		EN 61000-4-3:2006 + A1:200 radio-frequency, electromagr	
		EN 61000-4-4:2004 + A1:201 transients/burst test)	10 (Burst Electrical fast
		EN 61000-4-5:2006 (Surge te	est)
		EN 61000-4-6:2009 (to conde by radio- frequency fields)	ucted disturbances, induced
		EN 61000-4-8:2010 (power fi	requency magnetic field test)
		EN 61000-6-2:2005 + B1:201 environments)	11 (for industrial

 $^{^{10}}$ X1, X2 corresponds to the Hilscher convention for "interface" on the top or the bottom side of the PC Card cifX.

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F	Parameter	Value
UL Certification	The device CIFX 90E-CO\F is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 100: Technical Data CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F

10.1.12 CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F

CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F	Parameter	Value	
Part	Name	CIFX 90E-DN\F	CIFX 90E-DN\ET\F
	Part No.	1291.510	1291.514
	Name	CIFX 90E-DN\MR\F	CIFX 90E-DN\MR\ET\F
	Part No.	1291.512	1291.516
	Description	PC Card cifX Mini PCI Expres - with cable connector fieldbu interface (AIFX-DN),	ss DeviceNet Master or Slave s for DeviceNet assembly
		Note: The deviceheight and the Mini PCI Express CIFX 90E-DN\ DN\MR\F and CIFX 90E-DN\MR standard specifications.	F, CIFX 90E-DN\ET\F, CIFX 90E-
	Function	Communication Interface with DeviceNet interface	n Mini PCI Express and
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
	MRAM (only CIFX 90E-DN\MR\F and CIFX 90E-DN\MR\ET\F)	128Kbyte (= 64K Words); Note: Using the cifX Device I access to this memory is pos remanent memory.	
System Interface	Bus Type	Mini PCI Express One Lane F spec 2] and [bus spec 6], refe 108 and <i>Pin Assignment for I</i> <i>Connector (Bootstart), X1/X</i> 2	er to section Overview, page Mini PCI Express Bus / SYNC
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA* (Direct Memor *CIFX 90E-DN\F from Hardware CIFX 90E-DN\MR\F from Hardwa CIFX 90E-DN\ET\F and CIFX 90 Revision 1	Revision Á, are Revision B,
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
DeviceNet Communication	Supported communication standard/ protocol (determined by the loaded firmware)	DeviceNet Master, DeviceNet Slave	

CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F	Parameter	Value	
DeviceNet interface	Transmission Rate	125 kBit/s, 250 kBit/s, 500 kB	Bit/s
	Interface Type	ISO-11898 according to Devi refer to section <i>DeviceNet In</i>	
	DeviceNet Assembly Interface	AIFX-DN, refer to section AIF	<i>-X-DN,</i> page 152.
		Important! Operating the PC ca 'MR' or 'ET') requires proper con assembly interface (AIFX-DN) to	nection of the DeviceNet
	Connector AIFX-DN	Cable Connector Fieldbus X3 (JST 10FMN-BMT-A-TF(LF)(
Display	LED Display	SYS System Statu	is LED
Power supply	Supply Voltage	+3,3 V dc ±5 %, refer to section <i>Interface</i> , page 38.	ion Power Supply and Host
	Maximum Current at at 3,3 V (typically)	600 mA	
	Connector	Via Mini PCI Express Bus	
Environmental Conditions		CIFX 90E-DN\F, CIFX 90E-DN\MR\F	CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\ET\F
	Operating temperature range*	0 °C +55 °C	-20 °C +70 °C
	Storage temperature range	0 °C +70 °C	-10 °C +70 °C
-	*Air flow during measurment	0,5m/s	
	Humidity	10 95% relative humidity,	no condensation permitted
	Environment	For UL compliant usage: The device must be used in a environment.	a pollution degree 2
Device	Dimensions (L x W x H)	CIFX 90E-DN\F ^{1, 2} , CIFX 90E-DN\MR\F ²	CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\ET\F
		51 x 30,2 +/- 0,1 x 11 mm	51 x 30,2 +/- 0,1 x 12,5 mm
		- 0,1 mm x 11 mm; [W = 30,1	vision A: 51 x 30,2 mm + 0,1 / mm 30,3 mm: The printed me wider on the right side by ating from the norm].
		¹ For hardware revision 9: 51 mm: The printed circuit board the right side by 0.2 mm, thus	d (PCB) has become wider on
		For further details on the elel Notes on the Card Height - F Mini PCIe on page 36.	ment hights refer to section C Card cifX Mini PCI and
	Mounting/Installation		nnector (3,3 V), section <i>Slot for the PC Cards</i> <i>Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes	

¹¹ X1, X2 corresponds to the Hilscher convention for "interface" on the top or the bottom side of the PC Card cifX.

CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F	Parameter	Value
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device CIFX 90E-DN\F is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 101: Technical Data CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F

10.1.13 CIFX 104C-RE, CIFX 104C-RE-R

		Value	
Part	Name	CIFX 104C-RE	CIFX 104C-RE-R
	Part No.	1270.100	1271.100
	Description	PC Card cifX PCI-104 for Real-Time Ethernet Master or Slave; (for CIFX 104C-RE-R connectors at the left side)	
	Function	Communication Interface with PCI-104 and Ethernet interface	
Communication Controller	Туре	netX 500 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bi Overview, page 108.	us spec 7], refer to section
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Mer	mory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
Ethernet	Supported Real-Time Ethernet	EtherCAT Master, Ether	CAT Slave
Communication	communication systems (determined by the loaded firmware)	EtherNet/IP Scanner (Master), EtherNet/IP Adapter (Slave),	
		Open Modbus/TCP	
		POWERLINK Controlled	Node/Slave
		PROFINET IO-Controller PROFINET IO-Device (S	
		sercos Master, sercos SI	ave
		VARAN Client (Slave)	
	Ethernet Frame Types	Ethernet II	
Ethernet interface	Transmission Rate	100 MBit/s, 10 MBit/s (depending on loaded firmware)	
	Interface Type	100 BASE-TX, 10 BASE-T (depending on loaded firmware), refer to section <i>Ethernet Interface</i> , page 100.	
	Galvanic Isolation	isolated	
	Isolation Voltage	1000 VDC (tested for 1 n	ninute)
	Half duplex/Full duplex	depending on loaded firm supported (at 100 MBit/s	
	Auto-Negotiation	depending on loaded firm	nware
	Auto-Crossover	depending on loaded firm	nware
	Connector	2* RJ45 Socket	
	Channel 0 and 1	channel 0 can be used, of Beginning with the Ether channel 1 can be reactive	CAT Master firmware version 3 ated if redundancy is activated. with V2.3.4.0 and higher both
Diagnosis Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.	

CIFX 104C-RE, CIFX 104C-RE-R	Parameter	Value	
Display	LED Display	SYS System Status LED	
		The meaning of firmware:	of the following LEDs depends on the loaded
		COM 0	LED Communication Status 0 (duo LED)
		COM 1	LED Communication Status 1 (duo LED)
		LED yellow	at RJ45Ch0 and RJ45Ch1,
		LED green	for Ethernet Link status, Ethernet Aktivity status and additional status
		Refer to chapt	er LED Descriptions, page 78.
Power supply	Supply Voltage		or +3,3 V dc ±5 %, refer to section <i>Power</i> ost Interface, page 38.
	Maximum Current at at 5 V (typically) or	500 mA	
	at 3,3 V (typically)	730 mA	
	Connector	Via PCI-104 B	us
Operation	Rotary Switch PCI-104 Slot Number		-104 Slot Number, refer to section <i>Rotary</i> -104 Slot Number, page 104.
	Rotary Switch Device Address		assigned. Refer to section <i>Rotary Switch</i> so on page 104.
Environmental	Operating temperature range*	0 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	0 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	97 x 91 x 24 mm	
	Mounting/Installation	Signaling Volta	Supply Voltage 5 V or 3.3 V, age 5 V or 3.3 V), refer to section Slot for the Compact PCI, Mini PCI, Mini PCIe, PCI-
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)	
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)	
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)	
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)	
			2006 (Surge test)
		by radio- frequ	
			2010 (power frequency magnetic field test)
		environments)	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	
Configuration	Configuration Software Master and Slave	SYCON.net	

CIFX 104C-RE, CIFX 104C-RE-R	Parameter	Value
	Configuration Software Slave	netX Configuration Tool

Table 102: Technical Data CIFX 104C-RE, CIFX 104C-RE-R

10.1.14 CIFX 104C-RE\F, CIFX 104C-RE-R\F

CIFX 104C-RE\F, CIFX 104C-RE-R\F	Parameter	Value	
Part	Name	CIFX 104C-RE\F	CIFX 104C-RE-R\F
	Part No.	1270.101	1271.101
	Description	PC Card cifX PCI-104 for Real-Time Ethernet Master of Slave - with cable connector Ethernet for Ethernet assembly interface (AIFX-RE) - and cable connector DIAG for diagnostic assembly interface (AIFX-DIAG); (for CIFX 104C-RE-R\F connectors at the left side) Communication Interface with PCI-104 and Ethernet interface	
	Function		
Communication Controller	Туре	netX 500 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus spec 7], refer to section Overview, page 108.	
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Memory Access)	
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
Ethernet	Supported Real-Time Ethernet	EtherCAT Master, EtherCAT Slave	
Communication	communication systems (determined by the loaded firmware)	EtherNet/IP Scanner (Master), EtherNet/IP Adapter (Slave),	
		Open Modbus/TCP	
		POWERLINK Controlled Node/Slave	
		PROFINET IO-Controller (Master), PROFINET IO-Device (Slave)	
		sercos Master, sercos Slave	
		VARAN Client (Slave)	
	Ethernet Frame Types	Ethernet II	
Ethernet interface	Transmission Rate	100 MBit/s, 10 MBit/s (depending on loaded firmware)	
	Interface Type	100 BASE-TX, 10 BASE-T (depending on lo refer to section <i>Ethernet Inte</i>	
	Half duplex/Full duplex	depending on loaded firmware, supported (at 100 MBit/s)	
	Auto-Negotiation	depending on loaded firmware	
	Auto-Crossover	depending on loaded firmware	
	Ethernet Assembly Interface	AIFX-RE, refer to section All	FX-RE, page 149.
		Important! Operating the PC card CIFX 104C-RE\F or CIFX 104C-RE-R\F requires proper connection of the Ethernet assembly interface (AIFX-RE) to the basic card!	
	Connector AIFX-RE	Cable Connector Ethernet X	304 (X4)

CIFX 104C-RE\F, CIFX 104C-RE-R\F	Parameter	Value	
		(JST SM20B-SRSS-TB(LF)(SN), Pitch 1,0 mm)	
	Channel 0 and 1	With loaded EtherCAT Master firmware only the RJ45 channel 0 can be used, channel 1 is deactivated. Beginning with the EtherCAT Master firmware version 3 channel 1 can be reactivated if redundancy is activated. For Open Modbus/TCP with V2.3.4.0 and higher both RJ45 channels can be used.	
Diagnosis Interface	Diagnostic Assembly Interface	AIFX-DIAG, refer to section AIFX-DIAG, Page 153.	
		Note: If the diagnostic AIFX-DIAG assembly interface is connected to the PC card CIFX 104C-RE\F or CIFX 104C-RE-R\F, the Mini-B USB connector on the AIFX-DIAG can be used beginning with the hardware revision 5 of the PC card cifX.	
	Connector AIFX-DIAG	Cable Connector DIAG X303 (X3) (JST 12FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
Display	LED Display	SYS System Status LED	
Power supply	Supply Voltage	+5 V dc \pm 5 % or +3,3 V dc \pm 5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.	
	Maximum Current at at 5 V (typically) or	500 mA	
	at 3,3 V (typically)	730 mA	
	Connector	Via PCI-104 Bus	
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Number, refer to section <i>Rotary Switch for PCI-104 Slot Number</i> , page 104.	
Environmental	Operating temperature range*	0 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	0 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	97 x 91 x 24 mm	
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Volt- age 5 V or 3.3 V), refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)	
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)	
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)	
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)	
		EN 61000-4-5:2006 (Surge test)	
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)	
		EN 61000-4-8:2010 (power frequency magnetic field test)	
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	

CIFX 104C-RE\F, CIFX 104C-RE-R\F	Parameter	Value
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 103: Technical Data CIFX 104C-RE\F, CIFX 104C-RE-R\F

10.1.15 CIFX 104C-DP, CIFX 104C-DP-R

CIFX 104C-DP, CIFX 104C-DP-R	Parameter	Value	
Part	Name	CIFX 104C-DP	CIFX 104C-DP-R
	Part No.	1270.410 1271.410	
	Description	PC Card cifX PCI-104 PROFIBUS DP Master or Slave a PROFIBUS MPI Device; (for CIFX 104C-DP-R connectors at the left side)	
	Function	Communication Interface with PCI-104 and fieldbus interface PROFIBUS	
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus spectrum] <i>Overview</i> , page 108.	pec 7], refer to section
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Memory Access)	
Width for the data access to the Dual-Port Memory (DPM)		32-Bit	
PROFIBUS Communication	Supported communication standard/ protocol (determined by the loaded firmware)	PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device	
PROFIBUS Interface	Transmission Rate	9,6 kBit/s, 19,2 kBit/s, 31,25 kBit/s, 45,45 kBit/s, 93,75 kBit/s, 187,5 kBit/s, 500 kBit/s, 1,5 MBit/s, 3 MBit/s, 6 MBit/s, 12 MBit/s	
	Interface Type	RS 485, refer to section PROFIBUS Interface, page 102.	
	Galvanic Isolation	isolated	
	Isolation Voltage	1000 VDC (tested for 1 minute)	
	Connector	DSub female Connector, 9 pi	in
Diagnosis Interface	USB Interface	Mini B USB Plug (5 pin), refe Connector (5 Pin), page 103.	
Display	LED Display	SYS System Statu	us LED
		COM 0 LED Communication Status 0 (duo LE	
		The meaning of the COM LED depends on the loaded firmware. Refer to chapter <i>LED Descriptions</i> , page 78.	
Power supply	Supply Voltage	+5 V dc ±5 % or +3,3 V dc ±5 %, refer to section Powe Supply and Host Interface, page 38.	
	Maximum Current at at 5 V (typically)or	500 mA	
	at 3,3 V (typically)	650 mA	
	Connector	Via PCI-104 Bus	
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Number, refer to section <i>Rotary Switch for PCI-104 Slot Number</i> , page 104.	

PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104 | Installation, Operation and Hardware Description DOC120205UM39EN | Revision 39 | English | 2013-12 | Released | Public © Hilscher, 2008-2013

CIFX 104C-DP, CIFX 104C-DP-R	Parameter	Value
	Rotary Switch Device Address	Is currently unassigned. Refer to section <i>Rotary Switch Device Address</i> on page 104.
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	0 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	97 x 91 x 24 mm
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Vol- tage 5 V or 3.3 V), refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 104: Technical Data CIFX 104C-DP, CIFX 104C-DP-R

10.1.16 CIFX 104C-DP\F, CIFX 104C-DP-R\F

CIFX 104C-DP\F, CIFX 104C-DP-R\F	Parameter	Wert
Part	Name	CIFX 104C-DP\F CIFX 104C-DP-R\F
	Part No.	1270.411 1271.411
	Description	PC Card cifX PCI-104 PROFIBUS DP Master or Slave and PROFIBUS MPI Device - with cable connector fieldbus for PROFIBUS assembly interface (AIFX-DP) - and cable connector DIAG for diagnostic assembly interface (AIFX- DIAG); (for CIFX 104C-DP-R\F connectors at the left side)
	Function	Communication Interface with PCI-104 and fieldbus interface PROFIBUS
Communication Controller	Туре	netX 100 processor
Integrated Memory	RAM	8 MB SDRAM

CIFX 104C-DP\F, CIFX 104C-DP-R\F	Parameter	Wert	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus spec 7], refer to section <i>Overview</i> , page 108.	
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Memory Access)	
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
PROFIBUS Communication	Supported communication standard/ protocol (determined by the loaded firmware)	PROFIBUS DP Master, PROFIBUS DP Slave, PROFIBUS MPI Device	
PROFIBUS Interface	Transmission Rate	9,6 kBit/s, 19,2 kBit/s, 31,25 kBit/s, 45,45 kBit/s, 93,75 kBit/s, 187,5 kBit/s, 500 kBit/s, 1,5 MBit/s, 3 MBit/s, 6 MBit/s, 12 MBit/s	
	Interface Type	RS 485, refer to section PROFIBUS Interface page 102.	
	PROFIBUS Assembly Interface	AIFX-DP, refer to section AIFX-DP page 150.	
		Important! Operating the PC card CIFX 104C-DP\F bzw. CIFX 104C-DP-R\F requires proper connection of the PROFIBUS assembly interface (AIFX-DP) to the basic card!	
	Connector AIFX-DP	Cable Connector Fieldbus X304 (X4) (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
Diagnosis Interface	Diagnostic Assembly Interface	AIFX-DIAG, refer to section AIFX-DIAG, Page 153.	
	Connector AIFX-DIAG	Cable Connector DIAG X303 (X3) (JST 12FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
Display	LED Display	SYS System Status LED	
Power supply	Supply Voltage	+5 V dc \pm 5 % or +3,3 V dc \pm 5 %, refer to section <i>Power</i> Supply and Host Interface, page 38.	
	Maximum Current at at 5 V (typically) or	500 mA	
	at 3,3 V (typically)	650 mA	
	Connector	Via PCI-104 Bus	
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Number, refer to section <i>Rotary Switch for PCI-104 Slot Number</i> , page 104.	
Environmental Conditions	Operating temperature range*	-20 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	-10 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	97 x 91 x 24 mm	
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Vol- tage 5 V or 3.3 V), refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)	
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test) EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated,	

CIFX 104C-DP\F, CIFX 104C-DP-R\F	Parameter	Wert	
		radio-frequency, electromagnetic field test)	
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)	
		EN 61000-4-5:2006 (Surge test)	
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)	
		EN 61000-4-8:2010 (power frequency magnetic field test)	
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	
Configuration	Configuration Software Master and Slave	SYCON.net	
	Configuration Software Slave	netX Configuration Tool	

Table 105: Technical Data CIFX 104C-DP\F, CIFX 104C-DP-R\F

10.1.17 CIFX 104C-CO, CIFX 104C-CO-R

CIFX 104C-CO, CIFX 104C-CO-R	Parameter	Value	
Part	Name	CIFX 104C-CO	CIFX 104C-CO-R
	Part No.	1270.500	1271.500
	Description	PC Card cifX PCI-104 CANopen Master or Slave; (for CIFX 104C-CO-R connectors at the left side)	
	Function	Communication Interface with PCI-104 and fieldbus interface CANopen	
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus spec 7], refer to section <i>Overview</i> , page 108.	
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Memory Access)	
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
CANopen Communication	Supported communication standard/ protocol (determined by the loaded firmware)	CANopen Master, CANopen Slave	
CANopen Interface	Transmission Rate	10 kBit/s, 20 kBit/s, 50 kBit/s 250 kBit/s, 500 kBit/s, 800 k	
	Interface Type	ISO-11898, refer to section CANopen Interface, page 10	
	Galvanic Isolation	isolated (optically isolated)	
	Isolation Voltage	1000 VDC (tested for 1 minu	ute)
	Connector	DSub female Connector, 9 pin	
Diagnosis Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB</i> <i>Connector (5 Pin)</i> , page 103.	
Display	LED Display	SYS System Stat	us LED
		CAN CANopen S	tatus (duo LED)
		The meaning of the CAN LE	D is depending on loaded

CIFX 104C-CO, CIFX 104C-CO-R	Parameter	Value
		firmware. Refer to chapter LED Descriptions, page 78.
Power supply	Supply Voltage	+5 V dc \pm 5 % or +3,3 V dc \pm 5 %, refer to section <i>Power Supply and Host Interface</i> , page 38.
	Maximum Current at at 5 V (typically)	500 mA
	or at 3,3 V (typically)	650 mA
	Connector	Via PCI-104 Bus
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Number, refer to section <i>Rotary Switch for PCI-104 Slot Number</i> , page 104.
	Rotary Switch Device Address	Is currently unassigned. Refer to section <i>Rotary Switch Device Address</i> on page 104.
Environmental Conditions	Operating temperature range*	-20 °C +70 °C
	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	97 x 91 x 24 mm
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Vol- tage 5 V or 3.3 V), refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 106: Technical Data CIFX 104C-CO, CIFX 104C-CO-R

10.1.18 CIFX 104C-CO\F, CIFX 104C-CO-R\F

CIFX 104C-CO\F, CIFX 104C-CO-R\F	Parameter	Value	
Part	Name	CIFX 104C-CO\F	CIFX 104C-CO-R\F
	Part No.	1270.501	1271.501
	Description	PC Card cifX PCI-104 CANopen Master or Slave - with cable connector fieldbus for CANopen assembly interface (AIFX-CO) - and cable connector DIAG for diagnostic assembly interface (AIFX-DIAG); (for CIFX 104C-CO-R\F connectors at the left side)	
	Function	Communication Interface with PCI-104 and fieldbus interface CANopen	
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus <i>Overview</i> , page 108.	s spec 7], refer to section
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Mem	ory Access)
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
CANopen Communication	Supported communication standard/ protocol (determined by the loaded firmware)	CANopen Master, CANopen Slave	
CANopen Interface	Transmission Rate	10 kBit/s, 20 kBit/s, 50 kBit/s, 100 kBit/s, 125 kBit/s, 250 kBit/s, 500 kBit/s, 800 kBit/s, 1 MBit/s	
	Interface Type	ISO-11898, refer to section CANopen Interface, page 102.	
	CANopen Assembly Interface	AIFX-CO, refer to section	AIFX-CO, page 151.
		Important! Operating the PC 104C-CO-R\F requires proper assembly interface (AIFX-CO)	
	Connector AIFX-CO	Cable Connector Fieldbus (JST 10FMN-SMT-A-TF(L	
Diagnosis Interface	Diagnostic Assembly Interface	AIFX-DIAG, refer to section	n <i>AIFX-DIAG</i> , Page 153.
	Connector AIFX-DIAG	Cable Connector DIAG X3 (JST 12FMN-SMT-A-TF(L	
Display	LED Display	SYS System St	atus LED
Power supply	Supply Voltage	+5 V dc ±5 % or +3,3 V dc Supply and Host Interface,	±5 %, refer to section <i>Power</i> page 38.
	Maximum Current at at 5 V (typically)	500 mA	
	or at 3,3 V (typically)	650 mA	
	Connector	Via PCI-104 Bus	
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Nu Switch for PCI-104 Slot Nu	umber, refer to section <i>Rotary umber</i> , page 104.
Environmental	Operating temperature range*	-20 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	-10 °C +70 °C	
	Humidity	10 95% relative humidit	y, no condensation permitted
	-	For UL compliant usage:	

CIFX 104C-CO\F, CIFX 104C-CO-R\F	Parameter	Value
		The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	97 x 91 x 24 mm
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Voltage 5 V or 3.3 V), refer to section <i>Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 107: Technical Data CIFX 104C-CO\F, CIFX 104C-CO-R\F

10.1.19 CIFX 104C-DN, CIFX 104C-DN-R

CIFX 104C-DN, CIFX 104C-DN-R	Parameter	Value	
Part	Name	CIFX 104C-DN	CIFX 104C-DN-R
	Part No.	1270.510	1271.510
	Description	PC Card cifX PCI-104 DeviceNet Master or Slave; (for CIFX 104C-DN-R connectors at the left side)	
	Function	Communication Interface with PCI-104 and fieldbus interface DeviceNet	
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus sp Overview, page 108.	pec 7], refer to section
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Memory Access)	
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
DeviceNet Communication	Supported communication standard/ protocol (determined by	DeviceNet Master, DeviceNet Slave	

CIFX 104C-DN, CIFX 104C-DN-R	Parameter	Value
	the loaded firmware)	
DeviceNet Interface	Transmission Rate	125 kBit/s, 250 kBit/s, 500 kBit/s
	Interface Type	ISO-11898 according to DeviceNet specification, refer to section <i>DeviceNet Interface</i> , page 103.
	Galvanic Isolation	isolated (optically isolated)
	Isolation Voltage	1000 VDC (tested for 1 minute)
	Connector	CombiCon-Stecker, 5-polig
Diagnosis Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.
Display	LED Display	SYS System Status LED
		MNS Module network status (duo LED)
		The meaning of the MNS LED is depending on loaded firmware. Refer to chapter <i>LED Descriptions</i> , page 78
Power supply	Supply Voltage	+5 V dc \pm 5 % or +3,3 V dc \pm 5 %, refer to section <i>Power</i> Supply and Host Interface, page 38.
	Maximum Current at at 5 V (typically)	500 mA
	or at 3,3 V (typically)	650 mA
	Connector	Via PCI-104 Bus
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Number, refer to section <i>Rotary Switch for PCI-104 Slot Number</i> , page 104.
	Rotary Switch Device Address	Is currently unassigned. Refer to section <i>Rotary Switch Device Address</i> on page 104.
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	97 x 91 x 24 mm
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Vol- tage 5 V or 3.3 V), refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test) EN 61000-6-2:2005 + B1:2011 (for industrial

CIFX 104C-DN, CIFX 104C-DN-R	Parameter	Value
		environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 108: Technical Data CIFX 104C-DN, CIFX 104C-DN-R

10.1.20 CIFX 104C-DN\F, CIFX 104C-DN-R\F

CIFX 104C-DN\F, CIFX 104C-DN-R\F	Parameter	Value	
Part	Name	CIFX 104C-DN\F	CIFX 104C-DN-R\F
	Part No.	1270.511	1271.511
	Description	PC Card cifX PCI-104 DeviceNet Master or Slave - with cable connector fieldbus for DeviceNet assembly interfac (AIFX-DN) - and cable connector DIAG for diagnostic assembly interface (AIFX-DIAG); (for CIFX 104C-DN-R\F connectors at the left side)	
	Function	Communication Interface with PCI-104 and fieldbus interface DeviceNet	
Communication Controller	Туре	netX 100 processor	
Integrated Memory	RAM	8 MB SDRAM	
	FLASH	4 MB serial Flash EPROM	
	Size of the Dual-Port Memory	64 KByte	
System Interface	Bus Type	PCI-104, according to [bus spec 7], refer to section <i>Overview</i> , page 108.	
	Transmission Rate	33 MHz	
	Data Access	DPM or DMA (Direct Memory Access)	
	Width for the data access to the Dual-Port Memory (DPM)	32-Bit	
DeviceNet Communication	Supported communication standard/ protocol (determined by the loaded firmware)	DeviceNet Master, DeviceNet Slave	
DeviceNet Interface	Transmission Rate	125 kBit/s, 250 kBit/s, 500 kBit/s	
	Interface Type	ISO-11898 according to DeviceNet specification, refer to section <i>DeviceNet Interface</i> , page 103.	
	DeviceNet Assembly Interface	AIFX-DN, refer to section AIFX-DN, page 152.	
		Important! Operating the PC ca 104C-DN-R\F requires proper co assembly interface (AIFX-DN) to	onnection of the DeviceNet
	Connector AIFX-DN	Cable Connector Fieldbus X304 (X4) (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
Diagnosis Interface	Diagnostic Assembly Interface	AIFX-DIAG, refer to section AIFX-DIAG, Page 153.	
	Connector AIFX-DIAG	Cable Connector DIAG X303 (JST 12FMN-SMT-A-TF(LF)	
Display	LED Display	SYS System State	us LED
Power supply	Supply Voltage	+5 V dc ±5 % or +3,3 V dc ± Supply and Host Interface, p	
	Maximum Current at at 5 V (typically) or	500 mA	

CIFX 104C-DN\F, CIFX 104C-DN-R\F	Parameter	Value
	at 3,3 V (typically)	650 mA
	Connector	Via PCI-104 Bus
Operation	Rotary Switch PCI-104 Slot Number	To set the PCI-104 Slot Number, refer to section <i>Rotary Switch for PCI-104 Slot Number</i> , page 104.
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	97 x 91 x 24 mm
	Mounting/Installation	PCI-104 Slot (Supply Voltage 5 V or 3.3 V, Signaling Vol- tage 5 V or 3.3 V), refer to section <i>Slot for the PC Cards</i> <i>cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104</i> , page 35.
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission	EN 55011:2009 + A1:2010, CISPR 11:2009, Class A (Radio disturbance characteristics - Limits and methods of measurement)
	Immunity	EN 61000-4-2:2009 (Electrostatic discharge test)
		EN 61000-4-3:2006 + A1:2008 + A2:2010 (Radiated, radio-frequency, electromagnetic field test)
		EN 61000-4-4:2004 + A1:2010 (Burst Electrical fast transients/burst test)
		EN 61000-4-5:2006 (Surge test)
		EN 61000-4-6:2009 (to conducted disturbances, induced by radio- frequency fields)
		EN 61000-4-8:2010 (power frequency magnetic field test)
		EN 61000-6-2:2005 + B1:2011 (for industrial environments)
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530
Configuration	Configuration Software Master and Slave	SYCON.net
	Configuration Software Slave	netX Configuration Tool

Table 109: Technical Data CIFX104C-DN\F, CIFX 104C-DN-R\F

AIFX-RE	Parameter	Value	
Part	Name	AIFX-RE	
	Part No.	2800.100	
	Description	Ethernet Assembly Interface (with Ethernet interface) for the PC cards CIFX 90-RE\F, CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\MR\ET\F, CIFX 104C-RE\F, CIFX 104C-RE-R\F	
Interface PC Card cifX	Connector	Cable Connector Ethernet X1 (JST SM20B-SRSS-TB(LF)(SN), Pitch 1.0 mm)	
Ethernet Interface	Galvanic Isolation	isolated	
	Isolation Voltage	1000 VDC (tested for 1 minute)	
	Connector	2* RJ45 Socket	
Display	LED Display (on the reverse side of the device)	The meaning of the following LEDs depends on the loaded firmware::	
		COM 0 LED Communication Status 0 (duo LED)	
		COM 1 LED Communication Status 1 (duo LED)	
		LED yellowat RJ45Ch0 and RJ45Ch1, for Ethernet Link status, Ethernet Aktivity status and additional status	
		Refer to chapter LED Descriptions, page 78.	
Power supply	Connector	Cable Connector Ethernet X1	
Environmental	Operating temperature range*	0 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	0 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	30,7 x 42,3 x 18,5 mm	
	Mounting/Installation	At the PC cards CIFX 90-RE\F, CIFX 90E-RE\F: Cable Connector Ethernet X4; or at CIFX 104C-RE\F, CIFX 104C-RE-R\F: Cable Connector Ethernet X304 (X4)	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission, Immunity	Tested together with the corresponding PC card cifX.	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	

Table 110: Technical Data AIFX-RE

10.1.22 AIFX-DP

AIFX-DP	Parameter	Value
Part	Name	AIFX-DP
	Part No.	2800.400
	Description	PROFIBUS assembly interface (with PROFIBUS interface) for the PC cards CIFX 90-DP\F, CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\MR\ET\F, CIFX 104C-DP\F, CIFX 104C-DP-R\F
Interface PC Card cifX	Connector	Cable Connector Fieldbus X1 (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)
PROFIBUS Interface	Galvanic Isolation	isolated
	Isolation Voltage	1000 VDC (tested for 1 minute)
	Connector	DSub female Connector, 9 pin
Display	LED Display (on the reverse side of the device)	The meaning of the following LEDs depends on the loaded firmware:
		ERR LED Error status (red)
		STA LED Status (green)
		For PROFIBUS MPI the STA LED is not used. Refer to chapter <i>LED Descriptions</i> , page 78.
Power supply	Connector	Cable Connector Fieldbus X1
Environmental	Operating temperature range*	-20 °C +70 °C
Conditions	*Air flow during measurment	0,5m/s
	Storage temperature range	-10 °C +70 °C
	Humidity	10 95% relative humidity, no condensation permitted
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.
Device	Dimensions (L x W x H)	17 x 31 x 18,5 mm
	Mounting/Installation	At the PC cards CIFX 90-DP\F, CIFX 90E-DP\F: Cable Connector Fieldbus X3; or at CIFX 104C-DP\F, CIFX 104C-DP-R\F: Cable Connector Fieldbus X304 (X4)
	RoHS	Yes
CE Sign	CE Sign	Yes
	Emission, Immunity	Tested together with the corresponding PC card cifX.
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530

Table 111: Technical Data AIFX-DP

10.1.23 AIFX-CO

AIFX-CO	Parameter	Value	
Part	Name	AIFX-CO	
	Part No.	2800.500	
	Description	CANopen assembly interface (with CANopen interface) for the PC cards CIFX 90-CO\F, CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-CO\MR\ET\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F	
Interface PC Card cifX	Connector	Cable Connector Fieldbus X1 (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
CANopen Interface	Galvanic Isolation	isolated (optically isolated)	
	Isolation Voltage	1000 VDC (tested for 1 minute)	
	Connector	DSub female Connector, 9 pin	
Display	LED Display (on the reverse side of the device)	The meaning of the following LEDs depends on the loaded firmware::	
		ERR LED Error status (red)	
		RUN LED Run (green)	
		Refer to chapter LED Descriptions, page 78.	
Power supply	Connector	Cable Connector Fieldbus X1	
Environmental	Operating temperature range*	-20 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	-10 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	17 x 31 x 18,5 mm	
	Mounting/Installation	At the PC cards CIFX 90-CO\F, CIFX 90E-CO\F: Cable Connector Fieldbus X3; or at CIFX 104C-CO\F, CIFX 104C-CO-R\F: Cable Connector Fieldbus X304 (X4)	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission, Immunity	Tested together with the corresponding PC card cifX	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	

Table 112: Technical Data AIFX-CO

10.1.24 AIFX-DN

AIFX-DN	Parameter	Value	
Part	Name	AIFX-DN	
	Part No.	2800.510	
	Description	DeviceNet assembly interface (with DeviceNet interface) for the PC cards CIFX 90-DN\F, CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-DN\MR\ET\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F	
Interface PC Card cifX	Connector	Cable Connector Fieldbus X1 (JST 10FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
DeviceNet Interface	Galvanic Isolation	isolated (optically isolated)	
	Isolation Voltage	1000 VDC (tested for 1 minute)	
	Connector	CombiCon-Stecker, 5-polig	
Display	LED Display (on the reverse side	MNS Module network status (duo LED)	
	of the device)	The meaning of the MNS LED is depending on loaded firmware. Refer to chapter <i>LED Descriptions</i> , page 78.	
Power supply	Connector	Cable Connector Fieldbus X1	
Environmental	Operating temperature range*	-20 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	-10 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	23,7 x 31 x 18,5 mm (L = 23,7, ohne CombiCon-Stecker)	
	Mounting/Installation	At the PC cards CIFX 90-DN\F, CIFX 90E-DN\F: Cable Connector Fieldbus X3; or at CIFX 104C-DN\F, CIFX 104C-DN-R\F: Cable Connector Fieldbus X304 (X4)	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission, Immunity	Tested together with the corresponding PC card cifX	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	

Table 113: Technical Data AIFX-DN

10.1.25 AIFX-DIAG

AIFX-DIAG	Parameter	Value	
Part	Name	AIFX-DIAG	
	Part No.	2800.000	
	Description	Diagnostic assembly interface (with diagnostic interface) for the PC cards CIFX 104C-RE\F, CIFX 104C-RE-R\F CIFX 104C-DP\F, CIFX 104C-DP-R\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F, CIFX 104C-DN\F, CIFX 104C-DN-R\F	
Interface PC Card cifX	Connector	Cable Connector DIAG X1 (JST 12FMN-SMT-A-TF(LF)(SN), Pitch 1,0 mm)	
Diagnosis Interface	USB Interface	Mini B USB Plug (5 pin), refer to section <i>Mini-B USB Connector (5 Pin)</i> , page 103.	
Display	LED Display	PWR Supply Voltage ON LED	
		SYS System Status LED	
		The meaning of the following LEDs depends on the loaded firmware::	
		COM 0 LED Communication Status 0 (duo LED)	
		COM 1 LED Communication Status 1 (duo LED)	
		Refer to chapter LED Descriptions, page 78.	
Power supply	Connector	Cable Connector DIAG X1	
Operation	Rotary Switch Device Address	Is currently unassigned. Refer to section <i>Rotary Switch Device Address</i> on page 104.	
Environmental	Operating temperature range*	-20 °C +70 °C	
Conditions	*Air flow during measurment	0,5m/s	
	Storage temperature range	-10 °C +70 °C	
	Humidity	10 95% relative humidity, no condensation permitted	
	Environment	For UL compliant usage: The device must be used in a pollution degree 2 environment.	
Device	Dimensions (L x W x H)	20,5 x 52,7 x 18,5 mm	
	Mounting/Installation	At the PC cards CIFX 104C-RE\F, CIFX 104C-RE-R\F, CIFX 104C-DP\F, CIFX 104C-DP-R\F, CIFX 104C-CO\F, CIFX 104C-CO-R\F, CIFX 104C-CN\F, CIFX 104C-DN-R\F Cable Connector DIAG X303 (X3)	
	RoHS	Yes	
CE Sign	CE Sign	Yes	
	Emission, Immunity	Tested together with the corresponding PC card cifX	
UL Certification	The device is certified according to UL 508	UL-File-Nr. E221530	

Table 114: Technical Data AIFX-DIAG

10.2 PCI IDs PC Cards cifX on the PCI Bus

On the PCI bus the PC Cards cifX have the following PCI IDs:

PCI IDs	Value
VendorID	0x15CF
DeviceID	0x0000
Subsystem Vendor ID	0x0000
Subsystem Device ID	0x0000

Table 115: PCI IDs PC Cards cifX on the PCI Bus

10.3 Supported PCI Bus Commands

From the following table you can see which PCI bus commands are supported by the Hilscher PC Cards cifX *Compact PCI, Mini PCI, Mini PCI Express* and *PCI-104*.

C/BE3#	C/BE2#	C/BE1#	C/BE0#	Command Type	supported
0	0	0	0	Interrupt Acknowledge	no
0	0	0	1	Special Cycle	no
0	0	1	0	I/O Read	✓
0	0	1	1	I/O Write	✓
0	1	0	0	Reserved	no
0	1	0	1	Reserved	no
0	1	1	0	Memory Read	✓
0	1	1	1	Memory Write	✓
1	0	0	0	Reserved	no
1	0	0	1	Reserved	no
1	0	1	0	Configuration Read	✓
1	0	1	1	Configuration Write	✓
1	1	0	0	Memory Read Multiple	no
1	1	0	1	Dual Address Cycle	no
1	1	1	0	Memory Read Line	no
1	1	1	1	Memory Write and Invalidate	no

Table 116: Supported / not supported PCI Bus Commands

C/BE = Bus Command and Byte Enable Signal of PCI

10.4 Technical Data of the Communication Protocols

10.4.1 EtherCAT Master

Parameter	Description
Maximum number of EtherCAT slaves	Maximum 200 Slaves. The number of usable slaves depends on the available memory for the configuration file. See 'configuration file' below.
Maximum number of cyclic input data	5760 bytes
Maximum number of cyclic output data	5760 bytes
Minimum bus cycle time	205 μ s, depending on the used number of slaves and the used number of cyclic input data and output data. Recommended is a cycle time of 1 ms and higher.
Acyclic communication	CoE (CANopen over EtherCAT)
	CoE-Upload, CoE-Download
	Maximum 1500 bytes
Functions	Get OD List
	Get object description
	Get entry description
	Emergency
	Slave diagnostics
Bus Scan	Supported
Redundancy	Supported, but not at the same time with Distributed Clocks
Distributed Clocks	Supported, but not at the same time with Redundancy
Topology	Line or ring
Baud rate	100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
Configuration File (ethercat.xml or config.nxd)	PC cards PCI, PCI Express, PCI Express Low Profile, Mini PCI, Compact PCI, Mini PCI Express, PCI-104 Real-Time Ethernet: Maximum 1 MByte
	PC cards PC/104 Real-Time Ethernet: Maximum 2 MByte
Limitations	The size of the bus configuration file is limited by the size of the RAM Disk (1 Mbyte) or FLASH disk (2 Mbyte).
	All CoE Uploads, Downloads and information services must fit in one TLR-Packet. Fragmentation is not supported
	Distubuted Clock and Redundancy can not be used at the same time.
Reference to firmware/stack version	V3.0.x.x

Table 117: Technical Data EtherCAT Master Protocol

10.4.2 EtherCAT Slave

Parameter	Description
Maximum number of cyclic input data	256* bytes
Maximum number of cyclic output data	256* bytes
Acyclic communication	SDO
	SDO Master-Slave
	SDO Slave-Slave (depending on Master capability)
Туре	Complex Slave
Functions	Emergency
FMMUs	3
SYNC Manager	4
Distributed Clocks (DC)	Supported, 32 Bit
Baud rate	100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
Limitation	LRW is not supported
Reference to firmware/stack version	V2.5.x.x and V4.2.x.x

Table 118: Technical Data EtherCAT Slave Protocol



Note: * The loadable firmware supports for the number of cyclic input data and for cyclic output data in total up to 512 bytes. If more than 256 bytes for input data or for output data shall be exchanged via EtherCAT, then a customer specific XML file is necessary. Additionally the following formula applies: The sum of the input data length and the ouput data length may not exceed 512 bytes, where each length has to be rounded up to the next multiple of 4 for this calculation.

10.4.3 EtherNet/IP Scanner (Master)

Parameter	Description
Maximum number of EtherNet/IP connections	64 connections for implicit and explicit
Maximum number of total cyclic input data	5712 bytes
Maximum number of total cyclic output data	5760 bytes
Maximum number of cyclic input data	504 bytes per slave per telegram
Maximum number of cyclic output data	504 bytes per slave per telegram
IO Connection type	Cyclic, minimum 1 ms (depending on used number of connections and used number of input and output data)
Maximum number of unscheduled data	1400 bytes per telegram
UCMM, Class 3	Supported
Explicit Messages, Client and Server Services	Get_Attribute_Single/All
	Set_Attribute_Single/All
Quick connect	Supported
Predefined standard objects	Identity Object
	Message Route Object
	Assembly Object
	Connection Manager
	Ethernet Link Object
	TCP/IP Object
	DLR Object
	QoS Object
Maximal number of user specific objects	20
Topology	Tree, Line, Ring
DLR (Device Level Ring)	Beacon based 'Ring Node'
ACD (Address Conflict Detection)	Supported
DHCP	Supported
BOOTP	Supported
Baud rates	10 and 100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
Switch function	Integrated
Limitations	CIP Sync Services are not implemented
	TAGs are not supported
Reference to firmware/stack version	V2.6.x.x

Table 119: Technical Data EtherNet/IP Scanner (Master) Protocol

10.4.4 EtherNet/IP Adapter (Slave)

Parameter	Description
Maximum number of input data	504 bytes
Maximum number of output data	504 bytes
IO connection types (implicit)	1 exclusive owner, 1 listen only, 1 input only
IO Connection trigger types	Cyclic, minimum 1 ms*
	Application Triggered, minimum 1 ms*
	Change Of State, minimum 1 ms*
	* depending on number of connections and number of input and output data
Explicit Messages	Connected and unconnected
Maximum number of connections	8, explicit and implicit connections
Unconnected Message Manager (UCMM)	Supported
Quick connect	Supported
Predefined standard objects	Identity Object
	Message Route Object
	Assembly Object
	Connection Manager
	DLR Object
	QoS Object
	TCP/IP Object
	Ethernet Link Object
Reset services	Identity Object Reset Service of Type 0 and 1
Maximum number of user specific objects	20
DLR V2 (ring topology)	Supported
ACD (Address Conflict Detection)	Supported
DHCP	Supported
BOOTP	Supported
Baud rates	10 and 100 MBit/s
Duplex modes	Half duplex, Full duplex, Auto negotiation
MDI modes	MDI, MDI-X, Auto-MDIX
Data transport layer	Ethernet II, IEEE 802.3
Switch function	Integrated
Limitations	CIP Sync Services are not implemented
	TAGs are not supported
Reference to firmware/stack version	V2.7.x.x

Table 120: Technical Data EtherNet/IP Adapter (Slave) Protocol

10.4.5 Open Modbus/TCP

Parameter	Description
Maximum number of input data	2880 Registers
Maximum number of output data	2880 Registers
Acyclic communication	Read/Write Register: - Maximum 125 Registers per Read Telegram (FC 3, 4, 23), - Maximum 121 Registers per Write Telegram (FC 23), - Maximum 123 Registers per Write Telegram (FC 16)
	Read/Write Coil: - Maximum 2000 Coils per Read Telegram (FC 1, 2), - Maximum 1968 Coils per Write Telegram (FC 15)
Modbus Function Codes	1, 2, 3, 4, 5, 6, 7, 15, 16, 23* * Function Code 23 can be used via the packet API, but not with the Command Table.
Protocol Mode	Message Mode (Client Mode): - Client (using the Command Table: The data is stored in the I/O process data image) - Client (using the packet API: The I/O process data image is not used) - Server (using the packet API: The I/O process data image is not used) I/O Mode (Server Mode):
	- Server (only) (The data is stored in the I/O process data image)
Baud rates	10 and 100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
Reference to firmware/stack version	V2.5.x.x

Table 121: Technical Data Open Modbus/TCP Protocol

10.4.6 POWERLINK Controlled Node/Slave

Parameter	Description
Maximum number of cyclic input data	1490 bytes
Maximum number of cyclic output data	1490 bytes
Acyclic data transfer	SDO Upload/Download
Functions	SDO over ASND and UDP
Baud rate	100 MBit/s, half-duplex
Data transport layer	Ethernet II, IEEE 802.3
Ethernet POWERLINK version	V 2
Limitation	No slave to slave communication
Reference to firmware/stack version	V2.1.x.x

Table 122: Technical Data POWERLINK Controlled Node (Slave) Protocol

10.4.7 PROFINET IO-Controller

Parameter	Description
Maximum number of PROFINET IO Devices	128
Maximum number of total cyclic input data	5712 bytes (including IOxS status bytes)
Maximum number of total cyclic output data	5760 bytes (including IOxS status bytes)
Maximum number of cyclic input data	1440 bytes per device (= IOCR data length including IOxS status bytes)
Maximum number of cyclic output data	1440 bytes per device (= IOCR data length including IOxS status bytes)
Acyclic communication	Read/Write Record
	Limited to 1392 bytes per telegram
	Limited to 4096 bytes per request
Alarm processing	yes, but requires handling in host application program
Diagnostic data	One 200 byte buffer per IO device
DCP functions via API	Name Assignment IO-Devices (DCP SET NameOfStation)
	Set IO-Devices IP (DCP SET IP)
	Signal IO-Device (DCP SET SIGNAL)
	Reset IO-Device to factory settings (DCP Reset FactorySettings)
	Bus scan (DCP IDENTIFY ALL)
Supported Protocols	RTC – Real Time Cyclic Protocol, Class 1
	RTA – Real Time Acyclic Protocol
	DCP – Discovery and configuration Protocol
	CL-RPC – Connectionless Remote Procedure Call
Context management by CL-RPC	Supported
Minimum cycle time	1 ms
	Different IO-Devices can be configured with different cycle times
Functions	Fast Startup of PROFINET IO Devices supported
Baud rate	100 MBit/s
	Full-Duplex mode
Data transport layer	Ethernet II, IEEE 802.3
Configuration file	Maximum 1 MByte
Limitations	RT over UDP not supported
	Multicast communication not supported
	DHCP is not supported (neither for PROFINET IO-Controller nor for the IO-Devices)
	Only one IOCR per IO Device
	NameOfStation of IO Controller CANNOT be set using the DCP SET NameOfStation service but only at start-up while configuring the IO Controller
	The buffer for IO-Device diagnosis data will be overwritten in case of multiple diagnostic events. Only one (the last) event is stored at the same time. If a single event produces more than 200 bytes of diagnosis data, only the first 200 bytes will be taken care of.
	The usable (minimum) cycle time depends on the number of used IO Devices, the number of used input and output data. The cycle-time, the number of configured IO Devices and the amount of IO data depend on each other. For example it is not possible due to performance reasons to have 128 IO Devices communication with cycle-time 1ms.
	The size of the bus configuration file is limited by the size of the RAM
	Disk (1 MByte)

Parameter	Description
Reference to firmware/stack version	V2.6.x.x

Table 123: Technical Data PROFINET IO RT Controller

10.4.8 PROFINET IO-Device (V3.4)

Parameter	Description
Maximum number of cyclic input data	1024 bytes
Maximum number of cyclic output data	1024 bytes
Acyclic communication	Read/Write Record, max. 1024 bytes per telegram
Alarm Types	Process Alarm, Diagnostic Alarm, Return of SubModule Alarm Plug Alarm (implicit), Pull Alarm (implicit)
Supported protocols	RTC – Real Time Cyclic Protocol, Class 1 and 2 (unsynchronized), Class 3 (synchronized)
	RTA – Real Time Acyclic Protocol
	DCP – Discovery and configuration Protocol
	CL-RPC – Connectionless Remote Procedure Call
	LLDP – Link Layer Discovery Protocol
	SNMP – Simple Network Management Protocol
	MRP – MRP Client
Used Protocols (subset)	UDP, IP, ARP, ICMP (Ping)
Topology recognition	LLDP, SNMP V1, MIB2, physical device
VLAN- and priority tagging	yes
Context Management by CL-RPC	Supported
Identification & Maintenance	Read and write of I&M1-4
Minimum cycle time	1 ms for RTC1 and RTC2
	250 μs for RTC3
Baud rate	100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
Limitations	RT over UDP not supported
	Multicast communication not supported
	Only one device instance is supported
	DHCP is not supported
	IRT "flex" (synchronized RT Class 2) is not supported
	FastStartUp is not supported.
	Media Redundancy (except MRP client) is not supported
	Access to the submodule granular status bytes (IOCS) is not supported.
	The amount of configured IO-data influences the minimum cycle time that can be reached.
	Supervisor-AR is not supported, Supervisor-DA-AR is supported
	Only 1 Input-CR and 1 Output-CR are supported
	Multiple WriteRequests are not supported
	Using little endian (LSB-MSB) byte order for cyclic process data instead of default big endian (MSB-LSB) byte order may have an negative impact on minimum reachable cycle time
Reference to firmware/stack version	V3.4.x.x

Table 124: Technical Data PROFINET IO RT IRT Device Protocol

10.4.9 PROFINET IO-Device (V3.5)

Parameter	Description
Maximum number of cyclic input data	1440 bytes
Maximum number of cyclic output data	1440 bytes
Maximum number of submodules	255 submodules per Application Relation at the same time, 1000 submodules can be configured
Multiple Application Relations (AR)	The Stack can handle up to 2 IO-ARs, one Supervisor AR and one Supervisor-DA AR at the same time
Acyclic communication	Read/Write Record, max. 1024 bytes per telegram
Alarm Types	Process Alarm, Diagnostic Alarm, Return of SubModule Alarm, Plug Alarm (implicit), Pull Alarm (implicit)
Supported protocols	RTC – Real Time Cyclic Protocol, class 1 (unsynchronized), class 3 (synchronized)
	RTA – Real Time Acyclic Protocol
	DCP – Discovery and configuration Protocol
	CL-RPC – Connectionless Remote Procedure Call
	LLDP – Link Layer Discovery Protocol
	SNMP – Simple Network Management Protocol
	MRP – MRP Client
Topology recognition	LLDP, SNMP V1, MIB2, physical device
Identification & Maintenance	Read and write of I&M1-4
Minimum cycle time	1 ms for RT_CLASS_1
	250 μs for RT_CLASS_3
IRT Support	RT_CLASS_3
Media Redundancy	MRP client is supported
Additional features	DCP, VLAN- and priority tagging, Shared Device
Baud rate	100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
PROFINET IO specification	V2.2 (legacy startup) and
•	V2.3 (but advanced startup only for RT) are supported
Limitations	RT over UDP not supported
	Multicast communication not supported
	Only one device instance is supported
	DHCP is not supported
	FastStartUp is not supported
	The amount of configured IO-data influences the minimum cycle time that can be reached.
	Only 1 Input-CR and 1 Output-CR are supported
	Using little endian (LSB-MSB) byte order for cyclic process data instead of default big endian (MSB-LSB) byte order may have an negative impact on minimum reachable cycle time
	System Redundancy (SR-AR) and Configuration-in-Run (CiR) are not supported
	Max. 255 submodules can be used simultaneously within one specific Application Relation
	Advanced Startup according Profinet specification V2.3 is NOT yet supported for IRT
	As there is no official certification available for Profinet IO specification V2.3 at the time of release of the stack there is no guarantee that this implementation will pass such a certification once it is available
Reference to firmware/stack version	V3.5.26.x

Table 125: Technical Data PROFINET IO RT IRT Device Protocol

10.4.10 sercos Master

Parameter	Description
Maximum number of cyclic input data	5760 bytes (including Connection Control per Connection)
Maximum number of cyclic output data	5760 bytes (including Connection Control per Connection)
Maximum number of configured slave devices	511
Minimum cycle time	250 μs
Acyclic communication	Service channel: Read/Write/Commands
Functions	Bus Scan
Communication phases	NRT, CP0, CP1, CP2, CP3, CP4
Topology	Line and double ring
Redundancy	supported
NRT channel	supported
Hot-Plug	supported
Cross Communication	supported, but only if the master is configured by the host application program by packets.
Baud rate	100 MBit/s, full duplex
Data transport layer	Ethernet II, IEEE 802.3
Auto crossover	supported
Supported sercos version	Communication Specification Version 1.3
TCP/IP stack	integrated
Reference to firmware/stack version	V2.1.x.x

Table 126: Technical Data sercos Master Protocol

10.4.11 sercos Slave

Parameter	Description
Maximum number of cyclic input data (Tx) of all slaves	128 bytes (including Connection Control and IO Status)
Maximum number of cyclic output data (Rx) of all slaves	128 bytes (including Connection Control and IO Status)
Maximum number of slave devices	8
Maximum number of applicable sercos addresses	1 511
Minimum cycle time	250 μs
Topology	Line and ring
Communication phases	NRT, CP0, CP1, CP2, CP3, CP4
Acyclic Communication (Service Channel)	Read/Write/Standard Commands
Baud rate	100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
Supported sercos version	sercos in the third generation
	Communication Specification Version 1.1.2
Supported sercos Communication Profiles	SCP_FixCFG Version 1.1.1
	SCP_VarCFG Version 1.1.1
	SCP_VarCFG Version 1.1.3
Supported User SCP Profiles	SCP_WD Version 1.1.1
	SCP_Diag Version 1.1.1
	SCP_RTB Version 1.1.1
	SCP_Mux Version 1.1.1
	SCP_Sig Version 1.1.1
	SCP_ExtMuX Version 1.1.2
	SCP_RTBListProd Version 1.3
	SCP_RTBListCons Version 1.3
	SCP_RTBWordProd Version 1.3
	SCP_RTBWordCons Version 1.3
	SCP_OvSBasic Version 1.3
	SCP_WDCon Version 1.3
Supported FSP profiles	FSP_IO
	FSP_Drive
SCP Sync	Supported
SCP_NRT	Supported
S/IP	Supported
Identification LED	Supported
Storage location of object dictionary	Mixed mode
Limitations	Max. 2 connections: 1 for consumer and 1 for producer
	Modifications of the Service-Channel Object Dictionary will be volatile after reset (if it resides on device)
	Hot plug is not supported yet
	Cross communication not supported yet

Table 127: Technical Data sercos Slave Protocol

10.4.12 VARAN Client (Slave)

Parameter	Description
Maximum number of cyclic input data	128 bytes
Maximum number of cyclic output data	128 bytes
Memory Area	Read Memory Area 1,
	Write Memory Area 1
Functions	Memory Read
	Memory Write
Integrated 2 port splitter for daisy chain topology	Supported
Baud rate	100 MBit/s
Data transport layer	Ethernet II, IEEE 802.3
VARAN protocol version	1.1.1.0
Limitations	Integrated EMAC for IP data exchange with client application not supported
	SPI single commands (optional feature) not supported
	Memory area 2 is not supported.
Reference to firmware/stack version	V1.0.x.x

Table 128: Technical Data VARAN Client Protocol

10.4.13 PROFIBUS DP Master

Parameter	Description
Maximum number of PROFIBUS DP slaves	125 (DPV0/DPV1)
Maximum number of total cyclic input data	5712 bytes
Maximum number of total cyclic output data	5760 bytes
Maximum number of cyclic input data	244 bytes per slave
Maximum number of cyclic output data	244 bytes per slave
Configuration data	Max. 244 bytes per slave
Parameterization data per slave	7 bytes standard parameter per slave
	Max. 237 bytes application specific parameters per slave
Acyclic communication	DPV1 class 1 read, write
	DPV1 class 1 alarm
	DPV1 class 2 initiate, read, write, data transport, abort
Maximum number of acyclic read/write	240 bytes per slave and telegram
Functions	Configuration in Run (CiR), requires host application program support
	Timestamp (Master functionality)
Redundancy	Supported, requires host application program support
Baud rate	9,6 kBits/s, 19,2 kBits/s, 31,25 kBits/s, 45,45 kBits/s 93,75 kBits/s, 187,5 kBits/s, 500 kBits/s, 1, 5 MBits/s, 3 MBits/s, 6 MBits/s, 12 MBit/s
	Auto baud rate detection is not supported
Data transport layer	PROFIBUS FDL
Limitations	DPV2 isochronous mode and slave slave communication are not supported.
	The redundancy function can not be used, if the master is configured by the host application program by packets.
Reference to firmware/stack version	V2.6.x.x

Table 129: Technical Data PROFIBUS DP Master Protocol

10.4.14 PROFIBUS DP Slave

Parameter	Description
Maximum number of cyclic input data	244 bytes
Maximum number of cyclic output data	244 bytes
Maximum number of acyclic data (read/write)	240 bytes/telegram
Maximum number of modules	24
Configuration data	Max. 244 bytes
Parameter data	237 bytes application specific parameters
Acyclic communication	DP V1 Class 1 Read/Write
	DP V1 Class 1 Alarm
	DP V1 Class 2 Read/Write/Data Transport
Baud rate	9,6 kBits/s, 19,2 kBits/s, 31,25 kBits/s, 45,45 kBits/s, 93,75 kBits/s, 187,5 kBits/s, 500 kBits/s, 1, 5 MBits/s, 3 MBits/s, 6 MBits/s, 12 MBit/s
	Auto baudrate detection is supported
Data transport layer	PROFIBUS FDL
Limitations	SSCY1S – Slave to slave communication state machine not implemented
	Data exchange broadcast not implemented
	I&M LR services other than Call-REQ/RES are not supported yet
Reference to firmware/stack version	V2.7.x.x

Table 130: Technical Data PROFIBUS DP Slave Protocol

10.4.15 PROFIBUS MPI

Parameter	Description
Maximum number of MPI connections	126
Maximum number of write data	216 bytes
Maximum number of read data	222 bytes
Functions	MPI Read/Write DB (data block), M (marker), Q (output), C (Counter), T (Timer)
	MPI Read I (Input)
	Data type bit to access to DB (data block), M (marker), Q (output) and I (Input, read only)
	MPI Connect (automatically when first read/write function is used)
	MPI Disconnect, MPI Disconnect All
	MPI Get OP Status
	MPI transparent (expert use only)
Baud rate	Fixed values ranging from 9,6 kBits/s to 12 MBit/s
	Auto-detection mode is supported
Data transport layer	PROFIBUS FDL
Reference to firmware/stack version	2.4.x.x

Table 131: Technical Data PROFIBUS-MPI Protocol

10.4.16 CANopen Master

Parameter	Description	
Maximum number of CANopen nodes	126	
Maximum number of cyclic input data	3584 bytes	
Maximum number of cyclic output data	3584 bytes	
Maximum number of receive PDOs	512	
Maximum number of transmit PDOs	512	
Exchange of process data	Via PDO transfer: - synchronized, - remotely requested and - event driven (change of date)	
Acyclic communication	SDO Upload/Download, max. 512 bytes per request	
Functions	Emergency message (consumer and producer)	
	Node guarding / life guarding, heartbeat	
	PDO mapping	
	NMT Master	
	SYNC protocol (producer)	
	Simple boot-up process, reading object 1000H for identification	
Baud rates	10 kBits/s, 20 kBits/s, 50 kBits/s, 100 kBits/s, 125 kBits/s, 250 kBits/s, 500 kBits/s, 800 kBits/s, 1 MBits/s	
CAN layer 2 access	Send/receive via API supported (11 bit/29 bit)	
Data transport layer	CAN Frames	
CAN Frame type for CANopen	11 Bit	
Reference to version	V2.11.x.x	

Table 132: Technical Data CANopen Master Protocol

10.4.17 CANopen Slave

Parameter	Description	
Maximum number of cyclic input data	512 bytes	
Maximum number of cyclic output data	512 bytes	
Maximum number of receive PDOs	64	
Maximum number of transmit PDOs	64	
Exchange of process data	Via PDO transfer - synchronized, - remotely requested and - event driven (change of date, event timer) On request of the host application program by packet	
Acyclic communication	SDO upload/download (server only)	
	Emergency message (producer)	
	Timestamp (producer/consumer)	
Functions	Node guarding / life guarding	
	Heartbeat: 1 producer, max. 64 consumer	
	PDO mapping	
	NMT Slave	
	SYNC protocol (consumer)	
	Error behaviour (configurable): - in state operational: change to state pre-operational - in any state: no state change - in state operational or pre-operational: change to state stopped	
Baud rates	10 kBits/s, 20 kBits/s, 50 kBits/s, 100 kBits/s, 125 kBits/s, 250 kBits/s, 500 kBits/s, 800 kBits/s, 1 MBits/s Auto baudrate detection is supported	
CAN layer 2 access	Send/receive via API supported (11 bit/29 bit)	
Data transport layer	CAN Frames	
CAN Frame type for CANopen	11 Bit	
Reference to firmware/stack version	V3.6.x.x	
Reference to IIIIIwale/Stack version	٧٥.٥.٨.٨	

Table 133: Technical Data CANopen Slave Protocol

10.4.18 DeviceNet Master

Parameter	Description
Maximum number of DeviceNet slaves	63
Maximum number of total cyclic input data	3584 bytes
Maximum number of total cyclic output data	3584 bytes
Maximum number of cyclic input data	255 bytes/connection
Maximum number of cyclic output data	255 bytes/connection
Maximum Configuration data	1000 bytes/slave
Acyclic communication	Explicit connection
	All service codes are supported
Connections	Bit Strobe
	Change of State
	Cyclic
	Poll
	Explicit Peer-to-Peer Messaging
Function	Quick Connect
Fragmentation	Explicit and I/O
UCMM	Supported
Objects	Identity Object (Class Code 0x01)
	Message Router Object (Class Code 0x02)
	DeviceNet Object (Class Code 0x03)
	Connection Object (Class Code 0x05)
	Acknowledge Handler Object (Class Code 0x06)
Baud rates	125 kBits/s, 250 kBit/s, 500 kBit/s
	Auto baudrate detection is not supported
Data transport layer	CAN frames
Reference to firmware/stack version	V2.3.x.x

Table 134: Technical Data DeviceNet Master Protocol

10.4.19 DeviceNet Slave

Parameter	Description
Maximum number of cyclic input data	255 bytes
Maximum number of cyclic output data	255 bytes
Acyclic communication	Get_Attribute_Single/All
	Max. 240 bytes per request
	Set_Attribute_Single/All
	Max. 240 bytes per request
Connections	Poll
	Change-of-state
	Cyclic
	Bit-strobe
Explicit messaging	Supported
Fragmentation	Explicit and I/O
UCMM	Not supported
Baud rates	125 kBits/s, 250 kBit/s, 500 kBit/s
	Auto baudrate detection is not supported
Data transport layer	CAN frames
Reference to firmware/stack version	V2.3.x.x

Table 135: Technical Data DeviceNet Slave Protocol

11 Annex

11.1 Matrix Label

A matrix label is on the device. It contains 3 items:

- 1. Part number
- 2. Hardware Revision
- 3. Serial number

The figure shows part number 1250.100, hardware revision 3 and serial number 23456.

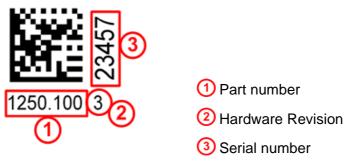


Figure 56: Matrix Label

11.2 EtherCAT Summary over Vendor ID, Conformance test, Membership and Network Logo

11.2.1 Vendor ID

The communication interface product is shipped with Hilscher's secondary vendor ID, which has to be replaced by the Vendor ID of the company shipping end products with the integrated communication interface. End Users or Integrators may use the communication interface product without further modification if they re-distribute the interface product (e.g. PCI Interface card products) only as part of a machine or machine line or as spare part for such a machine. In case of questions, contact Hilscher and/or your nearest ETG representative. The ETG Vendor-ID policies apply.

11.2.2 Conformance

EtherCAT Devices have to conform to the EtherCAT specifications. The EtherCAT Conformance Test Policies apply, which can be obtained from the EtherCAT Technology Group (ETG, <u>www.ethercat.org</u>).

Hilscher range of embedded network interface products are conformance tested for network compliance. This simplifies conformance testing of the end product and can be used as a reference for the end product as a statement of network conformance (when used with standard operational settings). It must however be clearly stated in the product documentation that this applies to the network interface and not to the complete product.

Conformance Certificates can be obtained by passing the conformance test in an official EtherCAT Conformance Test lab. Conformance Certificates are not mandatory, but may be required by the end user.

11.2.3 Certified Product vs. Certified Network Interface

The EtherCAT implementation may in certain cases allow one to modify the behavior of the EtherCAT network interface device in ways which are not in line with EtherCAT conformance requirements. For example, certain communication parameters are set by a software stack, in which case the actual software implementation in the device application determines whether or not the network interface can pass the EtherCAT conformance test. In such cases, conformance test of the end product must be passed to ensure that the implementation does not affect network compliance.

Generally, implementations of this kind require in-depth knowledge in the operating fundamentals of EtherCAT. To find out whether or not a certain type of implementation can pass conformance testing and requires such testing, contact EtherCAT Technology Group ("ETG", <u>www.ethercat.org</u>) and/or your nearest EtherCAT conformance test centre. EtherCAT may allow the combination of an untested end product with a conformant network interface. Although this may in some cases make it possible to sell the end product without having to perform network conformance tests, this approach is generally not endorsed by Hilscher. In case of questions, contact Hilscher and/or your nearest ETG representative.

11.2.4 Membership and Network Logo

Generally, membership in the network organization and a valid Vendor-ID are prerequisites in order to be able to test the end product for conformance. This also applies to the use of the EtherCAT name and logo, which is covered by the ETG marking rules.

Vendor ID Policy accepted by ETG Board of Directors, November 5, 2008

11.3 Notes on earlier Hardeware Revisions

11.3.1 Failure in 10 MBit/s Half Duplex Mode and Workaround

The note is only valid for the PC cards cifX up to serial numbers indicated:

PC Cars cifX	Part No	up to Serial Number
CIFX 80-RE	1280.100	20034
CIFX 90-RE\F	1290.100	20198
CIFX 90E-RE\F	1291.100	20311
CIFX 104C-RE	1270.100	20137
CIFX 104C-RE-R	1271.100	20029
CIFX 104C-RE\F	1270.101	20197
CIFX 104C-RE-R\F	1271.101	20071

NOTICE

Failure of the Network Communication

- Do not operate hardware with the communication controllers netX 50, netX100 or netX 500 with the protocols Ethernet TCP/UDP/IP, EtherNet/IP or Modbus TCP at 10 MBit/s in half-duplex mode, otherwise failure of the network communication can occur.
- Use only switches or 10/100 MBit/s dual-speed hubs and ensure that the network operates at 100 MBit/s and in full-duplex mode.

Affected Hardware

Hardware with the communication controller netX 50, netX 100 or netX 500; netX/Internal PHYs.

When can this Failure occur?

When using standard Ethernet communication with 10 MBit/s half duplex mode, the PHY gets stuck in case of network collisions. Then no further network communication is possible. Only device power cycling allows Ethernet communication again.

This problem can only occur with Ethernet TCP/UDP IP, EtherNet/IP or Modbus TCP protocols when using hubs at 10 MBit/s. The issue described above is not applicable for protocols which use 100 MBit/s or full duplex mode.

Solution / Workaround:

Do not use 10 MBit/s-only hubs. Use either switches or 10/100 MBit/s Dual Speed hubs, to make sure the netX Ethernet ports are connected with 100 MBit/s or in full duplex mode.

This erratum is fixed with all components of the 'Y' charge (9 digit charge number shows 'Y' at position 5 (nnnnYnnnn).

Reference

"Summary of 10BT problem on EthernetPHY", RenesasElectronics Europe, April 27, 2010

11.3.2 Pin Assignment for Mini PCI Express Bus / SYNC Connector (Bootstart), X1/X2

The following details refer to the PC cards CIFX 90E-RE\F*, CIFX 90E-DP\F, CIFX 90E-CO\F, CIFX 90E-DN\F of the earlier hardware revisions 5, 6, 7, 8 und A, as well as on the current hardware revision B (without the variants 'MR' or 'ET').

Pin (X1)	Signal	Pin (X2)	Signal
51	(not used)	52	+3.3V
49	(not used)	50	GND
47	(not used)	48	(not used) (beginning from HW Rev. 6)
45	(not used)	46	IO_SYNC0 (not used with fieldbus protocols)
43	(not used)	44	IO_SYNC1 (not used with fieldbus protocols)
41	(not used)	42	Bootstart
39	(not used)	40	GND
37	(not used)	38	USB_D+ (beginning from HW Rev. B disabled – not used)
35	GND	36	USB_D - (beginning from HW Rev. B disabled – not used)
33	PERp0	34	GND
31	PERn0	32	(not used)
29	GND	30	(not used)
27	GND	28	(not used) (beginning from HW Rev. 6)
25	PETp0	26	GND
23	PETn0	24	(not used) (for HW Rev. ,6,7,8' + B)
21	GND	22	PERST#
19	(not used)	20	(not used)
17	(not used)	18	GND
15	GND	16	(not used)
13	REFCLK+	14	(not used)
11	REFCLK-	12	(not used)
9	GND	10	(not used)
7	CLKREQ#	8	(not used)
5	(not used)	6	(not used) (beginning from HW Rev. 6)
3	(not used)	4	GND
1	(not used)	2	3.3V

*The SYNC connection is realized via the mini PCI Express bus.

Table 136: Pin Assignment Mini PCI Express Bus / SYNC Connector, X1/X2

Unless otherwise noted, the pin assignment for Mini PCI Express bus, X1/X2 described in *Table 89* corresponds to the bus specification for Mini PCI Express [bus spec 6, Rev. 1.2, Section 3.3].



Note! Please note the following characteristics for the pin assignment of the Mini PCI Express Bus, X1/X2, described in *Table 89*:

- The Pins 6, 28, 48 and Pin 24 are 'not used' (beginning from HW Rev. 6).
- The Pins 36 and 38 are 'not used' (beginning from HW Rev. B).
- The pin assignment of the **Pins 42, 44, 46** deviates from the standard specification Mini PCI Express.

E

For the reference for [bus spec 6] for the bus specification for Mini PCI Express refer to section *References PCI Specifications* on page 109 of this manual.

Pins 6, 28, 48

- Beginning from Hardware Revision 6: For the hardware revisions of the PC cards CIFX 90E-RE\F, CIFX 90E-DP\F, CIFX 90E-CO\F and CIFX 90E-DN\F beginning from Revision 6 the Pins 6, 28, 48 are 'not used', as listed in the *Table 89* on page 111.
- Hardware Revisions 1 to 5:



Important! For the hardware revisions **1 to 5** the **pins 6, 28, 48** may not be connected on the main board.

<u> Pin 24</u>

- Hardware Revisions 6,7,8 + B: For the hardware revisions 6,7,8 + B of the PC cards CIFX 90E-RE\F, CIFX 90E-DP\F, CIFX 90E-CO\F and CIFX 90E-DN\F the Pin 24 is 'not used'.
- Hardware Revisions 1 to 5 and A: For the hardware revisions 1 to 5 and A the Pin 24 is used for +3.3Vaux, according to the bus specification for Mini PCI Express.

Pin (X2)	Signal	
	for HW Rev. 6,7,8 + B	for HW Rev. 1 to 5 and A
24	(not used)	<u>+3.3Vaux</u>
Usable with main boards according to Mini PCI Express specification [bus spec 6]	rev. <u>1.1, 1.2</u> and <u>2.0</u>	rev. <u>1.2</u> and <u>2.0</u>

Table 137: Assignment of the Pin 24 for HW-Rev. 6,7,8 + B or 1 to 5 and A



Note: Due to how **pin 24** is used, the hardware revisions **6,7,8 + B** of the PC cards CIFX 90E-RE\F, CIFX 90E-DP\F, CIFX 90E-CO\F and CIFX 90E-DN\F can be used together with main boards according to any earlier revision as well as to the latest revision of the Mini PCI Express specification.

Pins 36 and 38 (USB Connector)

The USB connector at the Mini PCI Express Bus of the PC cards CIFX 90E-RE\F, CIFX 90E-DP\F, CIFX 90E-CO\F and CIFX 90E-DN\F is directly connected to the CPU of the PC and is not used for external diagnosis.

- Hardware Revisions 1 to A: When you start the PC, the operating system recognizes the USB port and asks for a driver.
- **Beginning of Hardware Revision B:** The Pins 36 and 38 are disabled and are 'not used'. When you start the PC, the operating system does not ask for an USB driver.

Pin 42 (Bootstart) and 44, 46 (SYNC)

- **Pin 42 (Bootstart):** For all PC cards CIFX 90E-RE\F, CIFX 90E-DP\F, CIFX 90E-CO\F and CIFX 90E-DN\F **pin 42** is used for **Bootstart**.
- **Pins 44 , 46 (SYNC):** For the PC cards CIFX 90E-**RE**\F depending by the protocol the **SYNC connection** is realized via the **pins 44 and 46** of the mini PCI Express bus. For further information to the SYNC pins (Pin 46, 44) refer to section *SYNC Connector (Pin-Assignment, Hardware/Firmware)* on page 107.

According to the bus specification for Mini PCI Express [bus spec 6] the pins are used to realize the LED status (Pin 42 "WWAN#", Pin 44 "WLAN#", Pin 46 "WPAN#").

11.4 Disposal of Waste Electronic Equipment

According to the European Directive 2002/96/EG "Waste Electrical and Electronic Equipment (WEEE)", waste electronic equipment may not be disposed of as household waste. As a consumer, you are legally obliged to dispose of all waste electronic equipment according to national and local regulations.



Waste Electronic Equipment

- This product must not be treated as household waste.
- This product must be disposed of at a designated waste electronic equipment collecting point.

11.5 References

- [1] THE CIP NETWORKS LIBRARY, Volume 6, CompoNet Adaptation of CIP, Edition 1.4 November 2008
- [2] Data sheet MOD JACK MJIM: https://www.erni-x-press.com/de/downloads/zeichnungen/203313.pdf
- [3] Design Specification for VARAN Rev. 0.76, section 5.1.4 VARAN Splitter

Ref	References Protocol API Manuals		
•	CANopen Master Protocol API Manual, Revision 14, Hilscher GmbH 2013		
•	CANopen Slave Protocol API Manual (V3), Revision 5, Hilscher GmbH 2013		
•	DeviceNet Master Protocol API Manual, Revision 10, Hilscher GmbH 2013		
•	DeviceNet Slave Protocol API Manual, Revision 13, Hilscher GmbH 2013		
•	EtherCAT Master Protocol API Manual (V3), Revision 5, Hilscher GmbH 2013		
•	EtherCAT Slave Protocol API Manual, Revision 3 (V4), Hilscher GmbH 2013		
•	EtherCAT Slave Protocol API Manual, Revision 21 (V2), Hilscher GmbH 2013		
•	EtherNetIP Scanner Protocol API Manual, Revision 13, Hilscher GmbH 2013		
•	EtherNetIP Adapter Protocol API Manual, Revision 12, Hilscher GmbH 2013		
•	Open Modbus/TCP Protocol API Manual, Revision 8, Hilscher GmbH 2013		
•	POWERLINK Controlled Node/Slave Protocol API Manual, Revision 12, Hilscher GmbH 2013		
•	PROFIBUS DP Master Protocol API Manual, Revision 18, Hilscher GmbH 2013		
•	PROFIBUS DP Slave Protocol API Manual, Revision 15, Hilscher GmbH 2013		
•	PROFIBUS MPI Protocol API Manual, Revision 4, Hilscher GmbH 2011		
•	PROFINET IO-Controller Protocol API Manual, Revision 18, Hilscher GmbH 2013		
•	PROFINET IO-Device Protocol API Manual (V3.4), Revision 13, Hilscher GmbH 2013		
•	PROFINET IO-Device Protocol API Manual (V3.5), Revision 6, Hilscher GmbH 2013		
•	sercos Master Protocol API Manual, Revision 11, Hilscher GmbH 2013		
•	sercos Slave Protocol API Manual (V3), Revision 12, Hilscher GmbH 2013		
•	VARAN Client Protocol API Manual, Revision 3, Hilscher GmbH 2013		

Table 138: References Protocol API Manuals

References referring to the safety issues are listed separately in section *References Safety* on page 26.

References referring to the Standard Bus Specifications for Compact PCI, Mini PCI, Mini PCI Express and PCI-104 are listed separately *References PCI Specifications* on page 109.

11.6 List of Figures

Figure 1: System Overview cifX to update Firmware, Driver and Software	46
Figure 2: CIFX 80-RE*	47
Figure 3: Front Plate CIFX 80-RE	47
Figure 4: CIFX 80-DP	48
Figure 5: Front Plate CIFX 80-RE	48
Figure 6: CIFX 80-CO	49
Figure 7: Front Plate CIFX 80-CO	49
Figure 8: CIFX 80-DN	50
Figure 9: Front Plate CIFX 80-DN	50
Figure 10: Basic Card CIFX 90 for CIFX 90-RE\F*	51
Figure 11: Basic Card CIFX 90E (for CIFX 90E-RE\F) respectively equally looking basic Card CIFX 90E\E (for CIFX 90E-RE\ET\F*)	ET 51
Figure 12: Basic Card CIFX 90E\MR for (CIFX 90E-RE\MR\F) respectively equally looking basic Card CIFX 90E\MR\ET (for CIFX 90E-RE\MR\ET\F*)	51
Figure 13: Basic Card CIFX 90 for CIFX 90-DP\F, CIFX 90-CO\F, CIFX 90-DN\F	52
Figure 14: Basic Card CIFX 90E (for CIFX 90E-FB\F) respectively equally looking basic Card CIFX 90E\E (for CIFX 90E-FB\ET\F)	T 52
Figure 15: Basic Card CIFX 90E\MR for (CIFX 90E-FB\MR\F) respectively equally looking basic Card	
CIFX 90E\MR\ET (for CIFX 90E-FB\MR\ET\F)	52
Figure 16: CIFX 104C-RE*	53
Figure 17: CIFX 104C-RE-R*	53
Figure 18: Basic Card for CIFX 104C-RE\F	54
Figure 19: Basic Card for CIFX 104C-RE-R\F	54
Figure 20: CIFX 104C-DP	55
Figure 21: CIFX 104C-DP-R	55
Figure 22: CIFX 104C-CO	56
Figure 23: CIFX 104C-CO-R	56
Figure 24: CIFX 104C-DN	57
Figure 25: CIFX 104C-DN-R	57
Figure 26: Basic Card CIFX 104C-FB\F for CIFX 104C-DP\F, CIFX 104C-CO\F, CIFX 104C-DN\F	58
Figure 27: Basic Card CIFX 104C-FB-R\F for CIFX 104C-DP-R\F, CIFX 104C-CO-R\F, CIFX 104C-DN-R	
	58
Figure 28: Ethernet Assembly Interface (AIFX-RE)*	59
Figure 29: Front Side Ethernet Assembly Interface (AIFX-RE)	59
Figure 30: Dimensioning and LED Display Ethernet Assembly Interface (AIFX-RE)	59
Figure 31: PROFIBUS Assembly Interface (AIFX-DP)	60
Figure 32: Dimensioning and LED Display PROFIBUS Assembly Interface (AIFX-DP)	60
Figure 33: CANopen Assembly Interface (AIFX-CO)	61
Figure 34: Dimensioning and LED Display CANopen Assembly Interface (AIFX-CO)	61
Figure 35: DeviceNet Assembly Interface (AIFX-DN)	62
Figure 36: Front Side DeviceNet Assembly Interface (AIFX-DN)	62
Figure 37: Dimensioning and LED Display DeviceNet Assembly Interface (AIFX-DN)	62
Figure 38: Diagnostic Assembly Interface (AIFX-DIAG)	63
Figure 39: Front Side Diagnostic Assembly Interface (AIFX-DIAG)	63
Figure 40: Dimensioning and LED Display Diagnostic Assembly Interface (AIFX-DIAG)	63
Figure 41: Front Plate Stickers for CIFX 80-RE	66
Figure 42: Mounting the Basic Card CIFX 90E into the PCI Express Mini System Connector	69
Figure 43: Connecting the Ethernet Assembly Interface (AIFX-RE) to the basic Card CIFX 90E (Example)	
Figure 44: Connecting the CANopen Assembly Interface (AIFX-CO) to the basic Card CIFX 90E (Example	e) 70
Figure 45: Demounting the Basic Card CIFX 90E from the PCI Express Mini System Connector (Example Basic Card CIFX 90E)	e: 72

Annex	180/191
Figure 46: Connecting the Ethernet Assembly Interface (AIFX-RE) to the Basic Card CIFX 104C-RE (Example)	F 74
Figure 47: Connecting the CANopen Assembly Interface (AIFX-CO) to the Basic Card 104C-FB-R\F (Example)	74
Figure 48: Connecting the Diagnostic Assembly Interface (AIFX-DIAG) to the Basic Card CIFX 104C (Example)	-FB-R∖F 75
Figure 49: Ethernet Pin Assignment at the RJ45 Socket for cifX or AIFX	100
Figure 50: PROFIBUS Interface (DSub female connector, 9 pin), X400	102
Figure 51: CANopen Interface (DSub male connector, 9 pin), X400	102
Figure 52: DeviceNet Interface (CombiCon male Connector, 5 pin), X360	103
Figure 53: Mini-B USB Connector (5 Pin)	103
Figure 54: 1x20 Pins for CIFX 104C-RE\F, CIFX 104C-RE-R\F, CIFX 90-RE\F or CIFX 90-RE\ET\F	105
Figure 55: 2x20 Pins for CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F or CIFX 90E-RE\M	//R∖ET∖F 105
Figure 56: Matrix Label	173

11.7 List of Tables

Table 1: List of Revisions	9
Table 2: Reference on Hardware PC Cards cifX, AIFX Assembly Interfaces	11
Table 3: Reference on Firmware	12
Table 4: Reference on Driver and Software	12
Table 5: PROFINET IO-Device Firmware Version 3.4 and 3.5, Header, GSDML and Protocol API Manual	15
Table 6: EtherCAT-Slave Firmware Version 2.5 and 4.2, Header, XML and Protocol API Manual	16
Table 7: Device Description Files for PC Cards cifX	17
Table 8: PC Cards cifX and the Real-Time Ethernet or Fieldbus Systems realizable	22
Table 9: PC Cards cifX with AIFX Assembly Interface	23
Table 10: Safety Symbols and Sort of Warning or Principle	26
Table 11: Signal Words	26
Table 12: PC Cards Compact PCI CIFX 80-XX	28
Table 13: PC Cards PCI-104: CIFX 104C-XX and CIFX 104C-XX-R	28
Table 14: PC Cards Mini PCI CIFX 90-XX\	29
Table 15: PC Cards Mini PCI Express CIFX 90E-XX\F	30
Table 16: PC Cards Mini PCI Express CIFX 90E-XX\ET\F	30
Table 17: PC Cards Mini PCI Express CIFX 90E-XX\MR\F	31
Table 18: PC Cards Mini PCI Express CIFX 90E-XX\MR\ET\F	32
Table 19: PC Cards PCI-104: CIFX 104C-XX\F and CIFX 104C-XX-R\F	33
Table 20: AIFX Assembly Interfaces for PC Cards cifX with Cable Connector	33
Table 21: Firmware Versions for the DMA Mode	34
Table 22: Versions Driver and SYCON.net for the DMA Mode	34
Table 23: Slot for the PC Cards cifX Compact PCI, Mini PCI, Mini PCIe, PCI-104	35
Table 24: Notes on the Card Height - PC Card cifX Mini PCI and Mini PCI Express	36
Table 25: Panel Cutout at the at the Housing Panel of the PC or at the front plate of the PC cabinet	37
Table 26: Required Panel Cutout and Holes for AIFX	37
Table 27: Requirements Power Supply and Host Interface for PC Cards cifX Compact PCI, Mini PCI, Min PCI Express and PCI-104	i 38
Table 28: Requirements to operate PC Cards cifX properly	39
Table 29: Steps for the Software and Hardware Installation, the Configuration and for the Diagnosis of a I	ъС
Card cifX Compact PCI, Mini PCI, Mini PCI Express and PCI-104 (Master and Slave)	43
Table 30: Notes for the Configuration of the Master Device	44
Table 31: Device Names in SYCON.net by Communication Protocol	45
Table 32: LED Labeling depending of the loaded Firmware	66

Annex	181/191
Table 33: Overview LEDs Real-Time Ethernet Systems	78
Table 34: Overview LEDs by Fieldbus System	79
Table 35: System Status LED	79
Table 36: Power On LED	79
Table 37: LEDs EtherCAT Master	80
Table 38: LED State Definition for EtherCAT Master for the RUN and ERR LEDs	80
Table 39: LEDs EtherCAT Slave	81
Table 40: LED State Definition for EtherCAT Slave for the RUN and ERR LEDs	81
Table 41: LEDs EtherNet/IP Scanner (Master)	82
Table 42: LEDs EtherNet/IP Adapter (Slave)	83
Table 43: LEDs Open Modbus/TCP	84
Table 44: LEDs POWERLINK Controlled Node Controlled Node/Slave	85
Table 45: LED State Definition for POWERLINK Controlled Node Controlled Node/Slave for the BS	
Table 46: LEDs PROFINET IO-Controller	85 86
Table 47: LEDs PROFINET IO-Device	87
Table 48: LEDs sercos Master	88
Table 49: LED State Definition for sercos Master for the STA and ERR LEDs	88
Table 50: LED state Definition for sercos Master for the STA and ERA LEDs	89
Table 50: LED's sercos Slave Table 51: LED State Definition for sercos Slave for the S3 LED	90
Table 51: LED State Deminion for serces Slave for the 33 LED	90 90
Table 52: LED'S VARAN Client Table 53: LED State Definition for VARAN Client for the RUN and ERR LED's	90 90
Table 54: LEDs PROFIBUS DP Master – 1 Communication Status LED (current Hardware Revisio	
Table 55: LEDs PROFIBUS DP Master – 2 Communication Status LEDs (AIFX-DP connected or pr Hardware Revision)	91
Table 56: LEDs PROFIBUS DP Slave – 1 Communication Status LED (current Hardware Revision)	
Table 57: LEDs PROFIBUS DP Slave - 2 Communication Status LEDs (AIFX-DP connected or price	or
Hardware Revision)	92
Table 58: LEDs PROFIBUS MPI – 1 Communication Status LED	93
Table 59: LEDs PROFIBUS MPI – 2 Communication Status LEDs (AIFX-DP connected)	93
Table 60: LEDs CANopen Master – 1 Communication Status LED (current Hardware Revision)	94
Table 61: LED State Definition for CANopen Master for the CAN LED	94
Table 62: LEDs CANopen Master – 2 Communication Status LEDs (AIFX-CO connected or prior H Revision)	lardware 95
Table 63: LED State Definition for CANopen Master for the CAN or RUN/ERR LEDs	95
Table 64: LEDs CANopen Slave – 1 Communication Status LED (current Hardware Revision)	96
Table 65: LED State Definition for CANopen Slave for the CAN LED	96
Table 66: LEDs CANopen Slave – 2 Communication Status LEDs (AIFX-CO connected or prior Ha	
Revision)	97
Table 67: LED State Definition for CANopen Slave for the CAN or RUN/ERR LEDs	97
Table 68: LEDs DeviceNet Master	98
Table 69: LED State Definition for DeviceNet Master for the MNS LED	98
Table 70: LEDs DeviceNet Slave	99
Table 71: LED State Definition for DeviceNet Slave for the MNS LED	99
Table 72: Ethernet Pin Assignment at the RJ45 Socket for cifX or AIFX	100
Table 73: Ethernet Connection Data	101
Table 74: Use of Hubs and Switches	101
Table 75: PROFIBUS Interface, X400	102
Table 76: CANopen Interface, X400	102
Table 77: DeviceNet Interface, X360	103
Table 78: Pin Assignment Mini-B USB Connector	103
Table 79: Rotary Switch for PCI-104 Slot Number, S1	104
Table 80: Pin Assignment for Cable Connector Ethernet X4 or X304	105
Table 81: Pin Assignment for Cable connector Fieldbus X3, X304 or X4	106

Annex	182/191
Table 82: Pin Assignment for Cable connector DIAG X3 or X303	106
Table 83: Pin Assignment for SYNC Connector, X51	107
Table 84: SYNC Connector: SYNC Signal, Connector, Max. Cable Length	107
Table 85: Meaning of the SYNC Signals for each Protocol	107
Table 86: Pin Assignment at the PCI Bus	108
Table 87: References PCI Specifications	109
Table 88: Pin Assignment for Mini PCI Bus, X1	110
Table 89: Pin Assignment Mini PCI Express Bus / SYNC Connector, X1/X2	111
Table 90: Technical Data CIFX 80-RE	115
Table 91: Technical Data CIFX 80-DP	116
Table 92: Technical Data CIFX 80-CO	117
Table 93: Technical Data CIFX 80-DN	119
Table 94: Technical Data CIFX 90-RE\F	121
Table 95: Technical Data CIFX 90-DP\F	121
Table 96: Technical Data CIFX 90-CO\F	122
Table 97: Technical Data CIFX 90-DN\F	124
Table 98: Technical Data CIFX 90E-RE\F, CIFX 90E-RE\ET\F, CIFX 90E-RE\MR\F, CIFX 90E-RE\M	
	127
Table 99: Technical Data CIFX 90E-DP\F, CIFX 90E-DP\ET\F, CIFX 90E-DP\MR\F, CIFX 90E-DP\M	
	129
Table 100: Technical Data CIFX 90E-CO\F, CIFX 90E-CO\ET\F, CIFX 90E-CO\MR\F, CIFX 90E-	
CO\MR\ET\F	132
Table 101: Technical Data CIFX 90E-DN\F, CIFX 90E-DN\ET\F, CIFX 90E-DN\MR\F, CIFX 90E-	
DN\MR\ET\F	134
Table 102: Technical Data CIFX 104C-RE, CIFX 104C-RE-R	137
Table 103: Technical Data CIFX 104C-RE\F, CIFX 104C-RE-R\F	139
Table 104: Technical Data CIFX 104C-DP, CIFX 104C-DP-R	140
Table 105: Technical Data CIFX 104C-DP\F, CIFX 104C-DP-R\F	142
Table 106: Technical Data CIFX 104C-CO, CIFX 104C-CO-R	143
Table 107: Technical Data CIFX 104C-CO\F, CIFX 104C-CO-R\F	145
Table 108: Technical Data CIFX 104C-DN, CIFX 104C-DN-R	147
Table 109: Technical Data CIFX104C-DN\F, CIFX 104C-DN-R\F	148
Table 110: Technical Data AIFX-RE	149
Table 111: Technical Data AIFX-DP	150
Table 112: Technical Data AIFX-CO	151
Table 113: Technical Data AIFX-DN	152
Table 114: Technical Data AIFX-DIAG	153
Table 115: PCI IDs PC Cards cifX on the PCI Bus	154
Table 116: Supported / not supported PCI Bus Commands	154
Table 117: Technical Data EtherCAT Master Protocol	155
Table 118: Technical Data EtherCAT Slave Protocol	156
Table 119: Technical Data EtherNet/IP Scanner (Master) Protocol	157
Table 120: Technical Data EtherNet/IP Adapter (Slave) Protocol	158
Table 121: Technical Data Open Modbus/TCP Protocol	159
Table 122: Technical Data POWERLINK Controlled Node (Slave) Protocol	159
Table 123: Technical Data PROFINET IO RT Controller	161
Table 124: Technical Data PROFINET IO RT IRT Device Protocol	161
Table 125: Technical Data PROFINET IO RT IRT Device Protocol	162
Table 126: Technical Data sercos Master Protocol	163
Table 127: Technical Data sercos Slave Protocol	164
Table 128: Technical Data VARAN Client Protocol	165
Table 129: Technical Data PROFIBUS DP Master Protocol	166
Table 130: Technical Data PROFIBUS DP Slave Protocol	160
Table 130: Technical Data PROFIBUS-MPI Protocol	168
	.00

Annex	183/191
Table 132: Technical Data CANopen Master Protocol	169
Table 133: Technical Data CANopen Slave Protocol	170
Table 134: Technical Data DeviceNet Master Protocol	171
Table 135: Technical Data DeviceNet Slave Protocol	172
Table 136: Pin Assignment Mini PCI Express Bus / SYNC Connector, X1/X2	176
Table 137: Assignment of the Pin 24 for HW-Rev. 6,7,8 + B or 1 to 5 and A	177
Table 138: References Protocol API Manuals	178

11.8 Glossary

10-Base T	
	Standard for communication on Ethernet over twisted pair lines with RJ45 connectors and a <u>Baud_rate</u> of 10 MBit/s (according to the IEEE 802.3 specification).
100-Base TX	
	Standard for communication on Ethernet over unshielded twisted pair lines with RJ45 connectors and a baud rate of 100 MBit/s according to the IEEE 802. specification
AIFX	
	Assembly InterFace based on netX
Auto-Crossover	
	Auto-Crossover is a feature of an interface: An interface with Auto-Crossover capability will automatically detect and correct if the data lines have been exchanged vice versa.
Auto-Negotiation	
	Auto-Negotiation is a feature of an interface: An interface with Auto-Negotiation will automatically determine a set of correct communication parameters.
Baud rate	
	Data transmission speed of a communication channel or interface.
Boot loader	
	Program loading the firmware into the memory of a device in order to be executed.
cifX	
	Communication InterFace based on netX
cifX TCP/IP Server	
	cifX TCP Server.exe
	Program for the remote diagnostics via Ethernet.
	Name: cifX TCP/IP Server for SYCON.net
	User Interface: TCP/IP Server for cifX

-	
Coil	
	A coil is a single bit in the memory that can be accessed using Modbus: read or write access with FC 1, 5, 15. Depending on the used Modbus function code a single coil or several coils lying in succession can be accessed.
CSP	
	electronic device data sheet, required for each CC-Link device
Device Description	File
	A file containing configuration information about a device being a part of a network that can be read out by masters for system configuration. Device Description Files use various formats which depend on the communication system.
DHCP	
	Dynamic Host Configuration Protocol
	This is a protocol simplifying the configuration of IP networks by automatically assigning IP addresses.
Discrete Input	
	A "Discrete Input" (as defined in the Modbus terminology) is a single bit in the memory which can be accessed using Modbus (read with FC 2).
DP	
	Decentral Periphery
DPM	
	Dual-Port Memory
EDS	
	Electronic Data Sheet
EDS file	
	A special kind of Device Description File used for example by EtherNet/IP.
ET	
	Extended Temperature Range (Operating Temperature)
	PC cards cifX with the addition of "ET" at the end of the part name can be used in an extended operating temperature range. Details to the operating temperature range are given in the technical data to the respective card.
EtherCAT	
	A communication system for industrial Ethernet designed and developed by Beckhoff Automation GmbH.
Ethernet	
	A networking technology used both for office and industrial communication via electrical or optical connections. It has been developed and specified by the Intel, DEC and XEROX. It provides data transmission with collision

Annex

control and allows various protocols. As Ethernet is not necessarily capable for real-time application, various real-time extensions have been developed.

EtherNet/IP

A communication system for industrial Ethernet designed and developed by Rockwell. It partly uses the CIP (Common Industrial Protocol).

EtherNet/IP Scanner

A Scanner exchanges real-time I/O data with Adapters and Scanners. This type of node can respond to connection requests and can also initiate connections on its own.

EtherNet/IP Adapter

An Adapter emulates functions provided by traditional rack-adapter products. This type of node exchanges real-time I/O data with a Scanner Class product. It does not initiate connections on its own.

Ethernet POWERLINK

A communication system for industrial Ethernet designed and developed by B&R. It partly uses CANopen technologies.

FDL

Fieldbus Data Link defines the PROFIBUS communication on layer 2, identical for DP and FMS $\,$

Firmware

Software running inside a device providing the basic functionality of this device. The firmware is stored remanently in the flash memory circuit of the device. It can be updated by a firmware download.

Full duplex

Full duplex denominates a telecommunication system between two communication partners which allows simultaneous communication in both directions is called a full-duplex telecommunication system. At such a system, it will be possible to transmit data even if currently data are received. Full-duplex is the opposite of Half_duplex.

Function code

A function code (FC) is a standardized method to access, i. e. read or write on coils (Bits) or registers via Modbus.

Modbus function codes are elements of Modbus request/reply telegrams.

GSD

Generic Station Description, Device description file

GSD file

A special kind of Device Description File used by PROFIBUS (GSD = Generic Station Description).

Annex	186/191
GSDML	
	Generic Station Description Markup Language
	XML based device description file.
GSDML file	
	A special kind of XML-based Device Description File used by PROFINET.
Half duplex	
-	Half duplex denominates a telecommunication system between two communication partners which does not allow simultaneous, but alternating, communication in both directions is called a half-duplex telecommunication system. At such a system, receiving data inhibits the transmission of data. Half-duplex is the opposite of _Full_duplex.
Hub	
	A network component connecting multiple communication partners with each other. A hub does not provide own intelligence, thus it does not analyze the data traffic and sends received data to all connected communication partners. A hub can be used for setting up a star topology.
Industrial Ethernet	
	See Real-Time Ethernet
IP	
	Internet Protocol.
	IP belongs to the TCP/IP family of protocols and is defined in RFC791. It is based on layer 3 of the ISO/OSI 7 layer model of networking.
	It is a connectionless protocol, i.e. you do not need to open a connection to a computer before sending an IP data packet to it. Therefore IP is not able to guarantee that the IP data packets really arrive at the recipient. On IP level neither the correctness of data nor the consistence and completeness are checked.
	IP defines special addressing mechanisms, see IP Address.
IP Address	
	Address within IP (the Internet Protocol, part of TCP/IP).
	An IP address is an address identifying a device or a computer within a network using the IP protocol. IP addresses are defined as a 32 bit number. Usually, for ease of notation the IP address is divided into four 8 bit numbers which are represented in decimal notation and separated by points:
	a.b.c.d
	where a.b.c.d are each integer values between 0 and 255.
	Example: 192.168.30.15
	However, not all combinations are allowed, some are reserved for special purposes.
	The IP address 0.0.0.0 is defined as invalid.

MAC-ID

MAC = Media Access Control

Definition for Ethernet:

A MAC-ID is on delivery a unique (physical) Ethernet address of the device.

MAC-IDs are defined as a 48 bit number. Usually, for ease of notation the IP address is divided into six 8 bit numbers which are represented in hexadecimal notation and separated by "minus"-signs (-):

A-B-C-D-E-F

where A-B-C-D-E-F are each integer values between 0 and 255.

Example: 00-02-A2-20-91-18

Definition for DeviceNet:

The network address of a device serves to distinguish itself on a DeviceNet fieldbus system from any other device or Slave on this network. This should be a unique number for each device. A valid MAC-ID address is within a range of 0 to 63 and can be re-entered and changed in the MAC-ID box in the Device Configuration Dialog.

Modbus Data Model

The data model distinguishes four basic types of data areas:

- Discrete Inputs (inputs) = FC 2 (Read)
- coils (outputs) = FC 1, 5, 15 (Write and Read back)
- Input register (input data) = FC 4 (Read)
- Holding register (output data) = FC 3, 6, 16, 23 (Write and Read back).

It should be noted, however, that depending on the device manufacturer and device type:

• the data area in the device may be present or not,

• and two data areas can be combined into one data region. For example, discrete inputs and input registers can be a common data area, which can be accessed with read-FC 2 and FC 4.

• Further FC 1 and FC 3 are used instead of reading back the inputs to read the outputs.

MPI

Multi Point Interface

The MPI is a proprietary interface of the SIMATIC[®] S7[®] series of PLCs. It is compatible to PROFIBUS and based on RS-485. It usually works with a transmission rate of 187.5 kBaud.

netX

networX on chip, Hilscher network communication controllers

netX Configuration Tool

The netX Configuration Tool allows users to operate cifX or netX based devices in different networks. Its graphical user interface serves as a configuration tool for the installation, configuration and diagnosis of the devices.

Object Dictionary		
	An object dictionary is a storage area for device parameter data structures. It is accessed in standardized manner.	
Open Modbus/TCP		
	A communication system for Industrial Ethernet designed and developed by Schneider Automation and maintained by the Modbus-IDA organization based on the Modbus protocols for serial communication.	
РСВ		
	Printed Circuit Board, (printed = machine	e-made) circuit board
PCle		
	Abbreviation for PCI Express	
PC Card cifX		
	Communication Interfaces of the cifX basis of the communication controller ne	
	for the Real-Time Ethernet systems	and for the fieldbus systems
	 EtherCAT EtherNet/IP Open Modbus/TCP POWERLINK PROFINET IO sercos VARAN as Communication Interface netX with PCI 	 PROFIBUS DP PROFIBUS MPI CANopen DeviceNet AS-Interface CompoNet CC-Link
	 PCI (CIFX50), PCI Express (CIFX 50E), Low Profile PCI Express (CIFX 70E, CIFX 100I Compact PCI (CIFX 80), Mini PCI (CIFX 90), Mini PCI Express (CIFX 90E), PCI-104 (CIFX 104C) 	
	 and as Communication Interface netX with PCI-104 (CIFX 104). *only Real-Time Ethernet 	ith ISA Bus
PROFINET		
	A communication system for Industrial Ethernet designed and developed by PROFIBUS International. It uses some mechanisms similar to those of the PROFIBUS field bus.	

PROFINET IO Controller

A PROFINET control unit responsible for the defined run-up of an I/O subsystem and the cyclic or acyclic data exchange.

PROFINET IO Device

A PROFINET field device that cyclically receives output data from its IO-Controller and responds with its input data.

RE

RE stands for Real-Time Ethernet

Real-Time Ethernet

Real-Time Ethernet (Industrial Ethernet) is an extension of the Ethernet networking technology for industrial purposes with very good real-time features and performance. There is a variety of different Real-Time Ethernet systems on the market which are incompatible with each other. The most important systems of these are

- EtherCAT
- EtherNet/IP
- Ethernet POWERLINK
- Open Modbus/TCP
- PROFINET
- sercos
- VARAN

Register

A register is a 16-bit wide storage area for data which can be accessed and addressed as a unit by some of the Modbus Function Codes.

Depending on the used Modbus function code a single register or multiple registers sequentially located can be accessed.

Modbus differs Input Registers (FC 4) and Holding Registers (FC 3, 6, 16, 23).

Remanent

Remanent memory holds its data even after power-off, for instance flash memory is remanent. It is also called non-volatile memory.

RJ45

A connector type often used for Ethernet connection. It has been standardized by the Federal Communications Commission of the USA (FCC).

sercos

A communication system for industrial Ethernet designed and developed by Bosch-Rexroth and supported by sercos International.

Switch

A network component connecting multiple communication partners (or even entire branches of a network) with each other. A switch is an intelligent network component which analyzes network traffic in order to decide on its own. For the connected communication partners a switch behaves transparently.

Annex	190/191
SYNC	
	Synchronization cycle of the master
TCP/IP	
	Transport Control Protocol/Internet Protocol connection-orientated, secure transfer protocol as basis for the Internet-protocols
UCMM	
	Unconnected Message Manager
VARAN	
	Versatile Automation Random Access Network
	A communication system for industrial Ethernet based on the DIAS-BUS developed by Sigmatek. The system is supported by the VARAN-BUS-NUTZERORGANISATION (VNO).
Watchdog Timer	
	A watchdog timer provides an internal supervision mechanism of a communication system. It supervises that an important event happens within a given timeframe (the watchdog time which can be adjusted accordingly, for instance by a parameter in the <u>Warmstart</u> message) and causes an alarm otherwise (usually this is accomplished by changing the operational state of the communication system to a more safe state).
XDD file	
	A special kind of Device Description file used by Ethernet POWERLINK.
XML	
	XML means Extended Markup Language. It is a symbolic language for structuring data systematically. XML is standard maintained by the W3C (World-wide web consortium). Device Description Files often use XML-based formats for storing the device-related data appropriately.

11.9 Contacts

Headquarters

Germany

Hilscher Gesellschaft für Systemautomation mbH Rheinstrasse 15 65795 Hattersheim Phone: +49 (0) 6190 9907-0 Fax: +49 (0) 6190 9907-50 E-Mail: info@hilscher.com

Support Phone: +49 (0) 6190 9907-99 E-Mail: de.support@hilscher.com

Subsidiaries

China

Hilscher Systemautomation (Shanghai) Co. Ltd. 200010 Shanghai Phone: +86 (0) 21-6355-5161 E-Mail: <u>info@hilscher.cn</u>

Support

Phone: +86 (0) 21-6355-5161 E-Mail: <u>cn.support@hilscher.com</u>

France

Hilscher France S.a.r.l. 69500 Bron Phone: +33 (0) 4 72 37 98 40 E-Mail: info@hilscher.fr

Support

Phone: +33 (0) 4 72 37 98 40 E-Mail: <u>fr.support@hilscher.com</u>

India

Hilscher India Pvt. Ltd. New Delhi - 110 065 Phone: +91 11 26915430 E-Mail: info@hilscher.in

Italy

Hilscher Italia S.r.l. 20090 Vimodrone (MI) Phone: +39 02 25007068 E-Mail: <u>info@hilscher.it</u>

Support Phone: +39 02 25007068 E-Mail: <u>it.support@hilscher.com</u>

Japan

Hilscher Japan KK Tokyo, 160-0022 Phone: +81 (0) 3-5362-0521 E-Mail: info@hilscher.jp

Support

Phone: +81 (0) 3-5362-0521 E-Mail: jp.support@hilscher.com

Korea

Hilscher Korea Inc. Seongnam, Gyeonggi, 463-400 Phone: +82 (0) 31-789-3715 E-Mail: info@hilscher.kr

Switzerland

Hilscher Swiss GmbH 4500 Solothurn Phone: +41 (0) 32 623 6633 E-Mail: info@hilscher.ch

Support Phone: +49 (0) 6190 9907-99

E-Mail: <u>ch.support@hilscher.com</u>

USA

Hilscher North America, Inc. Lisle, IL 60532 Phone: +1 630-505-5301 E-Mail: info@hilscher.us

Support

Phone: +1 630-505-5301 E-Mail: <u>us.support@hilscher.com</u>